

CentralTM Semiconductor Corp.

145 Adams Avenue, Hauppauge, NY 11788 USA
Tel: (631) 435-1110 • Fax: (631) 435-1824

Manufacturers of World Class Discrete Semiconductors

CQ202-4B
CQ202-4D
CQ202-4M
CQ202-4N

4.0 AMP TRIAC
200 THRU 800 VOLTS

JEDEC TO-202 CASE

DESCRIPTION

The CENTRAL SEMICONDUCTOR CQ202-4B series type is an Epoxy Molded Silicon Triac designed for full wave AC control applications featuring gate triggering in all four (4) quadrants.

MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

	SYMBOL	CQ202 -4B	CQ202 -4D	CQ202 -4M	CQ202 -4N	UNITS
Peak Repetitive Off-State Voltage	V _{DRM}	200	400	600	800	V
RMS On-State Current (T _C = 80°C)	I _{T(RMS)}		4.0			A
Peak One Cycle Surge (t = 10ms)	I _{TSM}		40			A
I ² t Value for Fusing (t = 10ms)	I ² t		2.4			A ² s
Peak Gate Power (tp = 10μs)	P _{GM}		3.0			W
Average Gate Power Dissipation	P _{G(AV)}		0.2			W
Peak Gate Current (tp = 10μs)	I _{GM}		1.2			A
Storage Temperature	T _{stg}		-40 to +150			°C
Junction Temperature	T _J		-40 to +125			°C
Thermal Resistance	θ _{J-A}		60			°C/W
Thermal Resistance	θ _{J-C}		7.5			°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

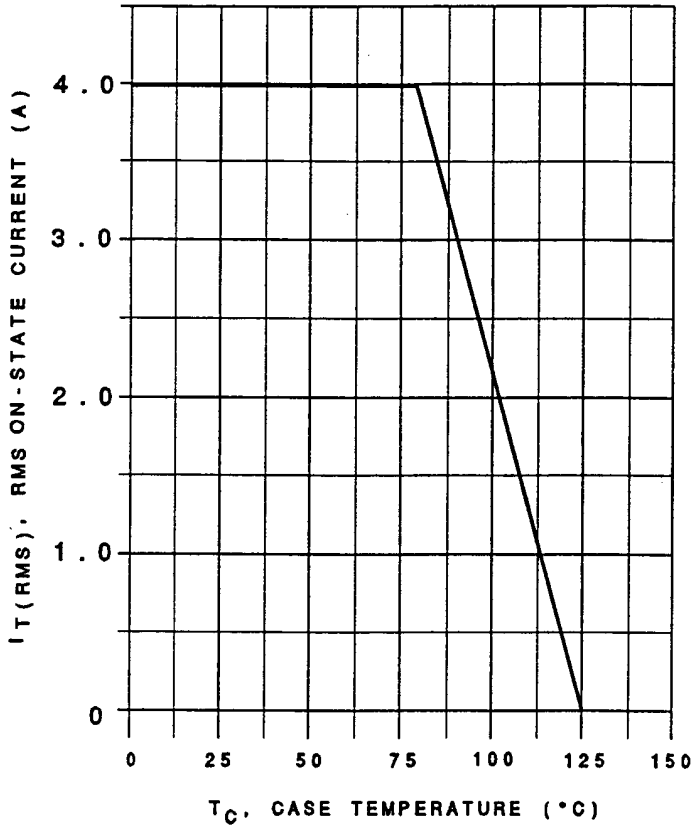
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _{DRM}	Rated V _{DRM} , R _{GK} = 1KΩ			5.00	μA
I _{DRM}	Rated V _{DRM} , R _{GK} = 1KΩ, T _C = 125°C			200	μA
I _{GT}	V _D = 12V, QUAD I,II,III,IV			25	mA
I _H	R _{GK} = 1KΩ			25	mA
V _{GT}	V _D = 12V, QUAD I,II,III,IV			2.00	V
V _{TM}	I _{TM} = 6.0A			2.10	V
dv/dt	V _D = 2/3 V _{DRM} , T _C = 125°C	100			V/μs

(OVER)

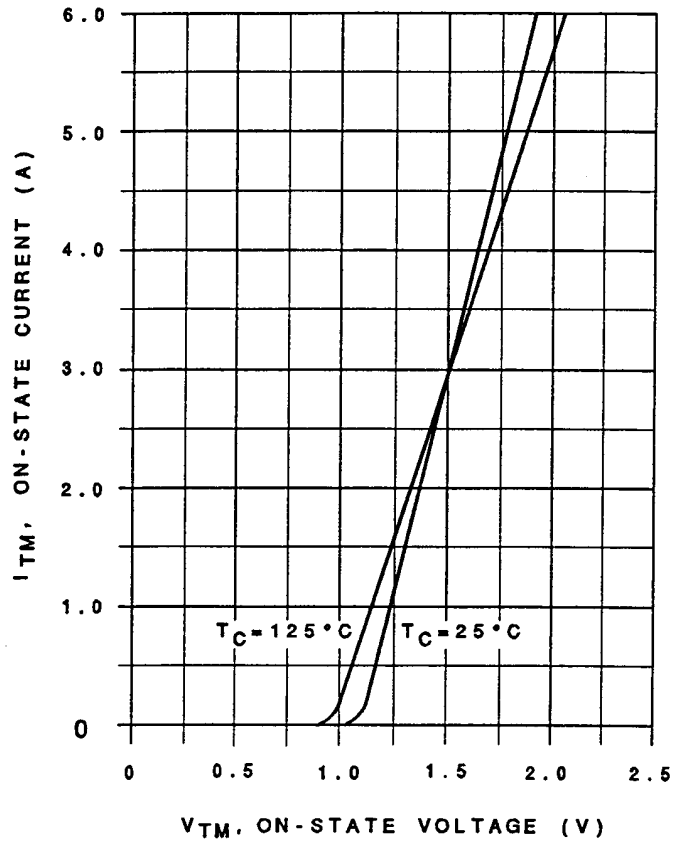
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CQ202-4B SERIES RATING AND CHARACTERISTIC CURVES

RMS ON-STATE CURRENT vs. CASE TEMPERATURE



MAXIMUM ON-STATE CHARACTERISTICS



MECHANICAL OUTLINE

ALL DIMENSIONS IN INCHES (mm).

