

**HIGH ISOLATION VOLTAGE  
AC INPUT RESPONSE TYPE  
4-PIN SOP PHOTOCOUPLER**

-NEPOC™ Series-

**DESCRIPTION**

The PS2765-1 is an optically coupled isolator containing GaAs light emitting diodes and an NPN silicon phototransistor.

This package is mounted in a plastic SOP (Small Outline Package) for high density applications.

The package has shield effect to cut off ambient light.

**FEATURES**

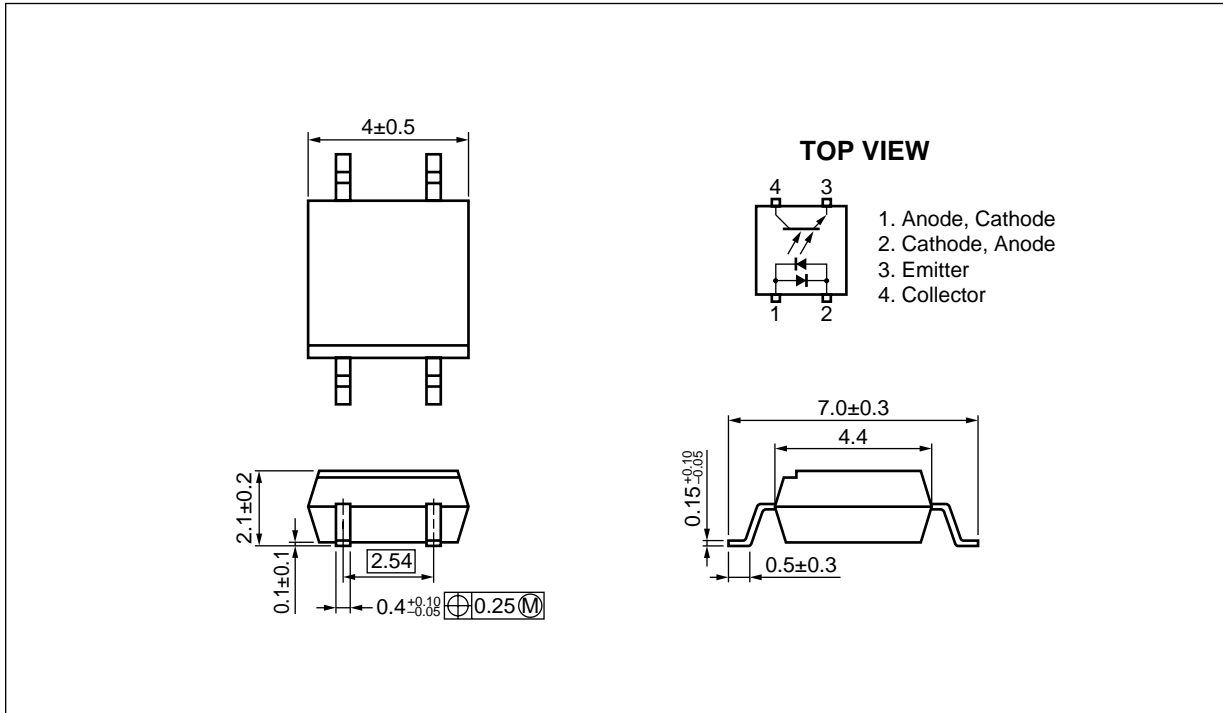
- Isolation distance (0.4 mm MIN.)
- AC input response
- SOP (Small Outline Package) type
- High isolation voltage ( $BV = 3\,750\text{ Vr.m.s.}$ )
- High-speed switching ( $t_r = 4\ \mu\text{s TYP.}$ ,  $t_f = 5\ \mu\text{s TYP.}$ )
- Ordering number of taping product: PS2765-1-F3, F4
- UL approved: File No. E72422 (S)
- BSI approved: No. 8436/8437

**APPLICATIONS**

- Hybrid IC
- Programmable logic controllers
- Power supply

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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

PACKAGE DIMENSIONS (Unit: mm)



PHOTOCOUPLER CONSTRUCTION

Parameter	Unit (MIN.)
Air Distance	5 mm
Creepage Distance	5 mm
Isolation Distance	0.4 mm

**ORDERING INFORMATION**

Part Number	Package	Packing Style	Application Part Number <sup>*1</sup>
PS2765-1	4-pin SOP	Magazine case 100 pcs	PS2765-1
PS2765-1-F3		Embossed Tape 3 500 pcs/reel	
PS2765-1-F4			

\*1 For the application of the Safety Standard, following part number should be used.

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 °C, unless otherwise specified)**

Parameter		Symbol	Ratings	Unit
Diode	Forward Current (DC)	I <sub>F</sub>	± 50	mA
	Power Dissipation	P <sub>D</sub>	80	mW
	Peak Forward Current <sup>*1</sup>	I <sub>FP</sub>	± 1.0	A
Transistor	Collector to Emitter Voltage	V <sub>CEO</sub>	40	V
	Emitter to Collector Voltage	V <sub>ECO</sub>	5	V
	Collector Current	I <sub>C</sub>	40	mA
	Power Dissipation Derating	ΔP <sub>C</sub> /°C	1.5	mW/°C
	Power Dissipation	P <sub>C</sub>	150	mW
Isolation Voltage <sup>*2</sup>		BV	3 750	Vr.m.s.
Operating Ambient Temperature		T <sub>A</sub>	-55 to +100	°C
Storage Temperature		T <sub>stg</sub>	-55 to +150	°C

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\*1 PW = 100 μs, Duty Cycle = 1 %

\*2 AC voltage for 1 minute at T<sub>A</sub> = 25 °C, RH = 60 % between input and output

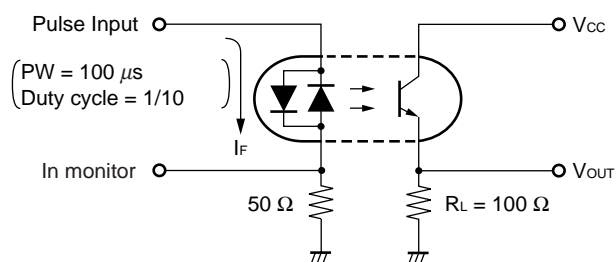
**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)**

Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Diode	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = ± 5 mA		1.1	1.4	V
	Terminal Capacitance	C <sub>t</sub>	V = 0 V, f = 1 MHz		30		pF
Transistor	Collector to Emitter Dark Current	I <sub>CEO</sub>	I <sub>F</sub> = 0 mA, V <sub>CE</sub> = 40 V			100	nA
Coupled	Current Transfer Ratio (I <sub>c</sub> /I <sub>F</sub> ) <sup>*1</sup>	CTR	I <sub>F</sub> = ± 5 mA, V <sub>CE</sub> = 5 V	50	100	400	%
	Collector Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>F</sub> = ± 10 mA, I <sub>c</sub> = 2 mA			0.3	V
	Isolation Resistance	R <sub>I-O</sub>	V <sub>I-O</sub> = 1 kV <sub>DC</sub>	10 <sup>11</sup>			Ω
	Isolation Capacitance	C <sub>I-O</sub>	V = 0 V, f = 1 MHz		0.4		pF
	Rise Time <sup>*2</sup>	t <sub>r</sub>	V <sub>CC</sub> = 5 V, I <sub>c</sub> = 2 mA, R <sub>L</sub> = 100 Ω		4		μs
Fall Time <sup>*2</sup>	t <sub>f</sub>			5			

\*1 CTR rank

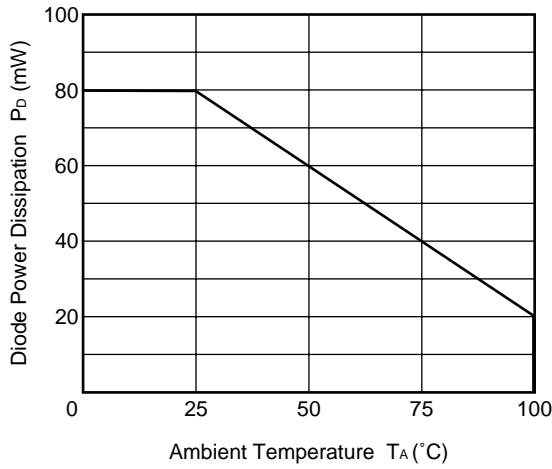
N: 50 to 400 (%)

\*2 Test circuit for switching time

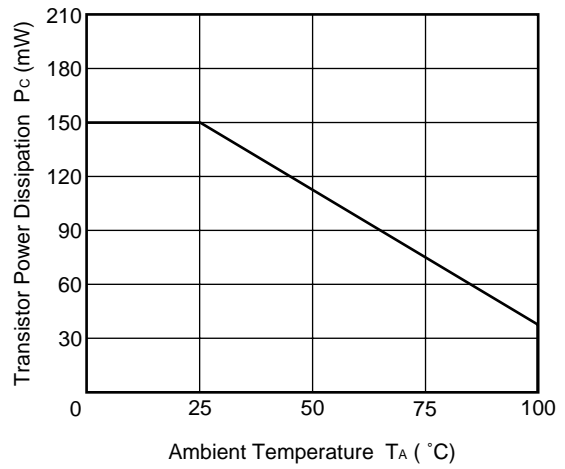


TYPICAL CHARACTERISTICS ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

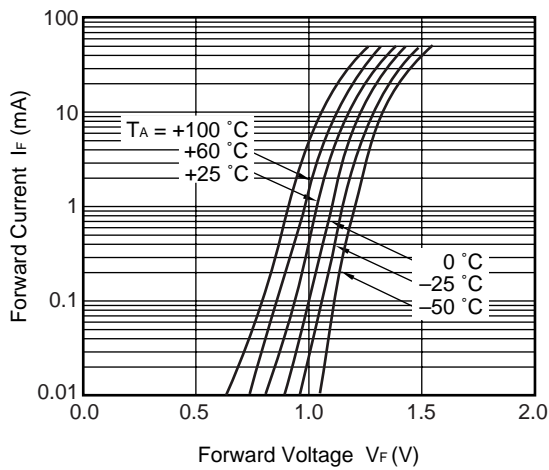
DIODE POWER DISSIPATION vs. AMBIENT TEMPERATURE



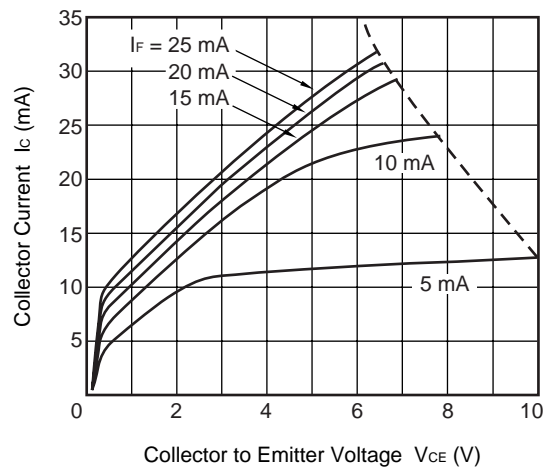
TRANSISTOR POWER DISSIPATION vs. AMBIENT TEMPERATURE



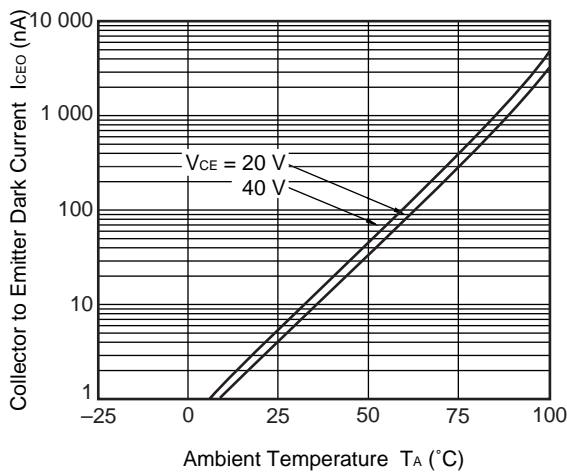
FORWARD CURRENT vs. FORWARD VOLTAGE



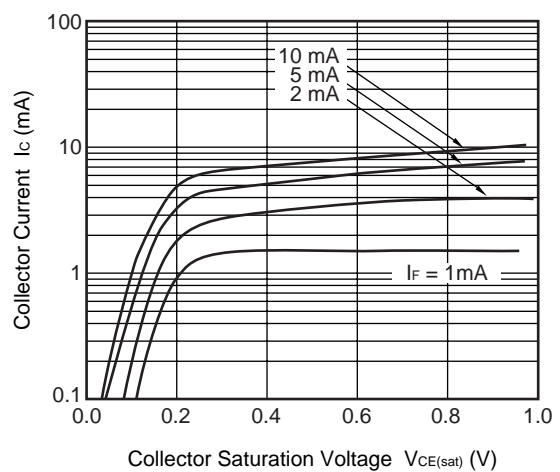
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



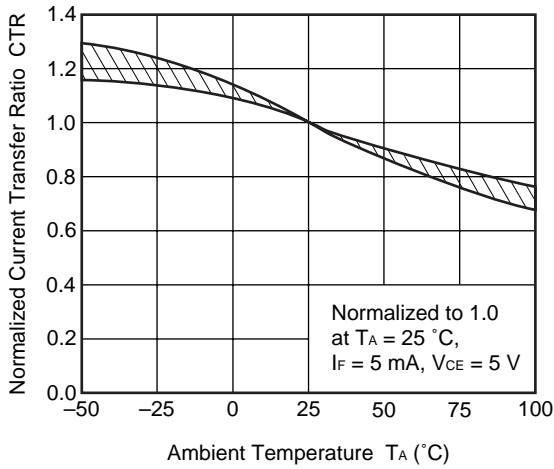
COLLECTOR TO EMITTER DARK CURRENT vs. AMBIENT TEMPERATURE



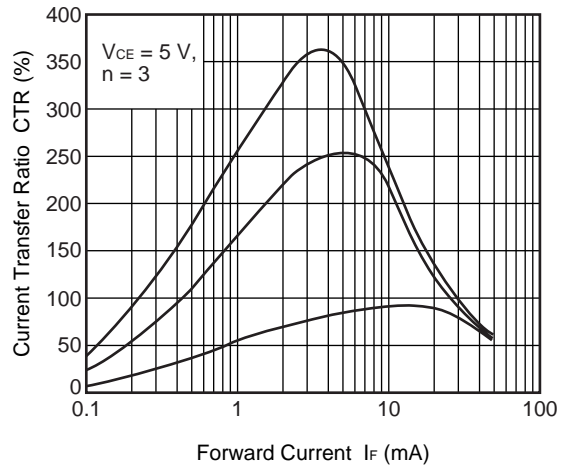
COLLECTOR CURRENT vs. COLLECTOR SATURATION VOLTAGE



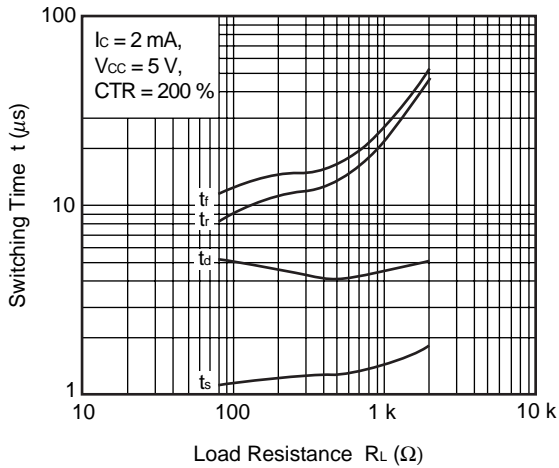
NORMALIZED CURRENT TRANSFER RATIO vs. AMBIENT TEMPERATURE



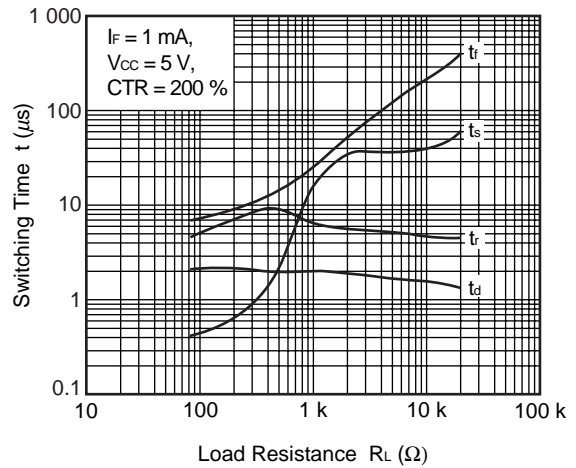
CURRENT TRANSFER RATIO vs. FORWARD CURRENT



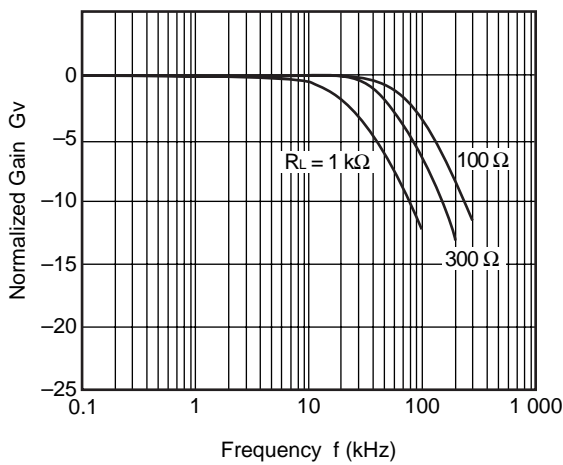
SWITCHING TIME vs. LOAD RESISTANCE



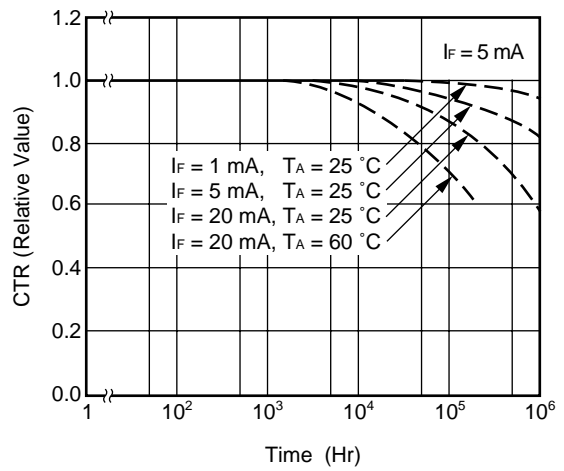
SWITCHING TIME vs. LOAD RESISTANCE



FREQUENCY RESPONSE



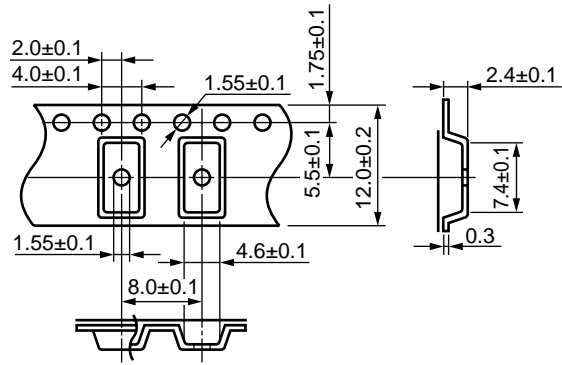
LONG TERM CTR DEGRADATION



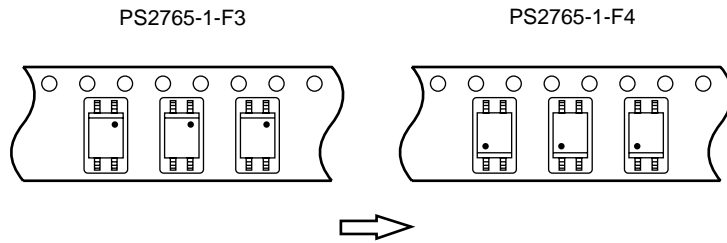
**Remark** The graphs indicate nominal characteristics.

TAPING SPECIFICATIONS (in millimeters)

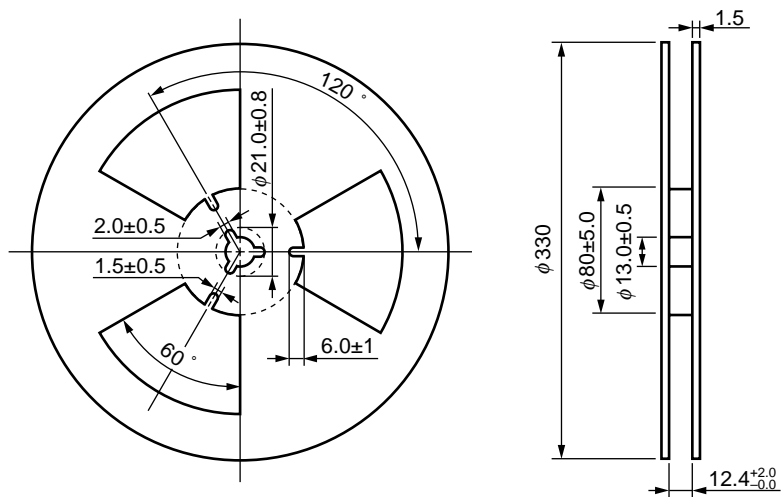
Outline and Dimensions (Tape)



Tape Direction



Outline and Dimensions (Reel)



Packing: 3 500 pcs/reel

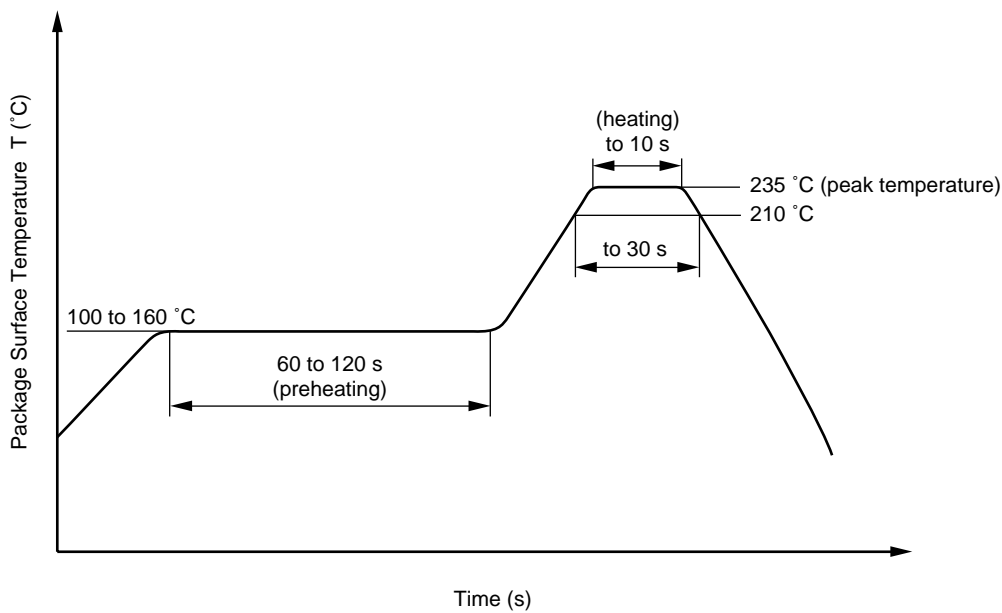
**NOTES ON HANDLING**

**1. Recommended soldering conditions**

**(1) Infrared reflow soldering**

- Peak reflow temperature 235 °C or below (package surface temperature)
- Time of temperature higher than 210 °C 30 seconds or less
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

Recommended Temperature Profile of Infrared Reflow



**(2) Dip soldering**

- Temperature 260 °C or below (molten solder temperature)
- Time 10 seconds or less
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt % is recommended.)

**(3) Cautions**

- Fluxes  
Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

**2. Cautions regarding noise**

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between corrector-emitters at startup, the output side may enter the on state, even if the voltage is within the absolute maximum ratings.



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## CAUTION

**Within this device there exists GaAs (Gallium Arsenide) material which is a harmful substance if ingested. Please do not under any circumstances break the hermetic seal.**

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