



**Data Sheet Supplement
Version 3.0 (valid for 8" product)**

Dynamic Differential Hall Effect Sensor

TLE4928C

For all parameters not specified in this document the TLE4925/25C data sheet is valid.



| Type | Marking | Ordering Code | Package |
|----------|---------|---------------|------------|
| TLE4928C | 28C | SP000269349 | PG-SSO-3-9 |

1 TLE4928C

1.1 Operating Range

| No. | Parameter | Symbol | min | typ | max | Unit | Remarks |
|-------|---------------|----------|-----|-----|-----|------|---|
| 1.1.1 | Power on time | t_{on} | | | 1 | ms | Time to achieve specified accuracy After power on the output of the IC is always in high-state. After internal resets output is locked ¹ . |

1.2 AC/DC Characteristics

Note: Not part of production testing, verified by design and characterization.

| No. | Parameter | Symbol | min | typ | max | Unit | Remarks |
|-------|--|-------------|-------|-----|-----|------|--|
| 1.2.1 | Frequency range | f | 0.006 | | 8 | kHz | Operation below 6Hz ² |
| 1.2.2 | Offset recalibration time after last output change | t_{reset} | 165 | 195 | 225 | ms | Valid for calibrated mode Output locked to state before recalibration |

¹ Output of the IC is locked in present state (high-state or low-state) after an internal reset is launched. This reset happens typically every 195ms when there is no output switching in either case. See also 1.2.2. A voltage reset causes a release of the output and output is in high state after power on again.

² output will switch if magnetic signal is changing more that $2x |\Delta B_{min}|$ within offset recalibration time even below 6Hz once per magnetic edge, increased phase error is possible

Revision History: **April 2007** Version 3.0

| | |
|-----------------------|--|
| Previous Version: 2.0 | |
| Page | Subjects (major changes since last revision) |
| 1 | Data sheet is valid for 8" products |
| 1 | Ordering code added |

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