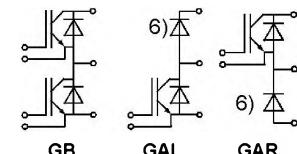


Absolute Maximum Ratings		Values		Units
Symbol	Conditions <sup>1)</sup>			
V <sub>CES</sub>		1200		V
V <sub>CCR</sub>	R <sub>GE</sub> = 20 kΩ	1200		V
I <sub>C</sub>	T <sub>case</sub> = 25/80 °C	150 / 100		A
I <sub>CM</sub>	T <sub>case</sub> = 25/80 °C; t <sub>p</sub> = 1 ms	300 / 200		A
V <sub>GES</sub>		± 20		V
P <sub>tot</sub>	per IGBT, T <sub>case</sub> = 25 °C	800		W
T <sub>j</sub> , (T <sub>stg</sub> )		- 40 ... +150 (125)		°C
V <sub>iso</sub>	AC, 1 min.	2 500 <sup>7)</sup>		V
humidity	DIN 40 040	Class F		
climate	DIN IEC 68 T.1	55/150/56		

SEMITRANS® M  
IGBT ModulesSKM 150 GB 123 D  
SKM 150 GAL 123 D <sup>6)</sup>  
SKM 150 GAR 123 D <sup>6)</sup>

## SEMITRANS 3



Characteristics				
Symbol	Conditions <sup>1)</sup>	min.	typ.	max.
V <sub>BV/ICES</sub>	V <sub>GE</sub> = 0, I <sub>C</sub> = 4 mA	≥ V <sub>CES</sub>	-	-
V <sub>GE(th)</sub>	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub> = 4 mA	4,5	5,5	6,5
I <sub>CES</sub>	V <sub>GE</sub> = 0   T <sub>j</sub> = 25 °C	-	0,2	2
	V <sub>CE</sub> = V <sub>CES</sub>   T <sub>j</sub> = 125 °C	-	9	mA
I <sub>GES</sub>	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0	-	-	1
V <sub>CESat</sub>	I <sub>C</sub> = 100 A   V <sub>GE</sub> = 15 V;	-	2,5(3,1)	3(3,7)
V <sub>CESat</sub>	I <sub>C</sub> = 150 A   T <sub>j</sub> = 25 (125) °C	-	3(3,8)	-
g <sub>f</sub>	V <sub>CE</sub> = 20 V, I <sub>C</sub> = 100 A	54	-	-
C <sub>CHC</sub>	per IGBT	-	-	700
C <sub>res</sub>	V <sub>GE</sub> = 0	-	6,5	nF
C <sub>oes</sub>	V <sub>CE</sub> = 25 V	-	1000	pF
C <sub>res</sub>	f = 1 MHz	-	500	600
L <sub>CE</sub>		-	-	20
t <sub>d(on)</sub>	V <sub>CC</sub> = 600 V	-	160	ns
t <sub>r</sub>	V <sub>GE</sub> = + 15 V; - 15 V <sup>3)</sup>	-	80	160
t <sub>d(off)</sub>	I <sub>C</sub> = 100 A, ind. load	-	400	520
t <sub>r</sub>	R <sub>Gon</sub> = R <sub>Goff</sub> = 6,8 Ω	-	70	100
E <sub>on</sub> <sup>5)</sup>	T <sub>j</sub> = 125 °C	-	13	mWs
E <sub>off</sub> <sup>5)</sup>		-	11	mWs
Inverse Diode <sup>8)</sup>				
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 100 A   V <sub>GE</sub> = 0 V;	-	2,0(1,8)	V
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 150 A   T <sub>j</sub> = 25 (125) °C	-	2,25(2,1)	V
V <sub>TO</sub>	T <sub>j</sub> = 125 °C	-	-	1,2
T <sub>T</sub>	T <sub>j</sub> = 125 °C	-	8	mΩ
I <sub>IRRM</sub>	I <sub>F</sub> = 100 A; T <sub>j</sub> = 25 (125) °C <sup>2)</sup>	-	35(50)	A
Q <sub>rr</sub>	I <sub>F</sub> = 100 A; T <sub>j</sub> = 25 (125) °C <sup>2)</sup>	-	5(14)	μC
FWD of types "GAL", "GAR" <sup>8)</sup>				
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 100 A   V <sub>GE</sub> = 0 V;	-	1,85(1,6)	V
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 150 A   T <sub>j</sub> = 25 (125) °C	-	2,0(1,8)	V
V <sub>TO</sub>	T <sub>j</sub> = 125 °C	-	-	1,2
T <sub>T</sub>	T <sub>j</sub> = 125 °C	-	5	mΩ
I <sub>IRRM</sub>	I <sub>F</sub> = 100 A; T <sub>j</sub> = 25 (125) °C <sup>2)</sup>	-	40(65)	A
Q <sub>rr</sub>	I <sub>F</sub> = 100 A; T <sub>j</sub> = 25 (125) °C <sup>2)</sup>	-	5(15)	μC
Thermal Characteristics				
R <sub>thjc</sub>	per IGBT	-	-	0,16
R <sub>thjc</sub>	per diode / FWD "GAL; GAR"	-	-	0,30/0,25
R <sub>thch</sub>	per module	-	-	0,038

## Features

- MOS input (voltage controlled)
- N channel, Homogeneous Si
- Low inductance case
- Very low tail current with low temperature dependence
- High short circuit capability, self limiting to 6 \* I<sub>cmn</sub>
- Latch-up free
- Fast & soft inverse CAL diodes <sup>8)</sup>
- Isolated copper baseplate using DCB Direct Copper Bonding Technology
- Large clearance (12 mm) and creepage distances (20 mm).

## Typical Applications: → B 6 - 63

- Switching (not for linear use)

1) T<sub>case</sub> = 25 °C, unless otherwise specified2) I<sub>F</sub> = - I<sub>C</sub>, V<sub>R</sub> = 600 V,  
- dI/dt = 1000 A/μs, V<sub>GE</sub> = 0 V3) Use V<sub>GEoff</sub> = -5 ... -15 V5) See fig. 2 + 3; R<sub>Goff</sub> = 6,8 Ω

6) The free-wheeling diodes of the GAL and GAR types have the data of the inverse diodes of SKM 200 GB 123 D

7) V<sub>iso</sub> = 4000 V<sub>rms</sub> on request

8) CAL = Controlled Axial Lifetime Technology.

Cases and mech. data → B6 - 64

SEMITRANS 3

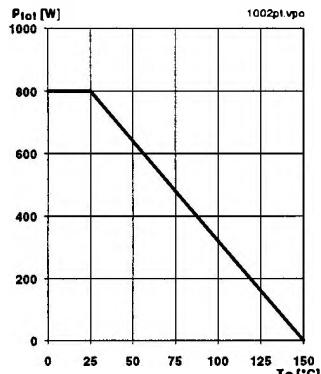
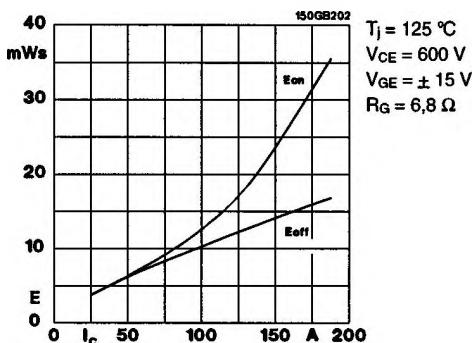
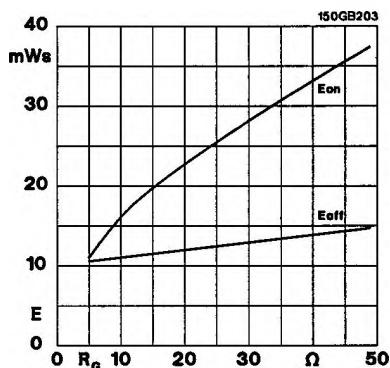
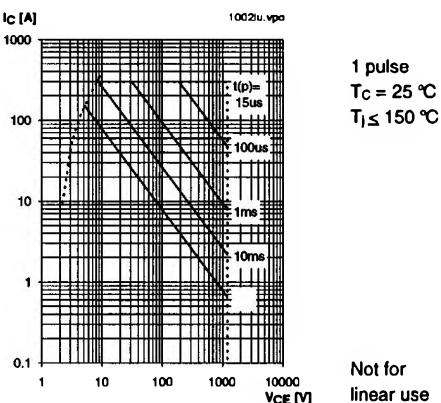
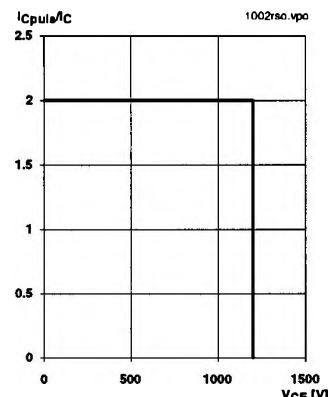
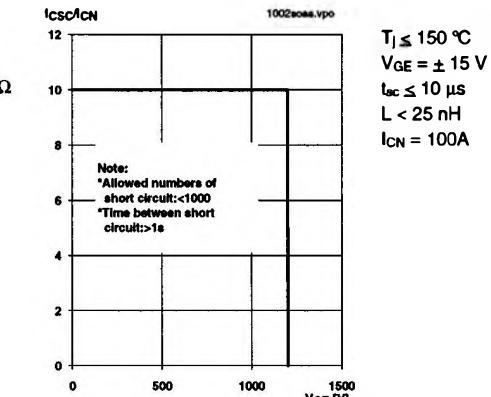
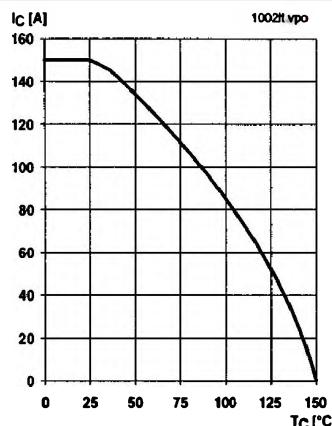
Fig. 1 Rated power dissipation  $P_{tot} = f(T_c)$ Fig. 2 Turn-on /-off energy =  $f(I_c)$ Fig. 3 Turn-on /-off energy =  $f(R_g)$ Fig. 4 Maximum safe operating area (SOA)  $I_c = f(V_{CE})$ 

Fig. 5 Turn-off safe operating area (RBSOA)

Fig. 6 Safe operating area at short circuit  $I_c = f(V_{CE})$



$T_j = 150^\circ\text{C}$   
 $V_{GE} \geq 15\text{ V}$

Fig. 8 Rated current vs. temperature  $I_c = f(T_c)$

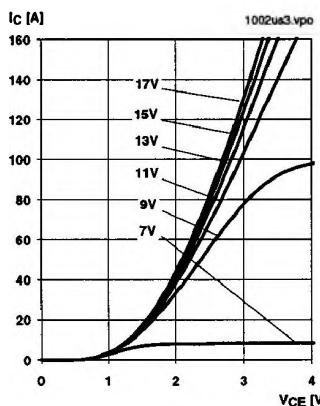


Fig. 9 Typ. output characteristic,  $t_p = 80\ \mu\text{s}; 25^\circ\text{C}$

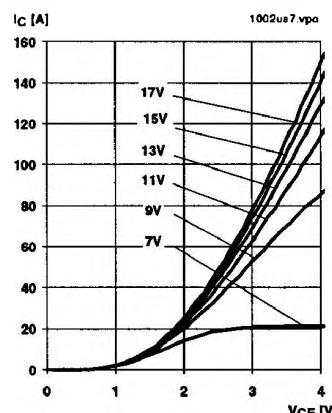


Fig. 10 Typ. output characteristic,  $t_p = 80\ \mu\text{s}; 125^\circ\text{C}$

$$P_{cond}(t) = V_{CEsat}(t) \cdot I_C(t)$$

$$V_{CEsat}(t) = V_{CE(T_0)(T)} + r_{CE(T)} \cdot I_C(t)$$

$$V_{CE(T_0)(T)} \leq 1,5 + 0,002 \cdot (T_j - 25) \text{ [V]}$$

$$r_{CE(T)} = 0,010 + 0,00004 \cdot (T_j - 25) \text{ [\Omega]}$$

valid for  $V_{GE} = + 15 \frac{+2}{-1} \text{ [V]}$ ;  $I_C > 0,3 I_{Chom}$

Fig. 11 Typ. saturation characteristic (IGBT)  
Calculation elements and equations

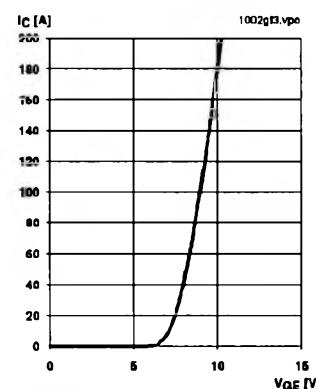


Fig. 12 Typ. transfer characteristic,  $t_p = 80\ \mu\text{s}; V_{CE} = 20\text{ V}$

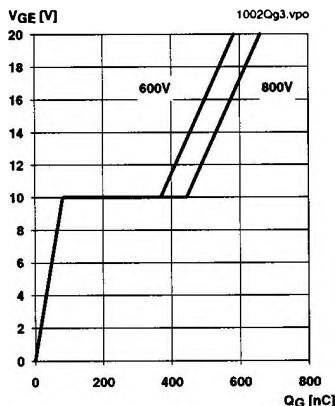
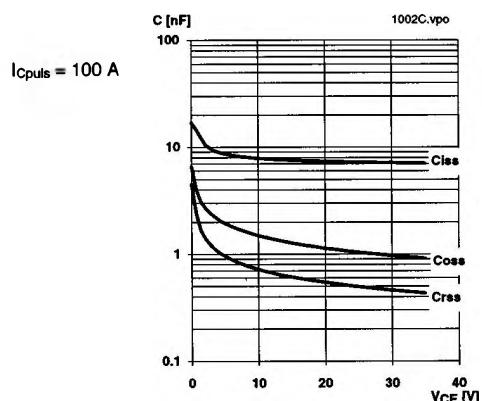
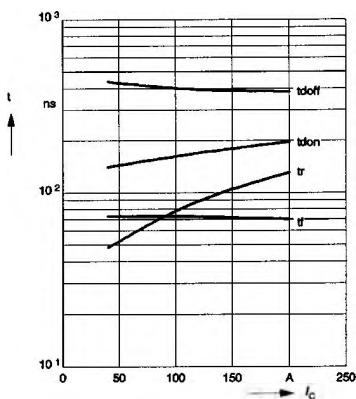


Fig. 13 Typ. gate charge characteristic

Fig. 14 Typ. capacitances vs.  $V_{CE}$ Fig. 15 Typ. switching times vs.  $I_c$ 

$T_j = 125^\circ\text{C}$   
 $V_{CE} = 600 \text{ V}$   
 $V_{GE} = \pm 15 \text{ V}$   
 $R_{gon} = 6,8 \Omega$   
 $R_{goff} = 6,8 \Omega$   
induct. load

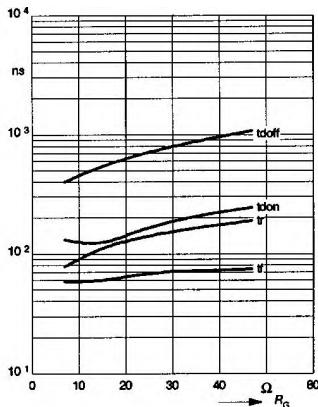
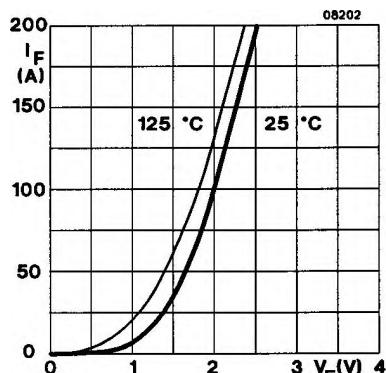
Fig. 16 Typ. switching times vs. gate resistor  $R_G$ 

Fig. 17 Typ. CAL diode forward characteristic

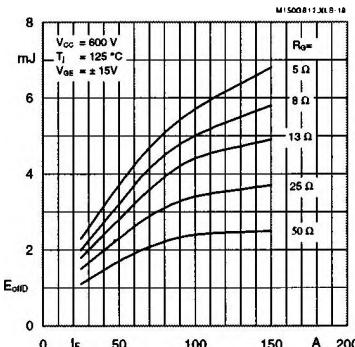


Fig. 18 Diode turn-off energy dissipation per pulse

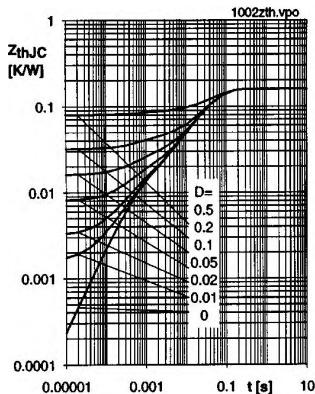


Fig. 19 Transient thermal impedance of IGBT  
 $Z_{thJC} = f(t_p); D = t_p / t_c = t_p \cdot f$

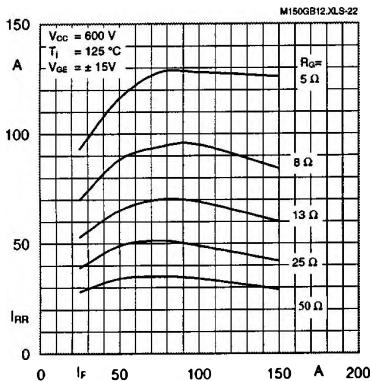


Fig. 22 Typ. CAL diode reverse recovery current  $I_{RR} = f(I_F; R_G)$

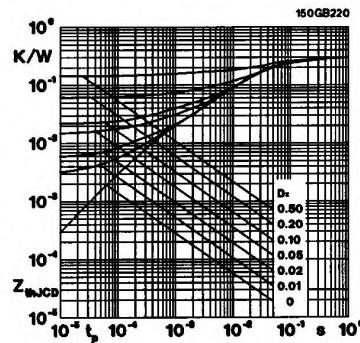


Fig. 20 Transient thermal impedance of  
inverse CAL diodes  $Z_{thJC} = f(t_p); D = t_p / t_c = t_p \cdot f$

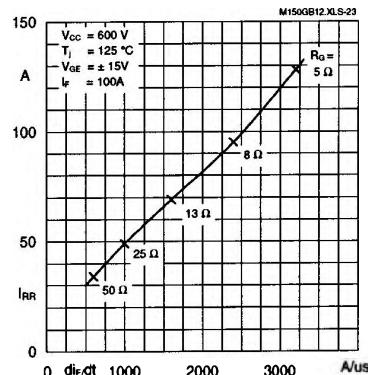


Fig. 23 Typ. CAL diode reverse recovery current  $I_{RR} = f(dif/dt)$

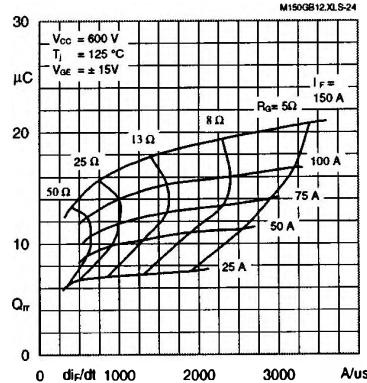


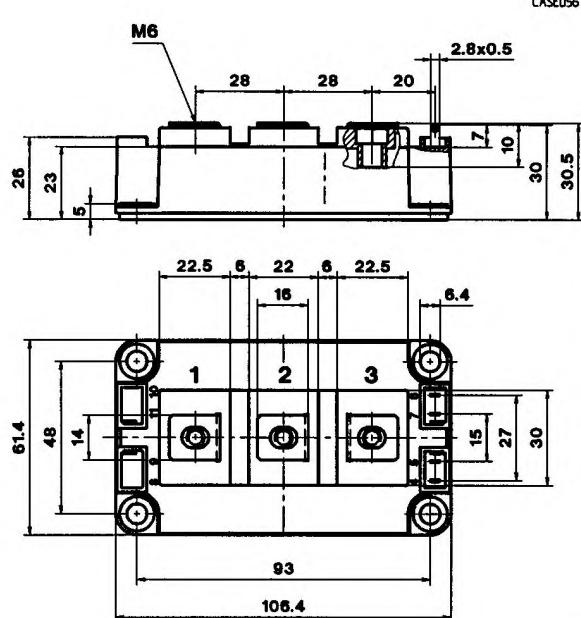
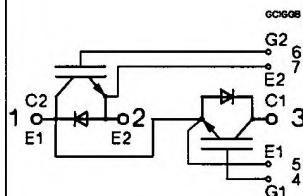
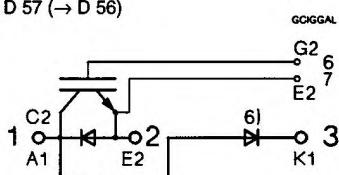
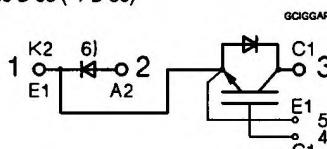
Fig. 24 Typ. CAL diode recovered charge  $Q_{rr} = f(dif/dt)$

**SEMITRANS 3**

Case D 56

UL Recognized

File no. E 63 532

**SKM 150 GB 123 D****SKM 150 GB 173 D****SKM 150 GAL 123 D**Case D 57 ( $\rightarrow$  D 56)**SKM 150 GAR 123 D**Case D 58 ( $\rightarrow$  D 56)

Case outline and circuit diagrams

Symbol	Conditions	Values			Units
		min.	typ.	max.	
M <sub>1</sub>	to heatsink, SI Units to heatsink, US Units	(M6) 3 27	— —	5 44	Nm lb.in.
M <sub>2</sub>	for terminals, SI Units for terminals US Units	(M6) 2,5 22	— —	5 44	Nm lb.in.
a		— —	— —	5x9,81 420	m/s <sup>2</sup> g
w		— —	— —	— —	—

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

Three devices are supplied in one SEMIBOX B without mounting hardware, which can be ordered separately under Ident No. 33321100 (for 10 SEMITRANS 3). Larger packing units of 12 and 20 pieces are used if suitable.

Accessories → page B 6 - 4.  
SEMIBOX → page C - 1.

<sup>5</sup> Freewheeling diode → page B 6 - 59, remark 6.