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MJE1320

**POWER TRANSISTOR
2 AMPERES
900 VOLTS
80 WATTS**

**NPN Silicon Power Transistor
Switchmode Series**

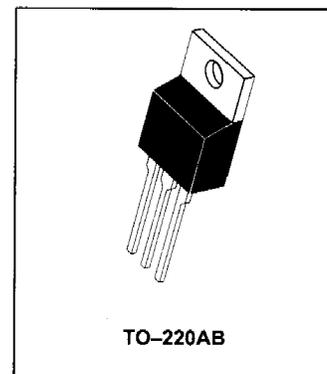
This transistor is designed for high-voltage, power switching in inductive circuits where RBSOA and breakdown voltage are critical. They are particularly suited for line-operated switchmode applications.

Typical Applications:

- Fluorescent Lamp Ballasts
- Inverters
- Solenoid and Relay Drivers
- Motor Controls
- Deflection Circuits

Features:

- High V_{CEV} Capability (1800 Volts)
- Low Saturation Voltage
- 100°C Performance Specified for:
 - Reverse-Biased SOA with Inductive Loads
 - Switching Times with Inductive Loads
 - Saturation Voltages
 - Leakage Currents



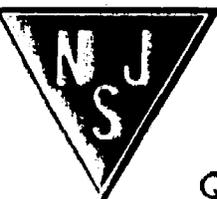
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO(sus)}$	900	Vdc
Collector-Emitter Voltage	V_{CEV}	1800	Vdc
Emitter Base Voltage	V_{EB}	9	Vdc
Collector Current — Continuous	I_C	2	Adc
Peak(1)	I_{CM}	5	
Base Current — Continuous	I_B	1.5	Adc
Peak(1)	I_{BM}	2.5	
Total Power Dissipation @ $T_C = 25^\circ C$	P_D	80	Watts
@ $T_C = 100^\circ C$		32	
Derate above 25°C		0.64	W/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.56	°C/W
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	T_L	275	°C

(1) Pulse Test: Pulse Width = 5 ms, Duty Cycle \leq 10%.



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Quality Semi-Conductors

MJE1320

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage ($I_C = 50\text{ mA}$, $I_B = 0$)	$V_{CEO(sus)}$	900	—	—	Vdc
Collector Cutoff Current ($V_{CEV} = \text{Rated Value}$, $V_{BE(off)} = 1.5\text{ Vdc}$) ($V_{CEV} = \text{Rated Value}$, $V_{BE(off)} = 1.5\text{ Vdc}$, $T_C = 100^\circ\text{C}$)	I_{CEV}	—	—	0.25 2.5	mAdc
Emitter Cutoff Current ($V_{EB} = 9\text{ Vdc}$, $I_C = 0$)	I_{EBO}	—	—	0.25	mAdc

SECOND BREAKDOWN

Second Breakdown Collector Current with base forward biased	$I_{S/b}$	See Figure 13			
Clamped Inductive SOA with Base Reverse Biased	RBSOA	See Figure 14			

ON CHARACTERISTICS(1)

DC Current Gain ($V_{CE} = 5\text{ Vdc}$)	$I_C = 2\text{ Adc}$ $I_C = 1\text{ Adc}$	h_{FE}	2.5 3	4.5 7	— —	— —
Collector-Emitter Saturation Voltage ($I_C = 1\text{ Adc}$, $I_B = 0.5\text{ Adc}$) ($I_C = 2\text{ Adc}$, $I_B = 1\text{ Adc}$) ($I_C = 1\text{ Adc}$, $I_B = 0.5\text{ Adc}$, $T_C = 100^\circ\text{C}$)		$V_{CE(sat)}$	— — —	0.18 0.3 0.3	1 2.5 1.5	Vdc
Base-Emitter Saturation Voltage ($I_C = 1\text{ Adc}$, $I_B = 0.5\text{ Adc}$) ($I_C = 2\text{ Adc}$, $I_B = 1\text{ Adc}$) ($I_C = 1\text{ Adc}$, $I_B = 0.5\text{ Adc}$, $T_C = 100^\circ\text{C}$)		$V_{BE(sat)}$	— — —	0.2 0.9 0.15	1.5 2.8 1.5	Vdc

DYNAMIC CHARACTERISTICS

Output Capacitance ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f_{test} = 1\text{ MHz}$)	C_{ob}	—	80	—	pF
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SWITCHING CHARACTERISTICS

Resistive Load (Table 1)							
Delay Time	$V_{CC} = 250\text{ Vdc}$, $I_C = 1\text{ A}$ $I_{B1} = I_{B2} = 0.5\text{ Adc}$ $t_p = 25\text{ }\mu\text{s}$, Duty Cycle $\leq 2\%$	t_d	—	0.1	—	μs	
Rise Time		t_r	—	0.8	—	μs	
Storage Time		t_s	—	4	—	μs	
Fall Time		t_f	—	0.8	—	μs	
Inductive Load, Clamped (Table 2)							
Storage Time	$I_C = 1\text{ A}$, $V_{clamp} = 400\text{ Vdc}$, $V_{BE(off)} = 2\text{ Vdc}$, $I_{B1} = 0.5\text{ Adc}$	$T_C = 25^\circ\text{C}$	t_{sv}	—	2.8	—	μs
Crossover Time			t_c	—	2.2	—	μs
Storage Time		$T_C = 100^\circ\text{C}$	t_{sv}	—	3.7	10.5	μs
Crossover Time			t_c	—	3.5	10	μs
Fall Time							

(1) Pulse Test: Pulse Width = 300 μs . Duty Cycle $\leq 2\%$.