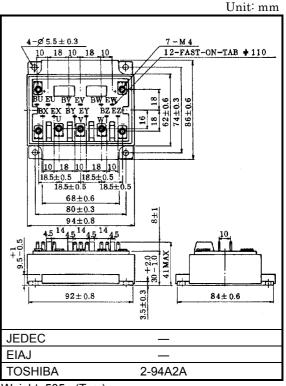
TOSHIBA GTR Module Silicon N Channel IGBT

# MG50J6ES50

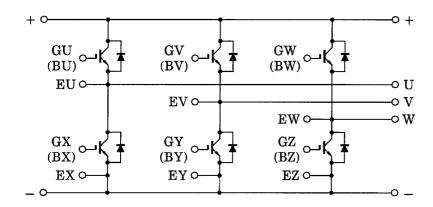
### High Power Switching Applications Motor Control Applications

- The electrodes are isolated from case.
- High input impedance.
- 6 IGBTs built into 1 package.
- Enhancement-mode.
- High speed :  $t_f = 0.30 \mu s$  (Max.) (IC = 50A)  $t_{rr} = 0.15 \mu s$  (Max.) (IF = 50A)
- Low saturation voltage
  - $: V_{CE (sat)} = 2.70 V (Max.) (I_{C} = 50 A)$



#### Weight: 505g (Typ.)

#### **Equivalent Circuit**



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damage to property.

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## Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Collector-emitter voltage		V <sub>CES</sub>	600	V	
Gate-emitter voltage		V <sub>GES</sub>	±20	V	
Collector current	DC	IC	50	Α	
	1ms	I <sub>CP</sub>	100		
Forward current	DC	l <sub>F</sub>	50	А	
	1ms	I <sub>FM</sub>	100		
Collector power dissipation (Tc = 25°C)		PC	280	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	<b>-</b> 40 ~ 125	°C	
Isolation voltage		V <sub>Isol</sub>	2500 (AC 1 min.)	V	
Screw torque (Terminal / mounting)		_	2/3	N·m	

#### **Electrical Characteristics (Ta = 25°C)**

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I <sub>GES</sub>	V <sub>GE</sub> = ±20V, V <sub>CE</sub> = 0	_	_	±500	nA	
Collector cut-off current		I <sub>CES</sub>	V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0	_	_	1.0	mA	
Gate-emitter cut-off voltage		V <sub>GE (off)</sub>	I <sub>C</sub> = 5mA, V <sub>CE</sub> = 5V	5.0	7.0	8.0	V	
Collector-emitter saturation voltage		V <sub>CE</sub> (sat)	I <sub>C</sub> = 50A, V <sub>GE</sub> = 15V	_	2.10	2.70	V	
Input capacitance		C <sub>ies</sub>	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0, f = 1MHz	_	4950	_	pF	
Switching time	Turn-on delay time	t <sub>d (on)</sub>	Inductive load $V_{CC}=300V$ $I_{C}=50A$ $V_{GE}=\pm15V$ $R_{G}=24\Omega$ (Note 1)	_	0.08	0.16	- µs	
	Rise time	t <sub>r</sub>		_	0.12	0.24		
	Turn-on time	t <sub>on</sub>		_	0.40	0.80		
	Turn-off delay time	t <sub>d (off)</sub>		_	0.20	0.40		
	Fall time	t <sub>f</sub>		_	0.15	0.30		
	Turn-off time	t <sub>off</sub>		_	0.50	1.00		
Forward voltage	•	V <sub>F</sub>	I <sub>F</sub> = 50 A, V <sub>GE</sub> = 0	_	2.30	3.00	V	
Reverse recovery time		t <sub>rr</sub>	$I_F = 50 \text{ A}, V_{GE} = -10 \text{ V},$ di / dt = 100 A / $\mu$ s	_	0.08	0.15	μs	
Thermal resistance		R <sub>th (j-c)</sub>	Transistor	_	_	0.45	°C/W	
			Diode	_	_	0.90	C / VV	

Note 1: Switching time test circuit & timing chart

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