

# GP350MHB06S

DS4923-6.1 july 2002

## Half Bridge IGBT Module

Replaces April 2002 version, DS4923-6.0

#### **FEATURES**

- n Channel
- High Switching Speed
- Low Forward Voltage Drop
- Isolated Base

#### **APPLICATIONS**

- PWM Motor Control
- UPS

The Powerline range of modules includes half bridge, chopper, dual and single switch configurations covering voltages from 600V to 3300V and currents up to 2400A.

The GP350MHB06S is a half bridge 600V n channel enhancement mode insulated gate bipolar transistor (IGBT) module. The module is suitable for a variety of medium voltage applications in motor drives and power conversion.

The IGBT has a wide reverse bias safe operating area (RBSOA) for ultimate reliability in demanding applications.

These modules incorporate electrically isolated base plates and low inductance construction enabling circuit designers to optimise circuit layouts and utilise earthed heat sinks for safety.

Typical applications include dc motor drives, ac pwm drivesand ups systems.

### **ORDERING INFORMATION**

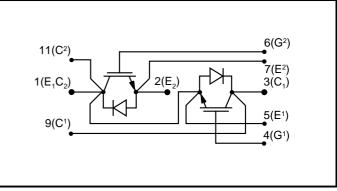
#### Order as: GP350MHB06S

Note; When ordering, use complete part number.

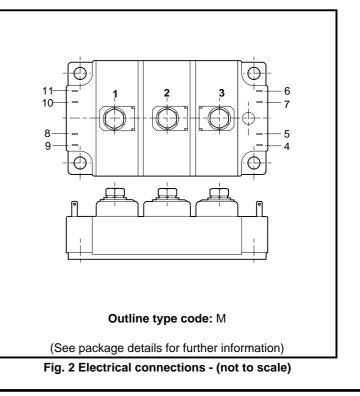
#### **KEY PARAMETERS**

$V_{\text{ces}}$		600V
V <sub>CE(sat)</sub> *	(typ)	2.0V
I <sub>C25</sub>	(max)	500A
I <sub>C75</sub>	(max)	350A
I <sub>С(РК)</sub>	(max)	1000A

\*(measured at the power busbars and not the auxiliary terminals)







Caution: This device is sensitive to electrostatic discharge. Users should follow ESD handling procedures. **www.dynexsemi.com** 



#### **ABSOLUTE MAXIMUM RATINGS - PER ARM**

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

## T<sub>case</sub> = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V <sub>CES</sub>	Collector-emitter voltage	V <sub>GE</sub> = 0V	600	V
V <sub>GES</sub>	Gate-emitter voltage	-	±20	V
Ι <sub>c</sub>	Collector current	DC, $T_{case} = 25^{\circ}C$	500	А
		DC, $T_{case} = 75^{\circ}C$	350	А
I <sub>C(РК)</sub>		1ms, T <sub>case</sub> = 25°C	1000	А
		1ms, T <sub>case</sub> = 75°C	700	А
P <sub>max</sub>	Maximum power dissipation	(Transistor)	1750	W
V <sub>isol</sub>	Isolation voltage	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2500	V

## THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Conditions	Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - transistor	DC junction to case per arm	-	70	°C/kW
R <sub>th(j-c)</sub>	Thermal resistance - diode	DC junction to case -	-	160	°C/kW
R <sub>th(c-h)</sub>	Thermal resistance - Case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	15	°C/kW
T <sub>j</sub>	Junction temperature	Transistor	-	150	°C
		Diode	-	125	°C
T <sub>stg</sub>	Storage temperature range	-	- 40	125	°C
-	Screw torque	Mounting - M6	-	5	Nm
		Electrical connections - M6	-	5	Nm



## **ELECTRICAL CHARACTERISTICS**

### $T_i = 25^{\circ}C$ unless stated otherwise.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I <sub>ces</sub>	Collector cut-off current	$V_{ge} = 0V, V_{ce} = V_{ces}$	-	-	2	mA
		$V_{\text{GE}} = 0$ V, $V_{\text{CE}} = V_{\text{CES}}$ , $T_{\text{j}} = 125$ °C	-	-	-	mA
I <sub>ges</sub>	Gate leakage current	$V_{\text{GE}} = \pm 20$ V, $V_{\text{CE}} = 0$ V	-	-	±1	μA
V <sub>GE(TH)</sub>	Gate threshold voltage	$I_c = 10 \text{mA}, V_{GE} = V_{CE}$	4	-	7.5	V
V t	Collector-emitter saturation voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 350A	-	2.0	2.6	V
V <sub>ce(sat)</sub> †		$V_{GE} = 15V, I_{C} = 350A, T_{j} = 125^{\circ}C$	-	2.2	2.8	V
I <sub>F</sub>	Diode forward current	DC	-	-	215	Α
I <sub>FM</sub>	Diode maximum forward current	t <sub>p</sub> = 1ms	-	-	700	Α
$V_{F}^{\dagger}$	Diode forward voltage	I <sub>F</sub> = 350A,	-	1.51	2.31	V
		I <sub>F</sub> = 350A, T <sub>j</sub> = 125°C	-	1.5	2.3	V
C <sub>ies</sub>	Input capacitance	V <sub>CE</sub> = 25V, V <sub>GE</sub> = 0V, f = 1MHz	-	22500	-	pF

#### Note:

<sup>†</sup> Measured at the power busbars and not the auxiliary terminals

\* L is the circuit inductance +  $L_{M}$ 



## INDUCTIVE SWITCHING CHARACTERISTICS

## $T_j = 25^{\circ}C$ unless stated otherwise

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
t <sub>d(off)</sub>	Turn-off delay time	$I_{c} = 350A$ $V_{GE} = \pm 15V$ $V_{CE} = 50\% V_{CES}$ $R_{G(ON)} = R_{G(OFF)} = 5\Omega$ $L \sim 100nH$	-	730	-	ns
t <sub>r</sub>	Fall time		-	250	-	ns
E <sub>OFF</sub>	Turn-off energy loss		-	26	-	mJ
t <sub>d(on)</sub>	Turn-on delay time		-	320	-	ns
t <sub>r</sub>	Rise time		-	150	-	ns
E <sub>on</sub>	Turn-on energy loss		-	10	-	mJ
t <sub>rr</sub>	Diode reverse recovery time	I <sub>F</sub> = 350A	-	190	-	ns
Q <sub>rr</sub>	Diode reverse recovery charge	$V_{R} = 50\% V_{CES}, dI_{F}/dt = 1000A/\mu s$	-	12	-	μC

## $T_j = 125^{\circ}C$ unless stated otherwise.

t <sub>d(off)</sub>	Turn-off delay time	I <sub>c</sub> = 350A	-	910	-	ns
t,	Fall time		-	490	-	ns
E	Turn-off energy loss	$V_{GE} = \pm 15V$	-	40	-	mJ
t <sub>d(on)</sub>	Turn-on delay time	$V_{CE} = 50\% V_{CES}$ $R_{G(ON)} = R_{G(OFF)} = 5\Omega$ $L \sim 100nH$	-	380	-	ns
t,	Rise time		-	250	-	ns
E <sub>ON</sub>	Turn-on energy loss		-	35	-	mJ
t <sub>rr</sub>	Diode reverse recovery time	I <sub>F</sub> = 350A	-	280	-	ns
Q <sub>rr</sub>	Diode reverse recovery charge	V <sub>R</sub> = 50%V <sub>CES</sub> , dI <sub>F</sub> /dt = 1000A/μs	-	18	-	μC



## **TYPICAL CHARACTERISTICS**

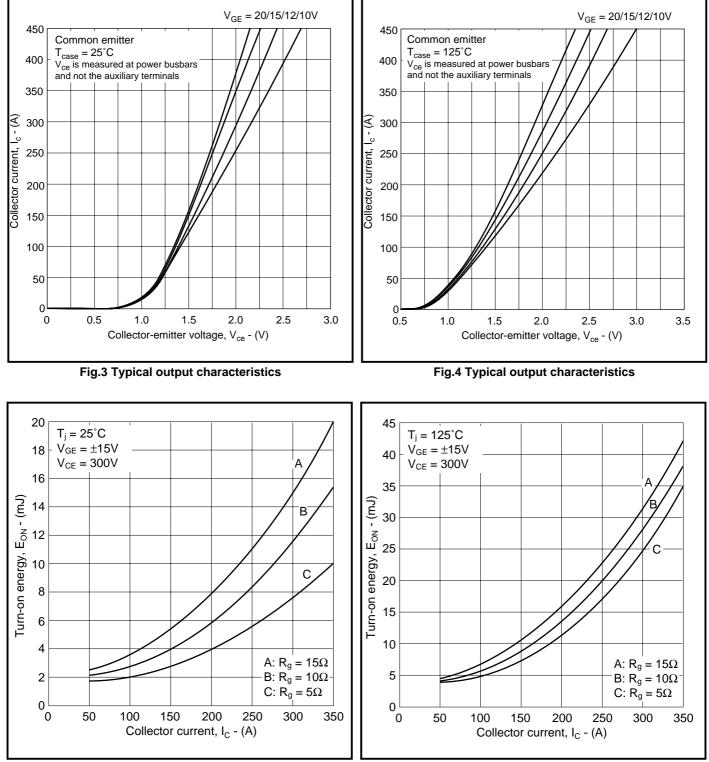
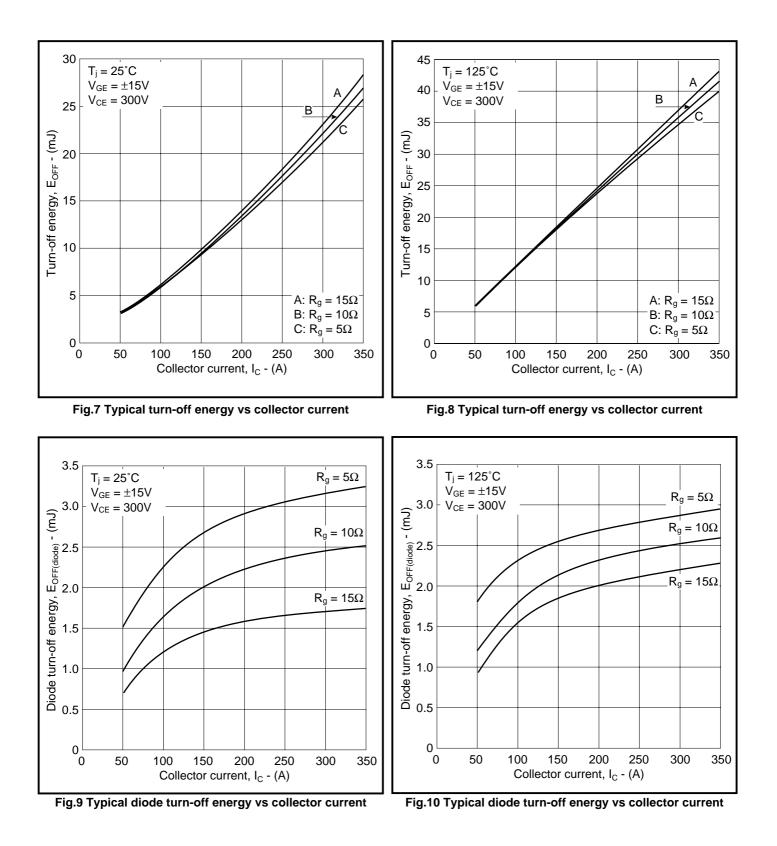


Fig.5 Typical turn-on energy vs collector current



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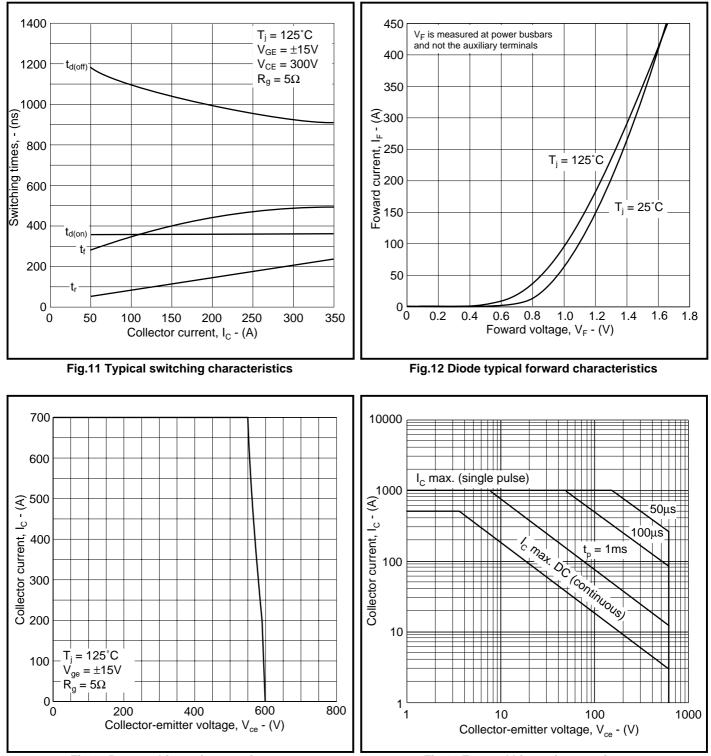


Fig.13 Reverse bias safe operating area

Fig.14 Forward bias safe operating area

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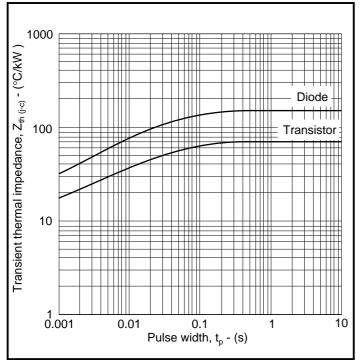
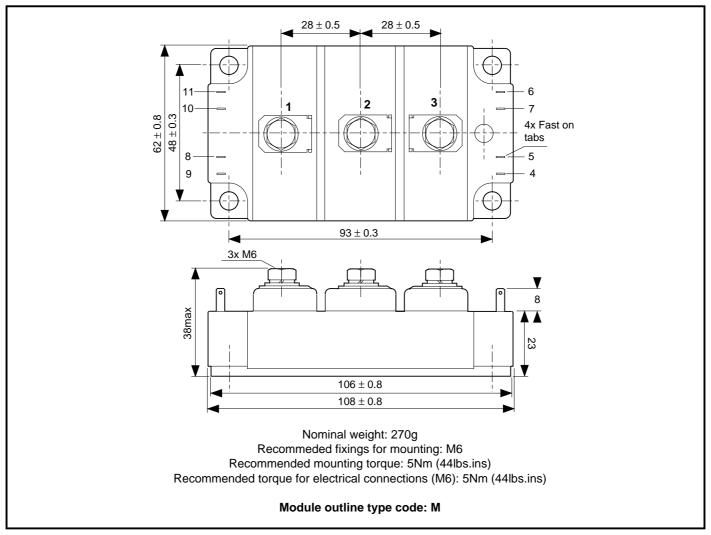


Fig.15 Transient thermal impedance



## PACKAGE DETAILS

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

#### HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



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CUSTOMER SERVICE

Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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