TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3471

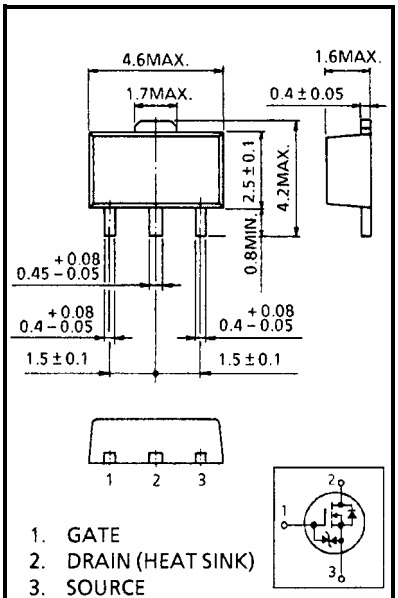
Switching Regulator and DC-DC Converter Applications

- Low drain-source ON resistance: $R_{DS\ (ON)}$ = 10 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}|$ = 0.4 S (typ.)
- Low leakage current: I_{DSS} = 100 μA (max) (V_{DS} = 500 V)
- Enhancement-model: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	500	V
Drain-gate voltage (R_{GS} = 20 kΩ)		V_{DGR}	500	V
Gate-source voltage		V_{GSS}	±30	V
Drain current	DC (Note 1)	I_D	0.5	A
	Pulse (Note 1)	I_{DP}	1.5	
Drain power dissipation		P_D	0.5	W
Drain power dissipation (Note 2)		P_D	1.5	W
Single pulse avalanche energy (Note 3)		E_{AS}	14.3	mJ
Avalanche current		I_{AR}	0.5	A
Repetitive avalanche energy (Note 4)		E_{AR}	0.05	mJ
Channel temperature		T_{ch}	150	°C
Storage temperature range		T_{stg}	−55 to150	°C

Unit: mm



JEDEC	—
JEITA	SC-62
TOSHIBA	2-5K1B

Weight: 0.05 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	$R_{th\ (ch-a)}$	250	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

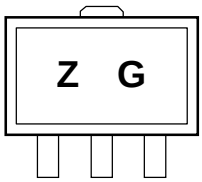
Note 2: Mounted on ceramic substrate (25.4 mm × 25.4 mm × 0.8 mm)

Note 3: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 100 mH, R_G = 25 Ω, I_{AR} = 0.5 A

Note 4: Repetitive rating: Pulse width limited by maximum channel temperature

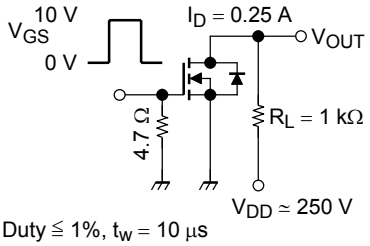
This transistor is an electrostatic sensitive device. Please handle with caution.

Marking



(The two digits represent the part number.)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	—	—	±10	μA
Drain-source breakdown voltage		$V_{(BR)\ GSS}$	I_G = ±10 μA, V_{DS} = 0 V	±30	—	—	V
Drain cut-OFF current		I_{DSS}	V_{DS} = 500 V, V_{GS} = 0 V	—	—	100	μA
Drain-source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 10 mA, V_{GS} = 0 V	500	—	—	V
Gate threshold voltage		V_{th}	V_{DS} = 10 V, I_D = 1 mA	2.0	—	4.0	V
Drain-source ON resistance		$R_{DS\ (ON)}$	V_{GS} = 10 V, I_D = 0.25 A	—	10	18	Ω
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 10 V, I_D = 0.25 A	0.2	0.4	—	S
Input capacitance		C_{iss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz	—	75	—	pF
Reverse transfer capacitance		C_{rss}		—	7	—	
Output capacitance		C_{oss}		—	24	—	
Switching time	Rise time	t_r		—	11	—	ns
	Turn-ON time	t_{on}		—	18	—	
	Fall time	t_f		—	54	—	
	Turn-OFF time	t_{off}		—	95	—	
Total gate charge (gate-source plus gate-drain)		Q_g	V_{DD} ≃ 400 V, V_{GS} = 10 V, I_D = 0.5 A	—	3.8	—	nC
Gate-source charge		Q_{gs}		—	1.9	—	
Gate-drain (“miller”) charge		Q_{gd}		—	1.9	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	—	—	—	0.5	A
Pulse drain reverse current (Note 1)	I_{DRP}	—	—	—	1.5	A
Forward voltage (diode)	V_{DSF}	I_{DR} = 0.5 A, V_{GS} = 0 V	—	—	−1.5	V
Reverse recovery time	t_{rr}	I_{DR} = 0.5 A, V_{GS} = 0 V, dI_{DR}/dt = 100 A/μs	—	190	—	ns
Reverse recovery charge	Q_{rr}		—	380	—	nC

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