TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3437

DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain-source ON resistance: RDS (ON) = 0.74Ω (typ.)

• High forward transfer admittance: $|Y_{fs}| = 4.5 \text{ S (typ.)}$

• Low leakage current: $IDSS = 100 \mu A (max) (VDS = 600 V)$

• Enhancement-model: $V_{th} = 3.0 \sim 5.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	600	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	10	А	
	Pulse (Note 1)	I _{DP}	30		
Drain power dissipation (Tc = 25°C)		P _D	80	W	
Single pulse avalanche energy (Note 2)		E _{AS}	252	mJ	
Avalanche current		I _{AR}	10	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	8	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.56	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

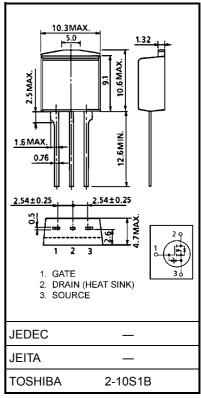
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 4.41 mH, $R_G = 25 \Omega$, $I_{\Delta R} = 10 \text{ A}$

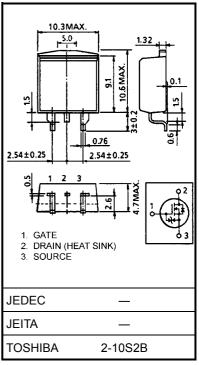
Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.





Weight: 1.5 g (typ.)



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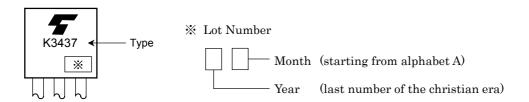
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	ent	I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain-source brea	kdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	_	_	V
Drain cut-OFF cui	rent	I _{DSS}	$V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	100	μА
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	600	_	_	V
Gate threshold vo	Itage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	5.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 5 A	_	0.74	1.0	Ω
Forward transfer a	admittance	Y _{fs}	V _{DS} = 15 V, I _D = 5 A	2.0	4.5	_	S
Input capacitance		C _{iss}		_	1200	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	10	_	pF
Output capacitance		C _{oss}		_	130	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{I}_D = 5 \text{ A} & \text{V}_{OUT} \\ \hline 0 \text{ V} & \text{RL} = 60 \Omega \\ \hline \end{array}$ $\begin{array}{c} R_L = 60 \Omega \\ \hline \end{array}$	_	13	_	- ns
	Turn-ON time	t _{on}			40		
	Fall time	t _f		_	8	_	
	Turn-OFF time	t _{off}		_	50	_	
Total gate charge (gate-source plus gate-drain)		Qg			28		nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	16	_	
Gate-drain ("miller") charge		Q_{gd}		_	12	_	

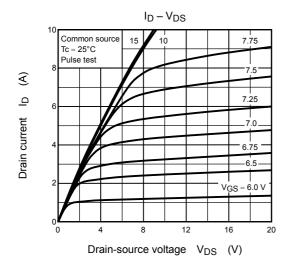
Source-Drain Ratings and Characteristics (Ta = 25°C)

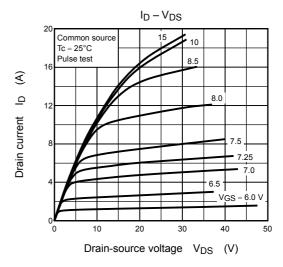
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 10 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 10 A, V _{GS} = 0 V,	_	1600	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 100 A/μs		17		μС

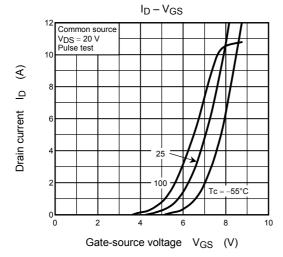
Marking

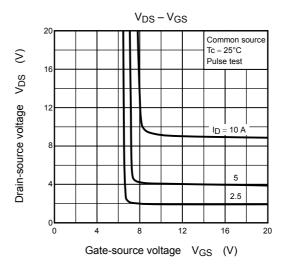


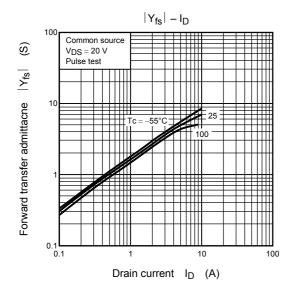
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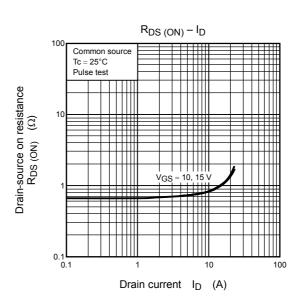




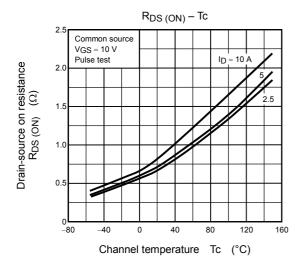


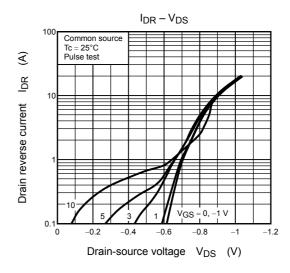


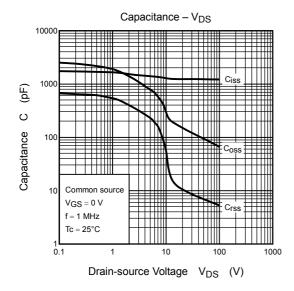


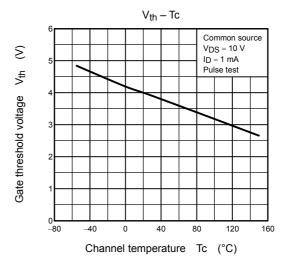


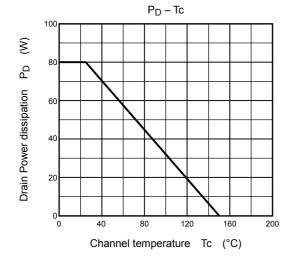
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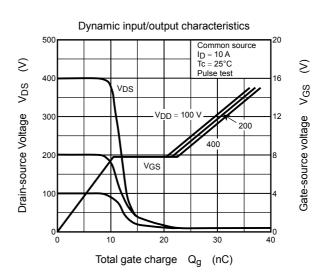




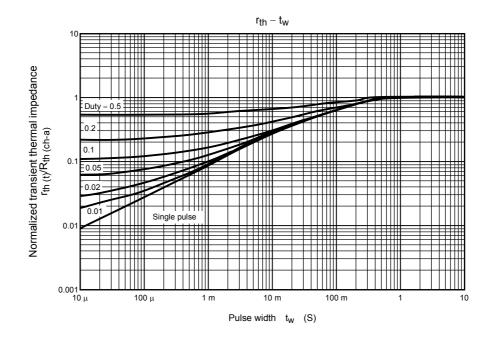


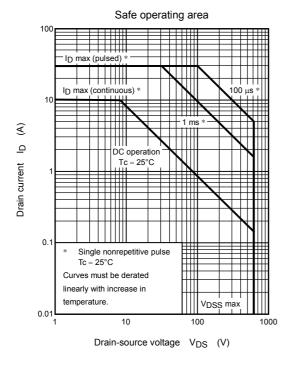


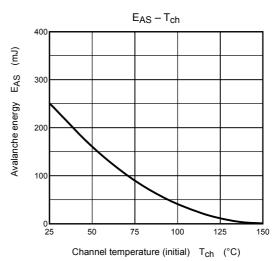


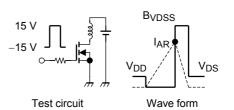


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$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 4.41~mH \end{aligned} \qquad \text{EAS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{\text{BVDSS}}{\text{BVDSS} - \text{VDD}} \right) \end{aligned}$$

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