## <u>TOSHIBA</u>

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSV)

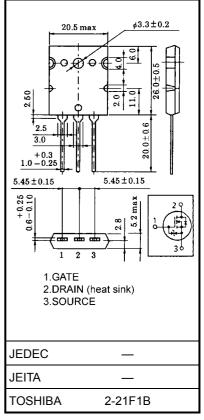
# 2SK3131

## Chopper Regulator DC–DC Converter and Motor Drive Applications

- Fast reverse recovery time  $: t_{rr} = 105 \text{ ns (typ.)}$
- Built-in high-speed free-wheeling diode
- Low drain-source ON resistance  $: RDS(ON) = 0.085 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 35 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement-mode :  $V_{th} = 2.4 \sim 3.4 \text{ V} (V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA})$

#### Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	500	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
DC Drain current	DC (Note 1)	Ι <sub>D</sub>	50	А	
	Pulse (Note 1)	I <sub>DP</sub>	200	А	
Drain power dissipation	n (Tc = 25°C)	PD	250	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	525	mJ	
Avalanche current		I <sub>AR</sub>	50	А	
Repetitive avalanche e	energy (Note 3)	E <sub>AR</sub>	25	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C	



Weight: 9.75 g (typ.)

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	0.5	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	35.7	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V\_DD = 90 V, T\_ch = 25 °C (initial), L = 357  $\mu$ H, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 50 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution. Unit: mm

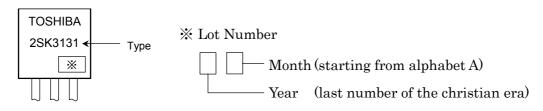
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0 V		—	±10	μA
Gate-source br	eakdown voltage	V <sub>(BR)</sub> GSS	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	-	100	μA
Drain-source br	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.4	_	3.4	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	_	0.085	0.11	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 25 A	15	35	_	S
Input capacitance	ce	C <sub>iss</sub>		_	11000	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	2100	_	pF
Output capacitance		C <sub>oss</sub>			4200	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \prod_{OV \\ OV \\ C \\ $	_	105	_	ns
	Turn-on time	t <sub>on</sub>		_	160	_	
	Fall time	t <sub>f</sub>		_	65	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> =10µs	_	245	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	280	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A		150	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			130	_	

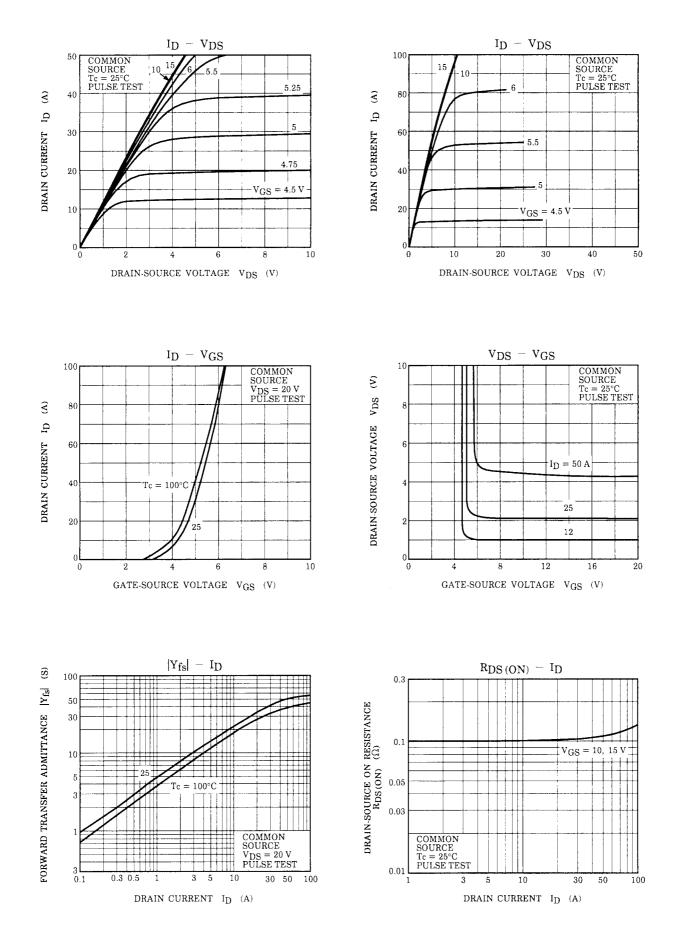
#### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	50	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	200	А
Forward voltage (diode)	V <sub>DSF</sub>	V <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 50 A, V <sub>GS</sub> = 0 V		105	—	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 100 A / μs		380	_	nC

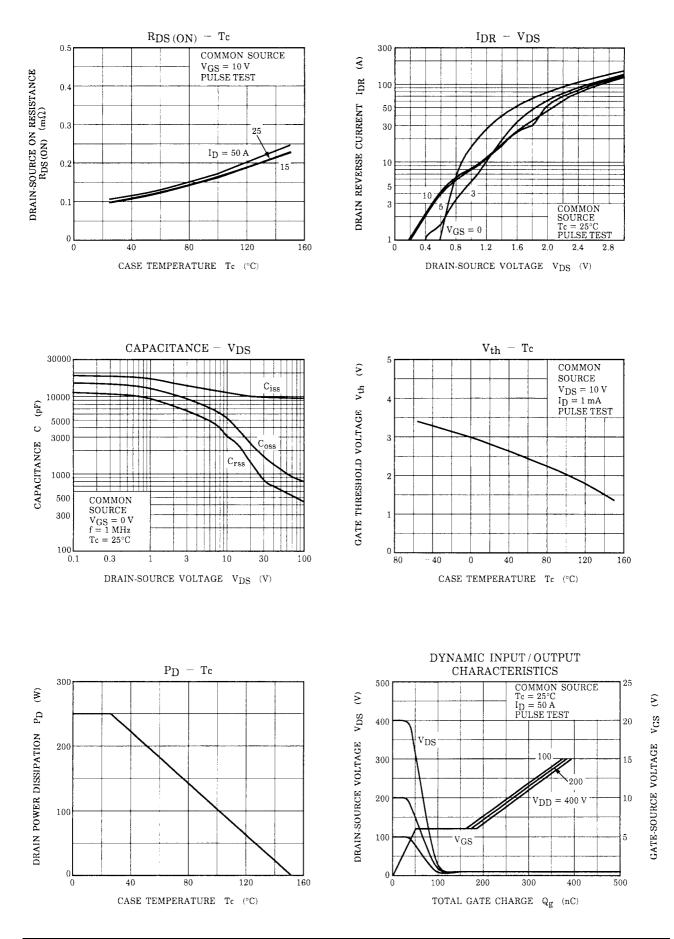
#### Marking

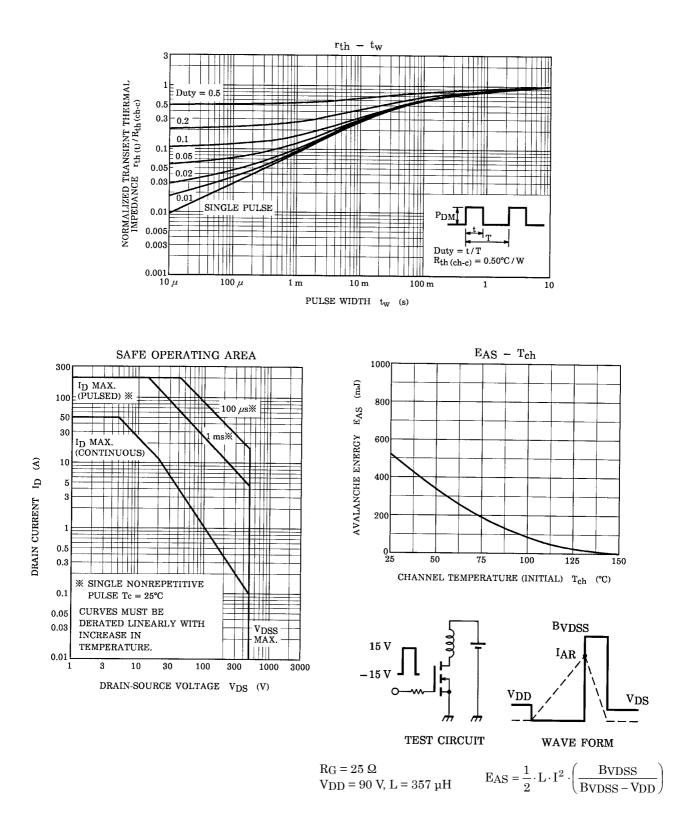


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