TOSHIBA

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

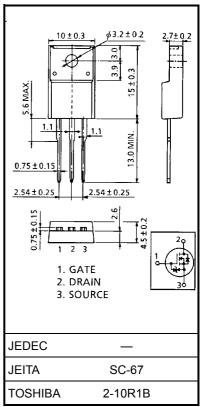
2SK3067

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- Low drain-source ON resistance $: R_{DS}(ON) = 4.2 \Omega$ (typ.)
- High forward transfer admittance \therefore |Y_{fs}| = 1.7 S (typ.)
- $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$ Low leakage current
- Enhancement-mode : $V_{th} = 2.0 \sim 4.0 V (V_{DS} = 10 V, I_D = 1 mA)$

Maximum Ratings (Tc = 25°C)

Charac	teristics	Symbol	Rating	Unit
Drain-source volta	ige	V _{DSS}	600	V
Drain-gate voltage	e (R _{GS} = 20 kΩ)	V _{DGR}	600	V
Gate-source volta	ge	V _{GSS}	±30	V
Drain current	DC (Note 1)	I _D	2	А
	Pulse (t = 1 ms) (Note 1)	I _{DP}	5	A
	Pulse (t = 100 µs) (Note 1)	I _{DP}	8	A
Drain power dissip	ation	PD	25	W
Single pulse avala	nche energy (Note 2)	E _{AS}	93	mJ
Avalanche current		I _{AR}	2	А
Repetitive avalanc	he energy (Note 3)	E _{AR}	2.5	mJ
Channel temperatu	ıre	T _{ch}	150	°C
Storage temperatu	re range	T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Electrical Characteristics (Tc = 25°C)

Characteristics	Symbol	Max	Unit
Thermal reverse, channel to case	R _{th (ch−c)}	5.0	°C / W
Thermal reverse, channel to ambient	R _{th (ch−a)}	62.5	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 41 mH, R_G = 25 Ω , I_{AR} = 2 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm

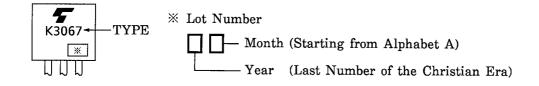
Electrical Characteristics (Tc = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I_{G} = ±10 µA, V_{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600		-	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	-	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 1 A	_	4.2	5.0	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 A	0.8	1.7	-	S
Input capacitance	e	C _{iss}		_	380	-	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		40	_	pF
Output capacitance		C _{oss}			120	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \prod_{\substack{OV \\ OV \\ IC}} I_{D} \stackrel{I_{D}=1A}{_{VOUT}} V_{OUT}$	_	15	_	
	Turn-on time	t _{on}		_	25	_	ns
	Fall time	t _f		_	20	_	115
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w =10µs	_	80	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	9	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 480 V, V _{GS} = 10 V, I _D = 2 A		5		nC
Gate-drain ("miller") charge		Q _{gd}			4	_	

Source–Drain Ratings and Characteristics (Tc = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	-	_	_	2	А
Pulse drain reverse current (Note 1)	I _{DRP}	t = 1 ms	_	_	5	А
	I _{DRP}	t = 100 μs	—	—	8	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 2 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 2 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs	_	1000	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs		5.0	_	μC

Marking



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