## TOSHIBA

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type

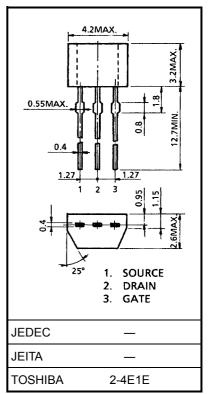
# 2SJ167

High Speed Switching Applications Analog Switch Applications Interface Applications

- Excellent switching time: t<sub>on</sub> = 14 ns (typ.)
- High forward transfer admittance:  $|\,Y_{\rm fs}\,|$  = 100 mS (min)
- Low on resistance:  $R_{DS}$  (ON) = 1.3  $\Omega$  (typ.)
- Enhancement-mode
- Complementary to 2SK1061

#### Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	-60	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC	I <sub>D</sub>	-200	mA	
	Pulse	I <sub>DP</sub>	-800		
Drain power dissipation (Ta = $25^{\circ}$ C)		PD	300	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	



Weight: 0.13 g (typ.)

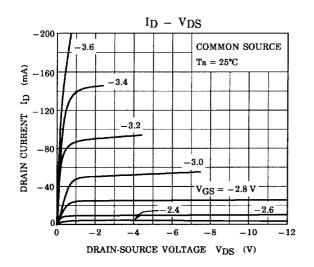
Electrical Characteristics (Ta = 25°C)

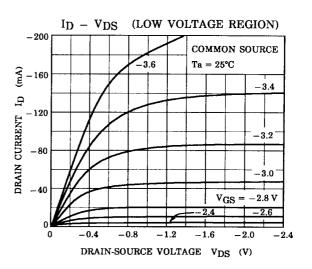
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS}=\pm 10~V,~V_{DS}=0$	_		±100	nA
Drain cut-off curre	ent	I <sub>DSS</sub>	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$			-10	μA
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = -1 \text{ mA},  V_{GS} = 0$	-60			V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = -10 V, I_D = -1 mA$	-2		-3.5	V
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -50 \text{ mA}$	100			mS
Drain-source ON	resistance	R <sub>DS (ON)</sub>	$I_D = -50 \text{ mA}, V_{GS} = -10 \text{ V}$		1.3	2.0	Ω
Drain-source ON	voltage	V <sub>DS (ON)</sub>	$I_D = -50 \text{ mA}, V_{GS} = -10 \text{ V}$		-65	-100	mV
Input capacitance	9	C <sub>iss</sub>	$V_{DS} = -10 V, V_{GS} = 0, f = 1 MHz$		73	85	pF
Reverse transfer	capacitance	C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$		15	22	pF
Output capacitance		C <sub>oss</sub>	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$		48	60	pF
Switching time	Rise time	tr	$I_{D} = -100 \text{ mA}$ $-10 \text{ V}_{IN} \qquad \qquad$		8	_	ns
	Turn-on time	t <sub>on</sub>			14	_	
	Fall time	t <sub>f</sub>	$v_{DD} = -30 V$		35	_	
	Turn-off time	t <sub>off</sub>	$V_{IN}$ : $t_r$ , $t_f < 5$ ns D.U. $\leq$ 1% (Z <sub>out</sub> = 50 $\Omega$ )	—	100	—	

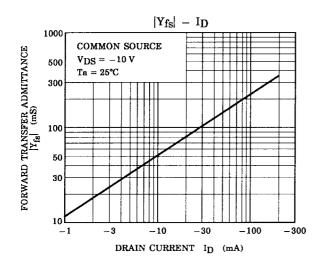
Note: This transistor is the electrostatic sensitive device.

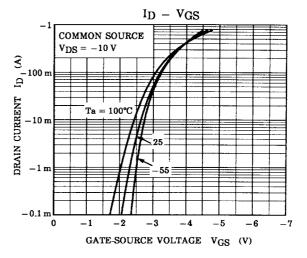
Please handle with caution.

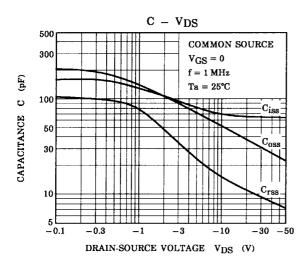
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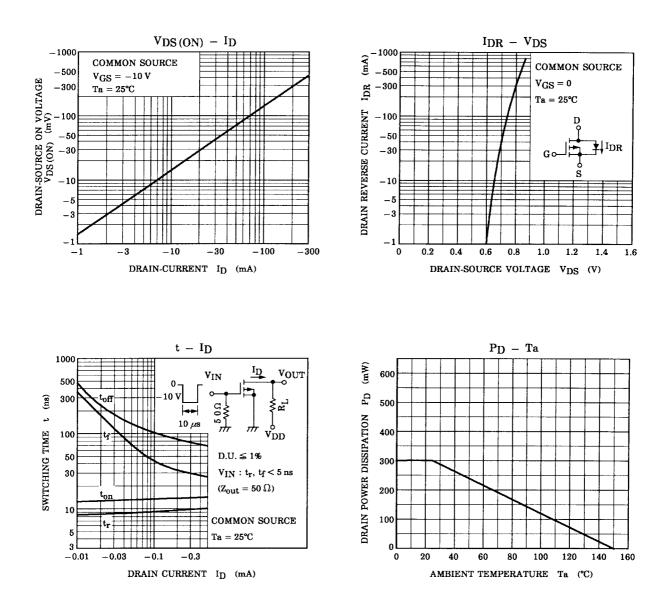








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