# 2SC3943

## Silicon NPN epitaxial planar type

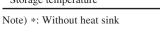
### For video amplifier

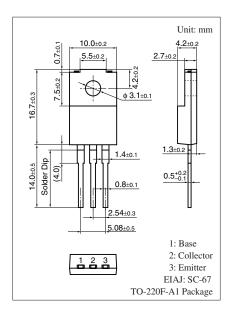
#### ■ Features

- High transition frequency f<sub>T</sub>
- Small collector output capacitance (Common base, input open circuited) Cob
- Full-pack package which can be installed to the heat sink with one screw

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	110	V	
Collector-emitter voltage (Resistor between B and E)	V <sub>CER</sub>	100	V	
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	50	V	
Emitter-base voltage (Collector open)	$V_{EBO}$	3.5	V	
Collector current	$I_C$	150	mA	
Peak collector current	$I_{CP}$	300	mA	
Collector power dissipation *	P <sub>C</sub>	2.0	W	
Junction temperature	$T_{j}$	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	

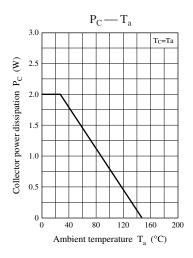


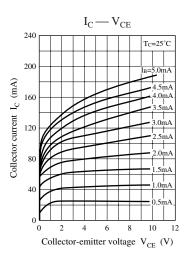


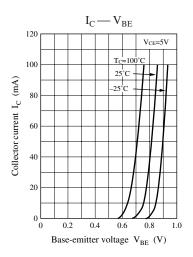
## ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

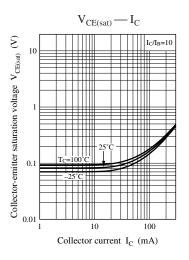
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V <sub>CBO</sub>	$I_C = 100 \mu\text{A},  I_E = 0$	110			V
Collector-emitter voltage	V <sub>CER</sub>	$I_C = 500 \ \mu A, R_{BE} = 470 \ \Omega$	100			V
(Resistor between B and E)						
Collector-emitter voltage (Base open)	V <sub>CEO</sub>	$I_C = 1 \text{ mA}, I_B = 0$	50			V
Emitter-base voltage (Collector open)	$V_{EBO}$	$I_E = 100  \mu A,  I_C = 0$	3.5			V
Collector-emitter cutoff current (Base open)	$I_{CEO}$	$V_{CE} = 35 \text{ V}, I_{B} = 0$			10	μΑ
Forward current transfer ratio	$h_{FE}$	$V_{CE} = 5 \text{ V}, I_{C} = 100 \text{ mA}$	20			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	$I_C = 150 \text{ mA}, I_B = 15 \text{ mA}$			0.5	V
Transition frequency	$f_{T1}$	$V_{CB} = 10 \text{ V}, I_{C} = 10 \text{ mA}, f = 10 \text{ MHz}$		300		MHz
	f <sub>T2</sub>	$V_{CB} = 10 \text{ V}, I_{C} = 110 \text{ mA}, f = 10 \text{ MHz}$		350		
Collector output capacitance	Cob	$V_{CB} = 30 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$		3.5		pF
(Common base, input open circuited)						

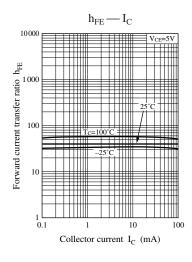
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

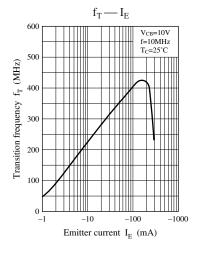


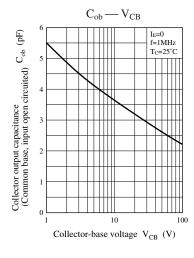


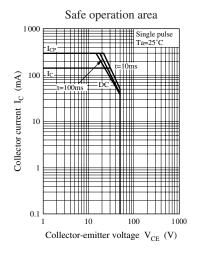












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