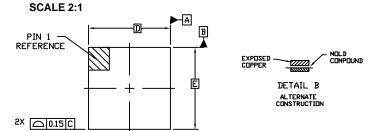


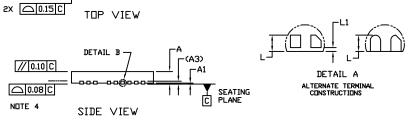
DATE 08 SEP 2017



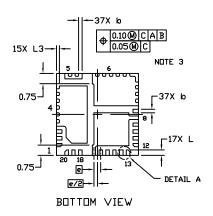
NUTES

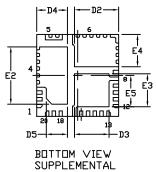
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 FROM THE TERMINAL TIP.
- 4. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.

MILLIMETERS



DIM	MIN.	MAX.	
Α	0.80	1.00	
A1	0.05		
A3	0.20 REF		
Ø	0.18 0.30		
D	6.00 BSC		
D2	3.15 3.35		
D3	2.50	2.60	
D4	2.15	2.35	
D5	1.50	1.60	
E	6.00 BSC		
E2	4.15 4.25		
E3	2.38	2.48	
E4	2.28	2.48	
E5	2.20	2.40	
L	0.30	0.50	
L1	0.15		
L3	0.10	0.30	





		6.30	-	
	<u> </u>	4.74 -1.69		
0.50-		-1.69		17X ┌0.60
‡ 1	<u>_baa</u>	##D0000		_
0.40			昌 2.37	-17
4.34 18X - 0.32	 	#	2.56	
<u>+</u>	<u>. </u>	Ц_	1 200	2.63
1.32-	 - 	<u> </u>	V_R0	.40
2.47		3.56-	- -	
	PECHN	MENDER	1	

MOUNTING FOOTPRINT

GENERIC
MARKING DIAGRAM*

XXXXXXX XXXXXXX AWLYYWWG

A = Assembly Location

WL = Wafer Lot YY = Year

WW = Work Week

G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ■", may or may not be present.

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98AON12706	iG .

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY N. JAOUDE.	19 JUL 2016
Α	CHANGE THE L MEASUREMENT MAX VALUE TO 0.50 MM. REQ. BY R SANTOS.	23 MAY 2017
В	UPDATED RECOMMENDED MOUNTING FOOTPRINT. REQ. BY R SANTOS.	08 SEP 2017
·		

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