# Very Low Forward Voltage Trench-based Schottky Rectifier

Exceptionally Low  $V_F = 0.455 \text{ V}$  at  $I_F = 5 \text{ A}$ 

#### **Features**

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- Pb-Free and Halide-Free Packages are Available

# **Typical Applications**

- Switching Power Supplies including Notebook / Netbook Adapters, ATX and Flat Panel Display
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation

## **Mechanical Characteristics**

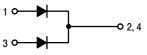
- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Maximum for 10 sec

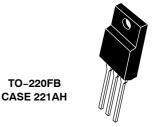


# ON Semiconductor®

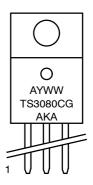
http://onsemi.com

# PIN CONNECTIONS





#### MARKING DIAGRAMS



A = Assembly Location

Y = Year WW = Work Week

AKA = Polarity Designator

G = Pb-Free Package

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

#### **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	80	V
Average Rectified Forward Current (Rated $V_R$ , $T_C = 115$ °C)	Per device Per diode	I <sub>F(AV)</sub>	30 15	А
Peak Repetitive Forward Current (Rated V <sub>R</sub> , Square Wave, 20 kHz, T <sub>C</sub> = 110°C)	Per device Per diode	I <sub>FRM</sub>	60 30	А
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I <sub>FSM</sub>	160	А
Operating Junction Temperature		TJ	-40 to +150	°C
Storage Temperature		T <sub>stg</sub>	-40 to +150	°C
Voltage Rate of Change (Rated V <sub>R</sub> )		dv/dt	10,000	V/μs

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### THERMAL CHARACTERISTICS

Rating		Symbol	Value	Unit
Maximum Thermal Resistance (insertion mounted to 1 oz FR4 Board)	Junction-to-Case	$R_{ heta JC}$	4.0	°C/W
(insertion mounted to 1 oz FR4 board)	Junction-to-Ambient	$R_{\theta JA}$	105	°C/W

<sup>1.</sup> Junction-to-Case, using large Heatsink attached to device.

# **ELECTRICAL CHARACTERISTICS** (Per Leg unless otherwise noted)

Rating	Symbol	Тур	Max	Unit
Maximum Instantaneous Forward Voltage (Note 3) $ \begin{aligned} &(I_F=5~A,~T_J=25^\circ\text{C})\\ &(I_F=7.5~A,~T_J=25^\circ\text{C})\\ &(I_F=15~A,~T_J=25^\circ\text{C})\\ &(I_F=5~A,~T_J=125^\circ\text{C}) \end{aligned} $	VF	0.516 0.576 0.734 0.455	- - 0.85 -	V
$(I_F = 7.5 \text{ A}, T_J = 125^{\circ}\text{C})$ $(I_F = 15 \text{ A}, T_J = 125^{\circ}\text{C})$		0.522 0.627	0.68	
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, T <sub>J</sub> = 25°C) (Rated dc Voltage, T <sub>J</sub> = 125°C)	I <sub>R</sub>	20 8	700 30	μA mA

<sup>3.</sup> Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq 2.0\%$ 

# **ORDERING INFORMATION**

Device	Package	Shipping
NTSJ3080CTG	TO-220FB (Pb-Free)	50 Units / Rail

<sup>2.</sup> Junction-to-Ambient, using with no Heatsink.

#### TYPICAL CHARACTERISITICS

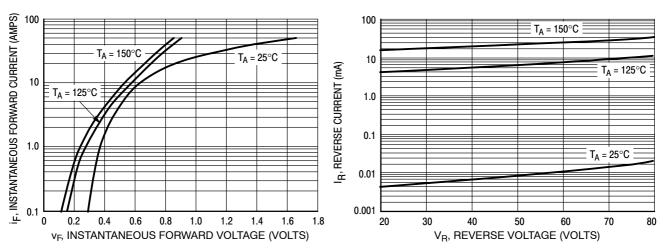


Figure 1. Typical Forward Voltage

Figure 2. Typical Reverse Current

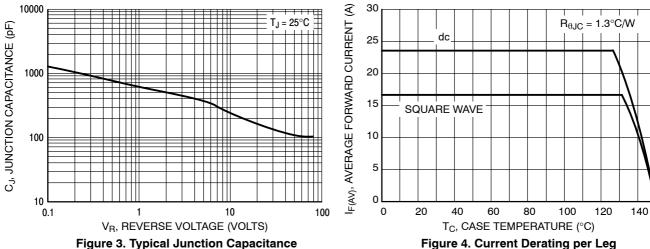


Figure 3. Typical Junction Capacitance

60

55

50

45

40 35

30

25 20

15 10 5

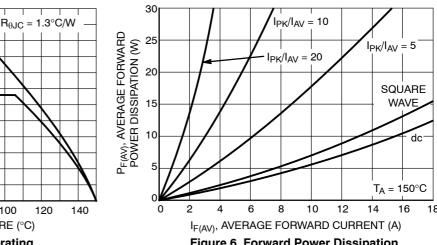
0

20

dc

**SQUARE WAVE** 

I<sub>E(AV)</sub>, AVERAGE FORWARD CURRENT (A)



T<sub>C</sub>, CASE TEMPERATURE (°C) Figure 5. Current Derating

Figure 6. Forward Power Dissipation

# **TYPICAL CHARACTERISITICS**

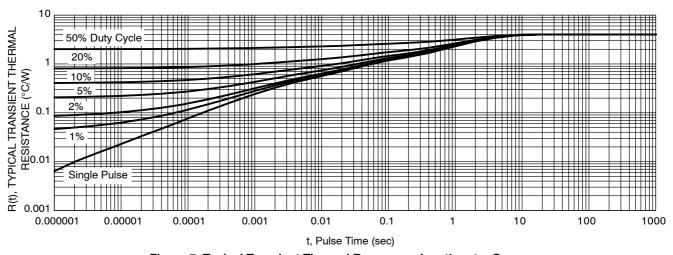
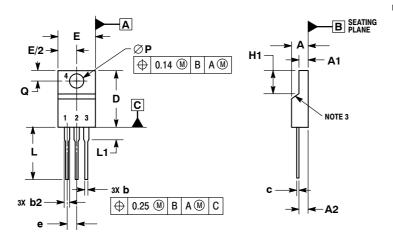


Figure 7. Typical Transient Thermal Response, Junction-to-Case

#### PACKAGE DIMENSIONS

# TO-220 FULLPACK, 3-LEAD

CASE 221AH **ISSUE B** 



- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

- THE JUNE 1987.

  CONTROLLING DIMENSION: MILLIMETERS.

  CONTOUR UNCONTROLLED IN THIS AREA.

  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH

  AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY.
- 5 DIMENSION 62 DOES NOT INCLUDE DAMBAR PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.

	MILLIMETERS			
DIM	MIN	MAX		
Α	4.30	4.70		
A1	2.50	2.90		
A2	2.50	2.70		
b	0.54	0.84		
b2	1.10	1.40		
С	0.49	0.79		
D	14.70	15.30		
Ε	9.70	10.30		
е	2.54	.54 BSC		
H1	6.70	7.10		
L	12.70	14.73		
L1		2.80		
Р	3.00	3.40		
Q	2.80	3.20		

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice on semiconductor and war engineer trademarks of semiconductor components industries, Ite (SciLLC) solitate services are injective to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada

Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative