

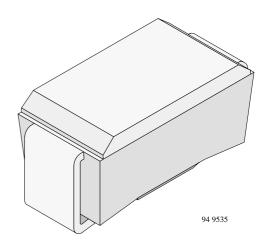
Super Fast Silicon Mesa SMD Rectifier

Features

- Controlled avalanche characteristic
- Glass passivated junction
- Low reverse current
- Low forward voltage
- Soft recovery characteristic
- Very fast reverse recovery time
- Good switching behaviour
- Wave and reflow solderable



Surface mounting Super fast rectifier Freewheeling diodes in SMPS and converters Snubber diodes



Absolute Maximum Ratings

 $T_j = 25^{\circ}C$

Parameter	Test Conditions	Type	Symbol	Value	Unit
Reverse voltage, repetitive peak		BYG22A	$V_R = V_{RRM}$	50	V
reverse voltage		BYG22B	$V_R = V_{RRM}$	100	V
		BYG22D		200	V
Peak forward surge current	t _p =10ms		I _{FSM}	35	A
Average forward current			I _{FAV}	2	A
Junction temperature			T_{j}	150	°C
Storage temperature range			T _{stg}	-55+150	°C
Max. pulse energy in avalanche mode, non repetitive (inductive load switch off)	I _{(BR)R} =1A, T _j =25°C		E _R	20	mWs

Maximum Thermal Resistance

 $T_i = 25^{\circ}C$

Parameter	Test Conditions	Symbol	Value	Unit
Junction case		R_{thJC}	25	K/W
Junction ambient	mounted on epoxy-glass hard tissue, Fig. 1a	R_{thJA}	150	K/W
	mounted on epoxy–glass hard tissue, Fig. 1b	R_{thJA}	125	K/W
	mounted on Al-oxid-ceramic (Al ₂ O ₃), Fig. 1b	R_{thJA}	100	K/W

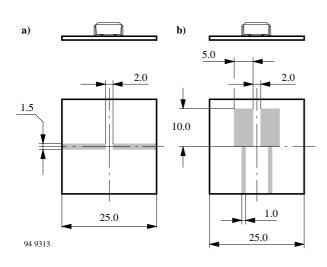


Characteristics

 $T_i = 25^{\circ}C$

Parameter	Test Conditions	Type	Symbol	Min	Тур	Max	Unit
Forward voltage	I _F =1A		V_{F}			1	V
	I _F =2A		V_{F}			1.1	V
Reverse current	$V_R = V_{RRM}$		I_R			1	μΑ
	$V_R = V_{RRM}, T_j = 100$ °C		I_R			10	μΑ
Reverse recovery time	I _F =0.5A, I _R =1A, i _R =0.25A		t _{rr}			25	ns

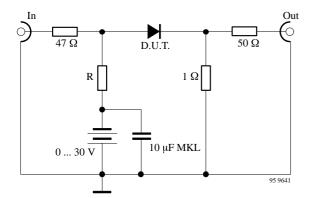
Typical Characteristics $(T_j = 25^{\circ}C \text{ unless otherwise specified})$



2.4 2.2 2.4 7.0 94 9314

Figure 1. Boards for R_{thJA} definition (copper overlay 35μ)

Figure 2. Recommended foot pads (in mm)



Circuit 1: $R = 24 \Omega$ Circuit 2: $R = 50 \Omega$

Figure 3. Turn off switching test circuit

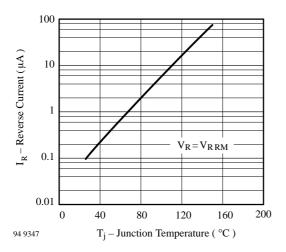


Figure 4. Reverse Current vs. Junction Temperature

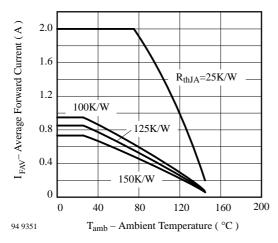


Figure 5. Average Forward Current vs. Ambient Temperature

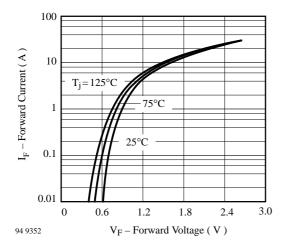


Figure 6. Forward Current vs. Forward Voltage

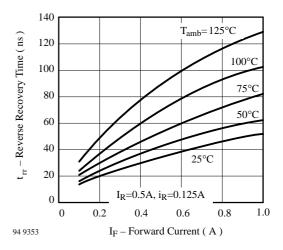


Figure 7. Reverse Recovery Time vs. Forward Current

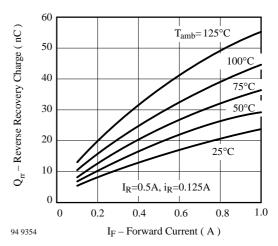


Figure 8. Reverse Recovery Charge vs. Forward Current



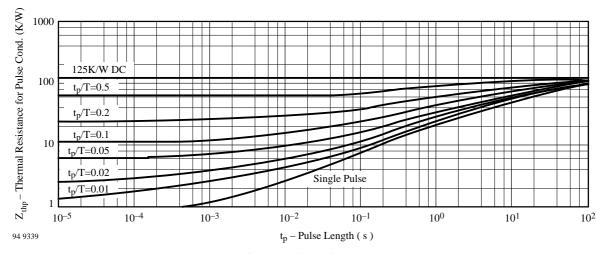
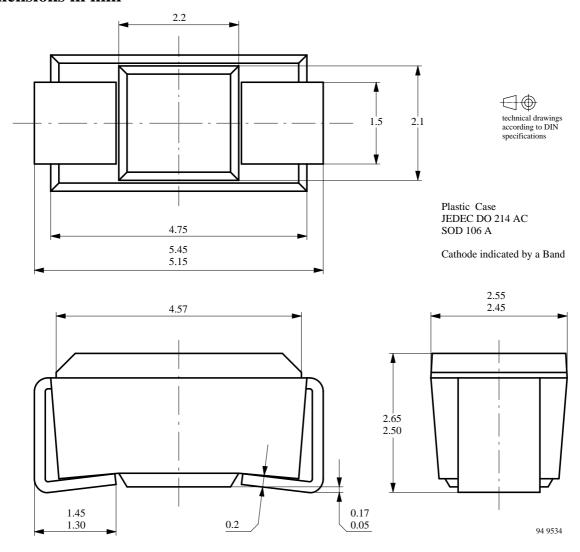


Figure 9. Thermal Response

Dimensions in mm





Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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