

GENERAL PURPOSE OPERATIONAL AMPLIFIER

 μ A741/ μ A741C/SA741C

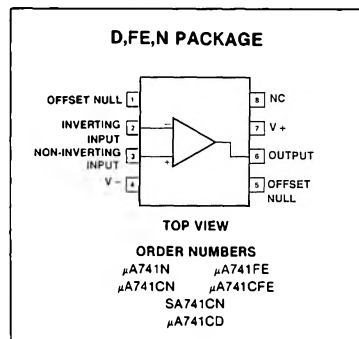
DESCRIPTION

The μ A741 is a high performance operational amplifier with high open loop gain, internal compensation, high common mode range and exceptional temperature stability. The μ A741 is short-circuit protected and allows for nulling of offset voltage.

FEATURES

- Internal frequency compensation
- Short circuit protection
- Excellent temperature stability
- High input voltage range

PIN CONFIGURATION



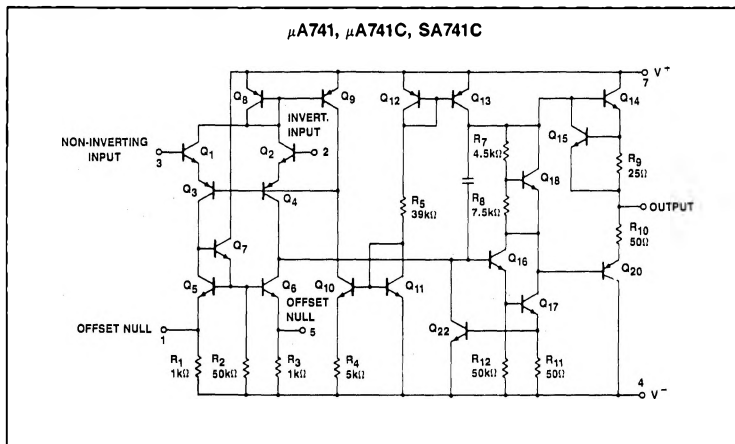
ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING	UNIT
Supply voltage		
μ A741C	± 18	V
μ A741	± 22	V
Internal power dissipation		
N package	500	mW
FE package	1000	mW
Differential input voltage	± 30	V
Input voltage ¹	± 15	V
Output short-circuit duration	Continuous	
Operating temperature range		
μ A741C	0 to +70	°C
SA741C	-40 to +85	°C
μ A741	-55 to +125	°C
Storage temperature range	-65 to +150	°C
Lead temperature (soldering 60sec)	300	°C

NOTE

- For supply voltages less than ± 15 V, the absolute maximum input voltage is equal to the supply voltage.

EQUIVALENT SCHEMATIC



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 μ A741/ μ A741C/SA741CDC ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, $V_S = \pm 15\text{V}$, unless otherwise specified.

PARAMETER	TEST CONDITIONS	μ A741			μ A741C			UNIT
		Min	Typ	Max	Min	Typ	Max	
V_{OS} Offset voltage	$R_S = 10\text{k}\Omega$ $R_S = 10\text{k}\Omega$, over temp.		1.0 1.0 10	5.0 6.0		2.0 10	6.0 7.5	mV mV $\mu\text{V}/^\circ\text{C}$
I_{OS} Offset current	Over temp. $T_A = +125^\circ\text{C}$ $T_A = -55^\circ\text{C}$		20 7.0 20 200	200 200 500		20 200	200 300	nA nA nA nA $\text{pA}/^\circ\text{C}$
$\Delta I_{OS}/\Delta T$								
I_{BIAS} Input bias current	Over temp. $T_A = +125^\circ\text{C}$ $T_A = -55^\circ\text{C}$		80 30 300 1	500 500 1500		80 1	500 800	nA nA nA nA $\text{nA}/^\circ\text{C}$
$\Delta I_B/\Delta T$								
V_{OUT} Output voltage swing	$R_L = 10\text{k}\Omega$ $R_L = 2\text{k}\Omega$, over temp.	± 12 ± 10	± 14 ± 13		± 12 ± 10	± 14 ± 13		V V
A_{VOL} Large signal voltage gain	$R_L = 2\text{k}\Omega$, $V_O = \pm 10\text{V}$ $R_L = 2\text{k}\Omega$, $V_O = \pm 10\text{V}$, over temp.	50 25	200		20 15	200		V/mV V/mV
Offset voltage adjustment range			± 30			± 30		mV
PSRR Supply voltage rejection ratio	$R_S \leq 10\text{k}\Omega$ $R_S \leq 10\text{k}\Omega$, over temp.		10	150		10	150	$\mu\text{V}/\text{V}$ $\mu\text{V}/\text{V}$
CMRR Common mode rejection ratio	Over temp.	70	90					dB dB
I_{CC} Supply current	$T_A = +125^\circ\text{C}$ $T_A = -55^\circ\text{C}$		1.4 1.5 2.0	2.8 2.5 3.3		1.4	2.8	mA mA mA
V_{IN} Input voltage range	(μ A741, over temp.)	± 12	± 13		± 12	± 13		V
R_{IN} Input resistance		0.3	2.0		0.3	2.0		M Ω
P_d Power consumption	$T_A = +125^\circ\text{C}$ $T_A = -55^\circ\text{C}$		50 45 45	85 75 100		50	85	mW mW mW
R_{OUT} Output resistance			75			75		Ω
I_{SC} Output short-circuit current		10	25	60	10	25	60	mA

PARAMETER	TEST CONDITIONS	SA741C			UNIT
		Min	Typ	Max	
V_{OS} Offset voltage	$R_S = 10\text{k}\Omega$ $R_S = 10\text{k}\Omega$, over temp.		2.0	6.0 7.5	mV mV $\mu\text{V}/^\circ\text{C}$
$\Delta V_{OS}/\Delta T$			10		
I_{OS} Offset current	Over temp.		20	200 500	nA nA $\text{pA}/^\circ\text{C}$
$\Delta I_{OS}/\Delta T$			200		
I_{BIAS} Input bias current	Over temp.		80	500 1500	nA nA $\text{nA}/^\circ\text{C}$
$\Delta I_B/\Delta T$			1		
V_{OUT} Output voltage swing	$R_L = 10\text{k}\Omega$ $R_L = 2\text{k}\Omega$, over temp.	± 12 ± 10	± 14 ± 13		V V
A_{VOL} Large signal voltage gain	$R_L = 2\text{k}\Omega$, $V_O = \pm 10\text{V}$ $R_L = 2\text{k}\Omega$, $V_O = \pm 10\text{V}$, over temp.	20 15	200		V/mV V/mV
Offset voltage adjustment range			± 30		mV

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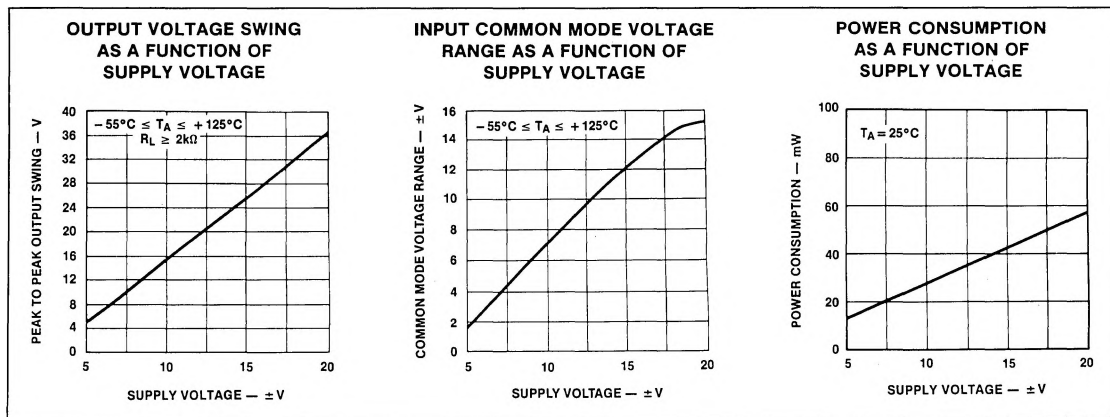
 μ A741/ μ A741C/SA741CDC ELECTRICAL CHARACTERISTICS (Cont'd) $T_A = 25^\circ\text{C}$, $V_S = \pm 15\text{V}$, unless otherwise specified.

PARAMETER		TEST CONDITIONS	SA741C			UNIT
			Min	Typ	Max	
PSRR	Supply voltage rejection ratio	$R_S \leq 10\text{k}\Omega$		10	150	$\mu\text{V/V}$
CMRR	Common mode rejection ratio					dB
V_{IN}	Input voltage range	(μ A741, over temp.)	± 12	± 13		V
	Input resistance		0.3	2.0		$\text{M}\Omega$
P_d	Power consumption			50	85	mW
R_{OUT}	Output resistance			75		Ω
I_{SC}	Output short-circuit current			25		mA

AC ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, $V_S = \pm 15\text{V}$, unless otherwise specified.

PARAMETER	TEST CONDITIONS	μ A741, μ A741C			UNIT
		Min	Typ	Max	
Parallel input resistance	Open loop, $f = 20\text{Hz}$				$\text{M}\Omega$
Parallel input capacitance	Open loop, $f = 20\text{Hz}$		1.4		pF
Unity gain crossover frequency	Open loop		1.0		MHz
Transient response unity gain	$V_{IN} = 20\text{mV}$, $R_L = 2\text{k}\Omega$, $C_L \leq 100\text{pf}$		0.3		μs
Rise time			5.0		%
Overshoot			0.5		$\text{V}/\mu\text{s}$
Slew rate	$C \leq 100\text{pf}$, $R_L \geq 2\text{k}$, $V_{IN} = \pm 10\text{V}$				

TYPICAL PERFORMANCE CHARACTERISTICS

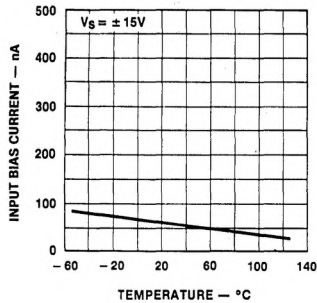


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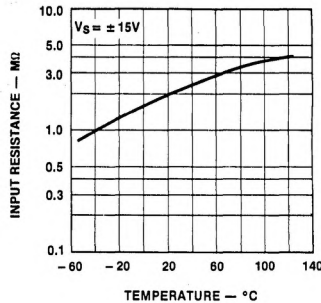
μ A741/ μ A741C/SA741C

TYPICAL PERFORMANCE CHARACTERISTICS (Cont'd)

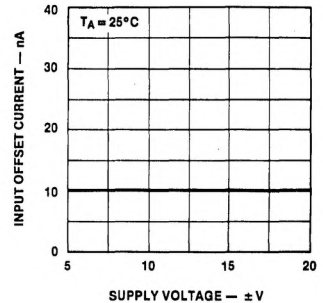
**INPUT BIAS CURRENT
AS A FUNCTION OF
AMBIENT TEMPERATURE**



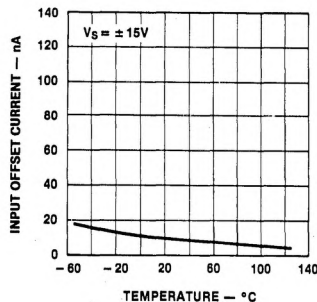
**INPUT RESISTANCE
AS A FUNCTION OF
AMBIENT TEMPERATURE**



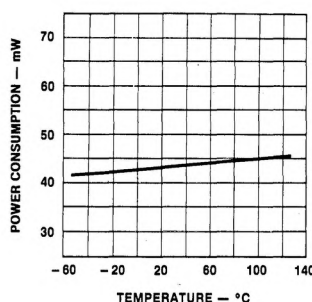
**INPUT OFFSET CURRENT
AS A FUNCTION OF
SUPPLY VOLTAGE**



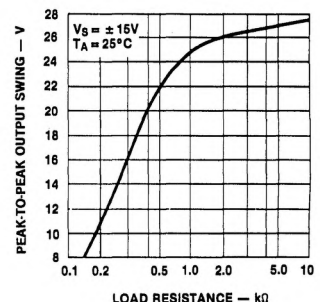
**INPUT OFFSET CURRENT
AS A FUNCTION OF
AMBIENT TEMPERATURE**



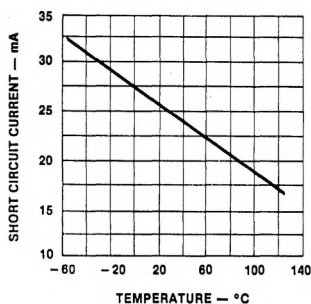
**POWER CONSUMPTION
AS A FUNCTION OF
AMBIENT TEMPERATURE**



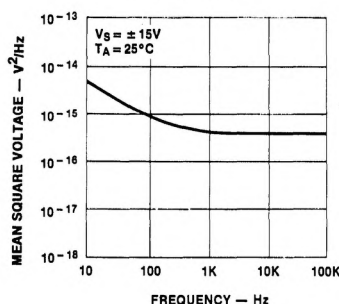
**OUTPUT VOLTAGE SWING
AS A FUNCTION OF
LOAD RESISTANCE**



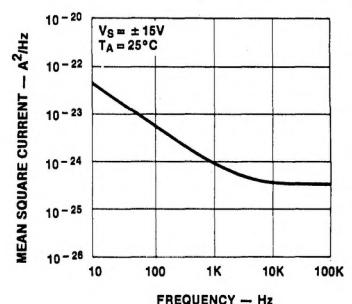
**OUTPUT SHORT-CIRCUIT CURRENT
AS A FUNCTION OF
AMBIENT TEMPERATURE**



**INPUT NOISE VOLTAGE
AS A FUNCTION OF
FREQUENCY**



**INPUT NOISE CURRENT
AS A FUNCTION OF
FREQUENCY**

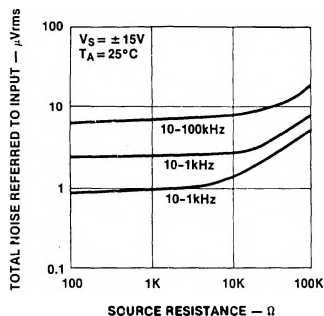


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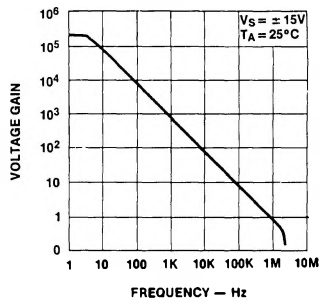
μ A741/ μ A741C/SA741C

TYPICAL PERFORMANCE CHARACTERISTICS (Cont'd)

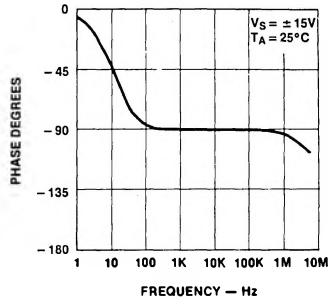
BROADBAND NOISE FOR VARIOUS BANDWIDTHS



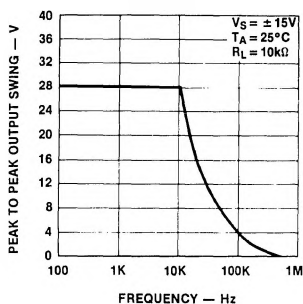
OPEN LOOP VOLTAGE GAIN AS A FUNCTION OF FREQUENCY



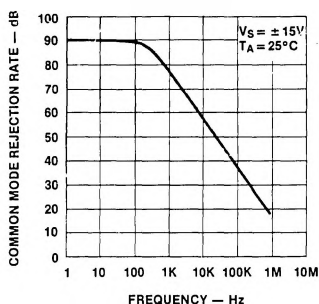
OPEN LOOP PHASE RESPONSE AS A FUNCTION OF FREQUENCY



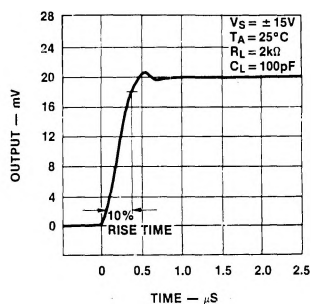
OUTPUT VOLTAGE SWING AS A FUNCTION OF FREQUENCY



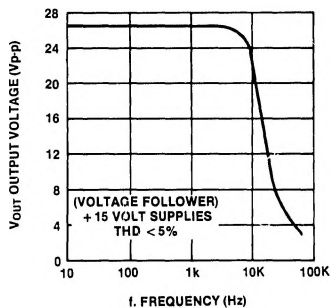
COMMON MODE REJECTION RATIO AS A FUNCTION OF FREQUENCY



TRANSIENT RESPONSE



POWER BANDWIDTH (Large Signal Swing vs Frequency)



VOLTAGE OFFSET NULL

