

PRELIMINARY DATA

18W Hi-Fi AMPLIFIER AND 30W DRIVER

The TDA 2030A is a monolithic IC in Pentawatt $^{\textcircled{R}}$ package intended for use as low frequency class AB amplifier.

With $V_{s max} = 44V$ it is particularly suited for more reliable applications without regulated supply and for 30W driver circuits using low-cost complementary pairs.

The TDA 2030A provides high output current and has very low harmonic and cross-over distortion. Further the device incorporates a short circuit protection system comprising an arrangement for automatically limiting the dissipated power so as to keep the working point of the output transistors within their safe operating area. A conventional thermal shut-down system is also included.

ABSOLUTE MAXIMUM RATINGS

v	Supply voltage	+ 22	
V _i	Input voltage		v
v,	Differential input voltage	± 15	v
I.	Peak oùtput current	3.5	Α
Ptot	Total power dissipation at $T_{case} = 90^{\circ}C$	20	W
T_{stg},T_{j}	Storage and junction temperature	-40 to 150	°C

ORDERING NUMBER: TDA 2030AV

MECHANICAL DATA

Dimensions in mm





CONNECTION DIAGRAM

(top view)



TEST CIRCUIT



THERMAL DATA

R _{th j-case}	Thermal resistance junction-case	max	3	°C/W
		4		



ELECTRICAL CHARACTERISTICS (Refer to the test circuit, $V_s = \pm 16V$, $T_{amb} = 25^{\circ}C$ unless otherwise specified)

	Parameter	Test c	onditions	Min.	Typ.	Max.	Unit
Vs	Supply voltage			± 6		± 22	v
۱d	Quiescent drain current				50	80	mA
۱ _b	Input bias current	V = + 22V			0.2	2	μA
Vos	Input offset voltage	V ₅ - ± 22V			± 2	± 20	mV
los	Input offset current				± 20	± 200	nA
Po	Output power	d = 0.5% f = 40 to 1500	$G_{v} = 26 \text{ dB}$ H_{z} $R_{L} = 4 \Omega$ $R_{L} = 8 \Omega$	15 10	18 12	.).	w
	Dower handwideh	$v_s = 1 19v$	HL= 812	13	16		
BVV		P ₀ = 15W	$R_{\perp} = 4 \Omega$		100		KHZ
SR	Slew Rate				8		V/µsec
Gv	Open loop voltage gain	f = 1 KHz		_	80		dB
Gv	Closed loop voltage gain			25.5	26	26.5	dB
d	Total harmonic distortion	P _o = 0.1 to 14W f = 40 to 15000	R _L = 4 Ω) Hz f = 1 KHz		0.08 0.03		%
		P _o = 0.1 to 9W f = 40 to 15000	RL=8Ω)Hz		0.05		%
d ₂	Second order CCIF intermodulation distortion	P _o = 4W R _L = 4Ω	f ₂ -f ₁ = 1 KHz		0.03		%
d ₃ .	Third order CCIF intermodulation distortion	f ₁ = 14 KHz f ₂ = 15 KHz	2 f ₁ -f ₂ = 13KHz		0.08		%
e _N	Input noise voltage	B = curve A			2		
		B = 22 Hz to 22	2 KHz		3	10	μV
ÎN	Input noise current	B = curve A B = 22 Hz to 22 KHz			50		_ ^
					80	200	рА
S/N	Signal to noise ratio	$R_{L} = 4\Omega$	P _o = 15W		106		-10
		B = curve A	P _o = 1W		94		aB

ELECTRICAL CHARACTERISTICS (continued)

	Parameter	Test	conditions	Min.	Typ.	Max.	Unit
Ri	Input resistance (pin 1)	(open loop)	f = 1 KHz	0.5	5		MΩ
SVR	Supply voltage rejection	R _L = 4 Ω R _g = 22 ΚΩ	G _v = 26 dB f = 100 Hz		54		dB
Τj	Thermal shut-down junction temperature				145		°C

Fig. 1 - Single supply amplifier







Fig. 8 - Split-supply high power amplifier (TDA 2030A + BD907/BD908)



Fig. 9 - Single supply high power amplifier (TDA 2030A + BD907/BD908)





Fig. 10 - P.C. board and component layout for the circuit of fig. 9 (1:1 scale)



Typical performance of the circuit of fig. 9

	Parameter	Test conditi	ons	Min.	Тур.	Max.	Unit
Vs	Supply voltage				36	44	v
۱d	Quiescent drain current	V _s = 36V			50		mA
Po	Output power	d = 0.5%	V _s = 39V		35		w
		f = 40Hz to 15KHz	V _s = 36V		28		
		d = 10%; f = 1KHz	V _s = 39V		44		
		R _L ≃ 4Ω	V _s = 36V		35		w
Gv	Voltage gain	f = 1 KHz		19.5	20	20.5	dB
SR	Slew Rate				8		V/µsec
d	Total harmonic distortion		f = 1KHz		0.02		01
		$P_o = 20W$ f = 40 Hz to 15 KHz			0.05		70
Vi	Input sensitivity	G _v = 20 dB P _o = 20W	f = 1 KHz R _L = 4Ω		890		mV
S/N	Signal to noise ratio	Dise ratio $R_{L} = 4\Omega$			108		dB
		B = curve A	P _o = 4W		100		







Fig. 15 - Typical amplifier whit split power supply

TDA 2030A



Fig. 16 - P.C. board and component layout for the circuit of fig. 15 (1:1 scale)





Fig. 17 – Bridge amplifier whit split power supply ($P_o = 34W$, $V_s = \pm 16V$)



Fig. 18 - P.C. board and component layout for the circuit in fig. 17 (1:1 scale)



Multiway speaker systems and active boxes

Multiway loudspeaker systems provide the best possible acoustic performance since each loudspeaker is specially designed and optimized to handle a limited range of frequencies. Commonly, these loudspeaker systems divide the audio spectrum into two or three bands.

To maintain a flat frequency response over the Hi-Fi audio range the bands covered by each loudspeaker must overlap slightly. Imbalance between the loudspeakers produces unacceptable results therefore it is important to ensure that each unit generates the correct amount of acoustic energy for its segment of the audio spectrum. In this respect it is also important to know the energy distribution of the music spectrum to determine the cutoff frequencies of the crossover filters (see fig. 19). As an example, a 100W three-way system with crossover frequencies of 400 Hz and 3 KHz would require 50W for the woofer, 35W for the midrange unit and 15W for the tweeter.

Both active and passive filters can be used for crossovers but today active filters cost significantly less than a good passive filter using air-cored inductors and non-electrolytic capacitors. In addition, active filters do not suffer from the typical defects of passive filters:

power loss

- increased impedance seen by the loudspeaker (lower damping)
- difficulty of precise design due to variable loudspeaker impedance.



Fig. 20 - Active power filter



Obviously, active crossovers can only be used if a power amplifier is provided for each drive unit.

This makes it particularly interesting and economically sound to use monolithic power amplifiers.

In some applications, complex filters are not really necessary and simple RC low-pass and high-pass networks (6 dB/octave) can be recommended.

The results obtained are excellent because this is the best type of audio filter and the only one free from phase and transient distortion.

The rather poor out of band attenuation of single RC filters means that the loudspeaker must operate linearly well beyond the crossover frequency to avoid distortion.

A more effective solution, named "Active Power Filter" by SGS is shown in fig. 20.

The proposed circuit can realize combined power amplifiers and 12 dB/octave or 18 dB/octave high-pass or low-pass filters.

In practice, at the input pins of the amplifier two equal and in-phase voltages are available, as required for the active filter operation.

The impedance at the pin (-) is of the order of 100Ω , while that of the pin (+) is very high, which is also what was wanted.



The component values calculated for $f_c = 900$ Hz using a Bessel 3rd order Sallen and Key structure are:

$C_1 = C_2 = C_3$	R ₁	R ₂	R ₃
22 nF	8.2 ΚΩ	5.6 ΚΩ	33 ΚΩ

Using this type of crossover filter, a complete 3-way 60W active loudspeaker system is shown in fig. 21. It employs 2nd order Buttherworth filters with the crossover frequencies equal to 300 Hz and 3 KHz. The midrange section consists of two filters, a high pass circuit followed by a low pass network.

With $V_s = 36V$ the output power delivered to the woofer is 25W at d = 0.06% (30W at d = 0.5%). The power delivered to the midrange and the tweeter can be optimized in the design phase taking in account the louspeaker efficiency and impedance ($R_L = 4\alpha$ or 8α).

It is quite common that midrange and tweeter speakers have an efficiency 3 dB higher than woofers.





STS TDA2030A

Musical instruments amplifiers

Another important field of application for active systems is music.

In this area the use of several medium power amplifiers is more convenient than a single high power amplifier, and it is also more reliable.

A typical example (see fig. 22) consist of four amplifiers each driving a low-cost, 4Ω , 12 inch loud-speaker. This application can supply 80 to 160W rms.

Similar output power can be obtained by a single amplifier using the "superbridge" circuit of fig.24.As shown in the diagram of fig. 16 this circuit can supply output power of 120W and more.



Fig. 22 - High power active box for musical instrument

Fig. 23 - Output power vs. supply voltage (application circuit of fig. 24)



Fig. 24 - 120W "superbridge" power amplifier





Transient intermodulation distortion (TIM)

Transient intermodulation distortion is an unfortunate phenomen associated with negative-feedback amplifiers. When a feedback amplifier receives an input signal which rises very steeply, i.e. it contains high-frequency components, the feedback can arrive too late so that the amplifiers overloads and a burst of intermodulation distortion will be produced as in fig. 25. Since transients occur frequently in music this is obviously a problem for the designer of audio amplifiers. Unfortunately, heavy negative feedback is frequently used to reduce the total harmonic distortion of an amplifier, which tends to aggravate the transient intermodulation (TIM) situation. The best known method for the measurement of TIM consists of feeding sine waves superimposed onto square waves, into the amplifier under test. The output spectrum is then examined using a spectrum analyser and compared to the input. This method suffers from serious disadvantages: the accuracy is limited, the measurement is a rather delicate operation and an expensive spectrum analyser is essential. A new approach (see Technical Note 143) applied by SGS to monolithic amplifiers measurement is fast cheap-it requires nothing more sophisticated than an oscilloscope - and sensitive - and it can be used down to the values as low as 0.002% in high power amplifiers. The "inverting-sawtooth" method of measurement is based on the response of an amplifier to a 20 KHz sawtooth waveform. The amplifier has no difficulty following the slow ramp but it cannot follow the fast edge. The output will follow the upper line in fig. 26 cutting of the shaded area and thus increasing the mean level. If this output signal is filtered to remove the sawtooth, a direct voltage remains which indicates the amount of TIM distortion, although it is difficult to measure because it is indistinguishable from the d.c. offset of the amplifier. This problem is neatly avoided in the IS-TIM method by periodically inverting the sawtooth waveform at a low audio frequency as shown in fig. 27. In the case of the sawtooth in fig. 26 the mean level was increased by the TIM distortion, for a sawtooth in the other direction the opposite is true.

Fig. 25 - Overshoot phenomenon in feedback amplifiers



Fig. 26 - 20 KHz sawtooth waveform



Fig. 27 - Inverting sawtooth waveform



The result is an a.c. signal at the output whose peak-to-peak value is the TIM voltage, which can be measured easily with an oscilloscope.

If the peak-to-peak value of the signal and the peak-to-peak of the inverting sawtooth are measured, the TIM can be found very simply from:

$$\mathsf{TIM} = \frac{\mathsf{V}_{\mathsf{out}}}{\mathsf{V}_{\mathsf{sawtooth}}} \cdot 100$$



In fig. 28 the experimental results are shown for the 30W amplifier using the TDA2030A as a driver and a low-cost complementary pair.

The measured performances are perfectly suitable for Hi-Fi systems.

A simple RC filter on the input of the amplifier to limit the maximum signal slope (SS) is an effective way to reduce TIM.

The diagram of fig. 29 originated by SGS can be used to find the Slew-Rate (SR) required for a given output power or voltage and a TIM design target.

For example if an anti-TIM filter with a cutoff at 30 KHz is used and the max. peak-to-peak output voltage is 20V then, referring to the diagram, a Slew-Rate of $6V/\mu S$ is necessary for 0.1% TIM.

As shown Slew-Rates of above $10V/\mu$ S do not contribute to a further reduction in TIM.

Slew-Rates of $100V/\mu$ S are not only useless but also a disadvantage in Hi-Fi audio amplifiers because they tend to turn the amplifier into a radio receiver.







Fig. 29 - TIM design dia-

Power supply

Using monolithic audio amplifier with non-regulated supply voltage it is important to design the power supply correctly. In any working case it must provide a supply voltage less than the maximum value fixed by the IC breakdown voltage.

It is essential to take into account all the working conditions, in particular mains fluctuations and supply voltage variations with and without load.

The TDA 2030A (V_{s max} = 44V) is particularly suitable for substitution of the standard IC power amplifiers (with V_{s max} = 36V) for more reliable applications.

An example, using a simple full-wave rectifier followed by a capacitor filter, is shown in the table and in the diagram of fig. 30.

A regulated supply is not usually used for the power output stages because of its dimensioning must be done taking into account the power to supply in the signal peaks. They are only a small percentage of the total music signal, with consequently large overdimensioning of the circuit.

Even if with a regulated supply higher output power can be obtained (V_s is constant in all working conditions), the additional cost and power dissipation do not usually justify its use. Using non-regulated supplies, there are fewer design restriction. In fact, when signal peaks are present, the capacitor filter acts as a flywheel supplying the required energy.

In average conditions, the continuous power supplied is lower. The music power/continuous power ratio is greater in this case than for the case of regulated supplied, with space saving and cost reduction.



Mains	Secondary	DC output voltage (V _o)				
(220∨)	voltage	I ₀ = 0	I _o = 0.1A	I _o = 1A		
+20%	28.8∨	43.2V	42V	37.5V		
+15%	27.6V	41.4V	40.3∨	35.8V		
+10%	26.4∨	39.6∨	38.5∨	34.2∨		
-	24V	36.2∨	35V	31V		
-10%	21.6V	32.4V	31,5V	27.8V		
- 15%	20.4∨	30.6∨	29.8V	26∨		
-20%	19.2V	28.8V	28∨	24.3V		



SHORT CIRCUIT PROTECTION

The TDA 2030A has an original circuit which limits the current of the output transistors. This function can be considered as being peak power limiting rather than simple current limiting. The TDA 2030A is thus protected against temporary overloads or short circuit. Should the short circuit exist for a longer time, the thermal shut-down protection keeps the junction temperature within safe limits.

THERMAL SHUT-DOWN

The presence of a thermal limiting circuit offers the following advantages:

- An overload on the output (even if it is permanent), or an above limit ambient temperature can be easily supported since the T_i cannot be higher than 150°C.
- 2) The heatsink can have a smaller factor of safety compared with that of a conventional circuit. There is no possibility of device damage due to high junction temperature. If for any reason, the junction temperature increases up to 150°C, the thermal shut-down simply reduces the power dissipation and the current consumption.