### TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT MULTI CHIP

# **TD62M8604AF**

### 8CH LOW SATURATION VOLTAGE SOURCE DRIVER

The TD62M8604AF is Multi Chip IC incorporates 8 low saturation discrete (PNP: 2SA1680) transistors.

This IC is suitable for a battery use motor drive and LED display module applications.

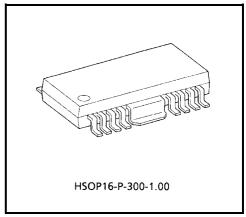
Please observe the thermal condition for using.

#### **FEATURES**

- Suitable for Motor drive circuit and LED display module
- Low Saturation Voltage

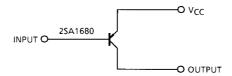
$$V_{CE (sat)} = -0.5 V (Typ.)$$
 at  $I_{C} = -0.5 A$   
 $V_{BE (sat)} = -1.2 V (Max.)$  at  $I_{C} = -1.0 A$ 

• HSOP16 (1 mm pitch) power small package sealed

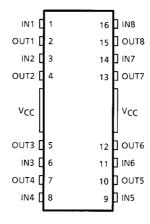


Weight: 0.50 g (Typ.)

### **BLOCK DIAGRAM**



### **PIN CONNECTION (TOP VIEW)**



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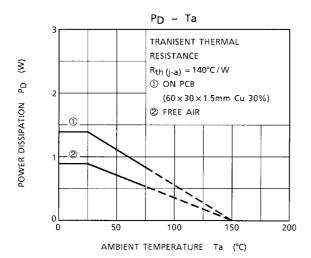
# MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	V <sub>CC</sub>	-50	V	
Breakdown Voltage	V <sub>CBO</sub>	-60	٧	
Breakdown Voltage	V <sub>CEO</sub>	-50	V	
Breakdown Voltage	V <sub>EBO</sub>	-6	V	
Output Current	IO	-2	A / ch	
Base Current	ΙΒ	-0.2	Α	
Power Dissipation	P <sub>D</sub>	900	mW	
Junction Temperature	Tj	150	°C	
Operating Temperature	T <sub>opr</sub>	-40~85	°C	
Storage Temperature	T <sub>stg</sub>	-55~150	°C	

# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Current Gain	h <sub>FE (1)</sub>	_	$V_{CE} = -2 \text{ V}, I_{C} = -0.1 \text{ A}$	120	_	400	-	
	h <sub>FE (2)</sub>		$V_{CE} = -2 \text{ V}, I_{C} = -1.5 \text{ A}$	40	_	_		
Saturation Voltage	V <sub>CE (sat)</sub>	_	$I_C = -1 \text{ A}, I_B = -50 \text{ mA}$	_	_	-0.5	V	
	V <sub>BE (sat)</sub>	_	$I_C = -1 \text{ A}, I_B = -50 \text{ mA}$	_	_	-1.2		
Transition Frequency	f <sub>T</sub>	_	$V_{CE} = -2 \text{ V}, I_{C} = -0.1 \text{ A}$	_	100	_	MHz	
Leakage Current	l <sub>OL</sub>	_	V <sub>CC</sub> = −50 V	_	0	-5	μA	

2



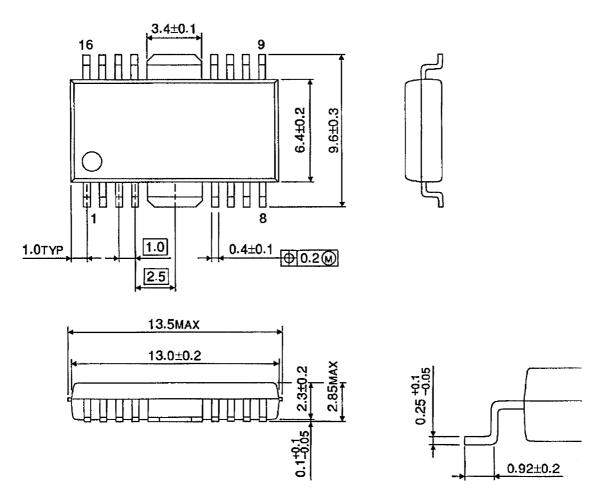
### **PRECAUTIONS for USING**

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line,  $V_{\rm CC}$  and GND line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

### **PACKAGE DIMENSIONS**

HSOP16-P-300-1.00 Unit: mm



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Weight: 0.50 g (Typ.)

## RESTRICTIONS ON PRODUCT USE

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