TOSHIBA Cmos Linear Integrated Circuit Silicon Monolithic

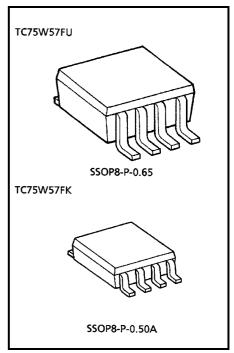
# TC75W57FU,TC75W57FK

#### **Dual Comparator**

TC75W57 is a CMOS type general-purpose dual comparator capable of single power supply operation and using lower supply currents than the conventional bipolar comparators. Its push-pull output can connect directly to local IC's such as TTL and CMOS circuits.

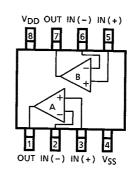
#### Features

- Low supply current:  $I_{DD} = 200 \mu A$  (typ.)
- Single power supply operation
- Wide common mode input voltage range:  $V_{SS}$ - $V_{DD}$ -0.9V
- Push-pull output circuit
- Low input bias current
- Small package

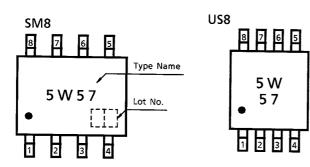


Weight SSOP8-P-0.65: 0.021g (typ.) SSOP8-P-0.50A: 0.01g (typ.)

#### Pin Connection (Top View)



### Marking (Top View)



### Maximum Ratings (Ta = 25°C)

Characterisstic	Symbol	Rating	Ν	
Supply voltage	V <sub>DD</sub> , V <sub>SS</sub>	±3.5 or 7	V	
Differential input voltage	DV <sub>IN</sub>	±7	V	
Input voltage	V <sub>IN</sub>	V <sub>SS</sub> ~V <sub>DD</sub>	V	
Output current	IOUT	±35	mA	
Power dissipation	D-	250 (SM8)	mW	
Power dissipation	PD	200 (US8)	11177	
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Storage temperature	T <sub>stg</sub>	-55~125	°C	

Note: Since this product sometimes brings about latchcap, which is peculiar to CMOS devices, note the following points:

- Don't raise the voltage level of I/O pins beyond  $V_{DD},$  nor lower it below  $V_{SS}.$  Consider the timing for power supply, too.

• Don't let any abnormal noise enter the device.

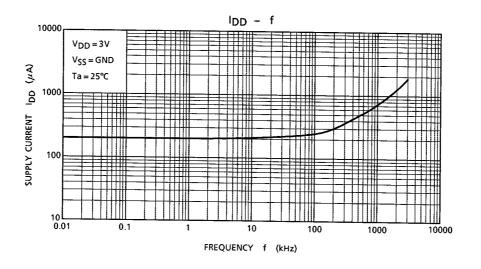
### Electrical Characteristics ( $V_{DD} = 5V$ , $V_{SS} = GND$ , Ta = 25°C)

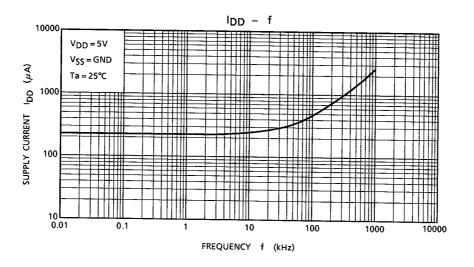
Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V <sub>IO</sub>	_	—	_	±1	±7	mV
Input offset current	I <sub>IO</sub>	_	_	_	1	_	pА
Input bias current	lį	-	—	_	1	_	pА
Common mode input voltage	CMVIN	_	—	0	_	4.1	V
Supply current	I <sub>DD</sub> (Note)	_	—	_	220	440	μA
Voltage gain	G <sub>V</sub>	_	—	_	94	_	dB
Sink current	I <sub>sink</sub>	_	V <sub>OL</sub> = 0.5V	13	25	_	mA
Source current	I <sub>source</sub>	_	V <sub>OH</sub> = 4.5V	9	21	_	mA
Output voltage	V <sub>OL</sub>	_	I <sub>sink</sub> = 5.0mA	_	0.1	0.3	v
	V <sub>OH</sub>	_	I <sub>source</sub> = 5.0mA	4.7	4.9	_	
Operating supply voltage	V <sub>DD</sub>	_	—	1.8	_	7.0	V
Propagation delay time (turn on)	t <sub>PLH</sub> (1)	_	Over drive = 100mV	_	140	_	ns
	t <sub>PLH</sub> (2)	_	TTL step input	_	90	_	
Propagation delay time (turn off)	t <sub>PHL</sub> (1)	_	Over drive = 100mV	_	90	_	ns
	t <sub>PHL</sub> (2)	_	TTL step input	_	70	_	
Response time	t <sub>TLH</sub>	—	Over drive = 100mV	_	11	_	ns
	t <sub>THL</sub>	—	Over drive = 100mV	_	7	_	

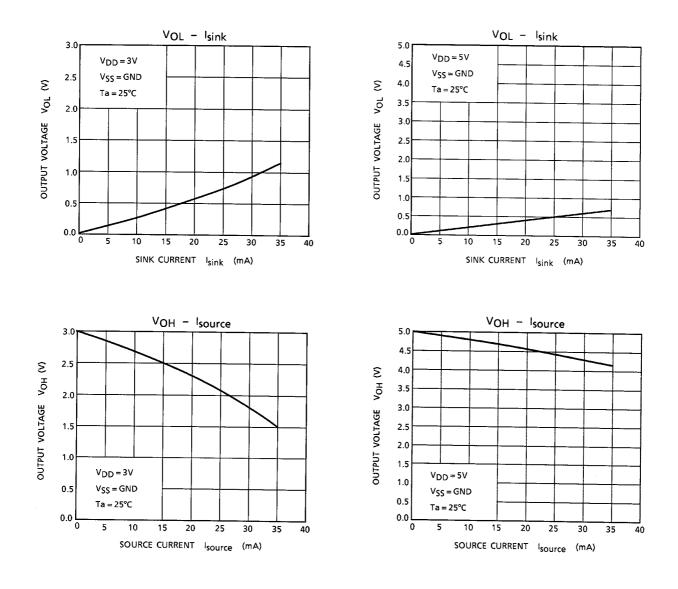
### Electrical Characteristics ( $V_{DD}$ = 3V, $V_{SS}$ = GND, Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Input offset voltage	V <sub>IO</sub>	-	—	_	±1	±7	mV
Input offset current	Ι <sub>ΙΟ</sub>	_	—		1	—	pА
Input bias current	lı	_	—	_	1	—	pА
Common mode input voltage	CMVIN	_	—	0	—	2.1	V
Supply current	I <sub>DD</sub> (Note)	_	—	_	200	400	μA
Sink current	l <sub>sink</sub>	_	V <sub>OL</sub> = 0.5V	6	18	—	mA
Source current	I <sub>source</sub>	_	V <sub>OH</sub> = 2.5V	3	15	—	mA
Output voltage	V <sub>OL</sub>	_	I <sub>sink</sub> = 5.0mA	_	0.15	0.35	V
	V <sub>OH</sub>	_	I <sub>source</sub> = 5.0mA	2.65	2.85	—	
Propagation delay time (turn on)	t <sub>PLH</sub>	_	Over drive = 100mV	_	110	_	ns
Propagation delay time (turn off)	tPHL	_	Over drive = 100mV	_	90	_	ns
Response time	t <sub>TLH</sub>	—	Over drive = 100mV	_	7	—	ns
	t <sub>THL</sub>	—	Over drive = 100mV	_	8	—	

Note: Since this product causes an increase in current consumption with a rise in operational frequency, make sure that power consumption does not exceed the allowable dissipation.



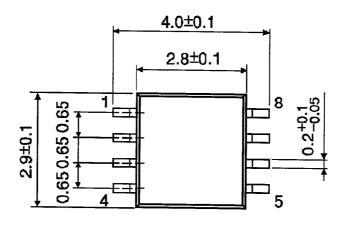


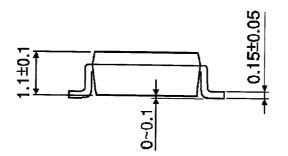


### Package Dimensions

SSOP8-P-0.65

Unit: mm



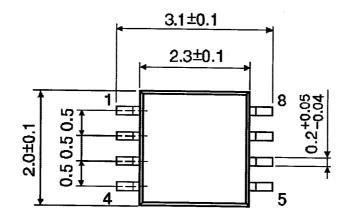


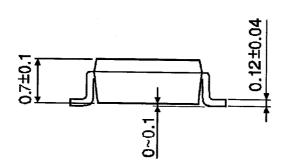
Weight: 0.021g(typ.)

### **Package Dimensions**

SSOP8-P-0.50A

Unit: mm





Weight: 0.01g(typ.)

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