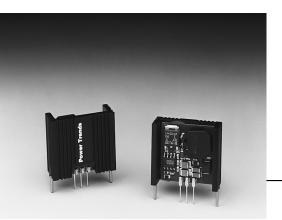
SLTS074A

(Revised 6/30/2000)



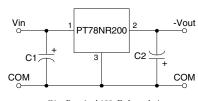
- Negative output from positive input
- Wide Input Range
- Self-Contained Inductor
- Short Circuit Protection
- Over-Temperature Protection
- Fast Transient Response

The PT78NR200 series creates negative output voltage from a posi-

tive input voltage greater than 9V. These easy-to-use, 3-terminal, Integrated Switching Regulators (ISRs) have maximum output power of 10 to 12 watts and a negative output voltage that is laser trimmed. They also have excellent line and load regulation.

The PT78NR200 requires 100 LFM of airflow at its maximum output current.

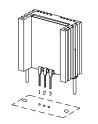
Standard Application



C1 = Required 100µF electrolytic C2 = Required 100µF electrolytic

Pin-Out Information

Pin	Function
1	$+V_{in}$
2	-V _{out}
3	GND



SUGGESTED BEIARD LAYEUT COMPONENT SIDE VIEW Pkg Style 600

Ordering Information

PT78NR2 XX Y
Output Voltage Package Suffix

52 = -5.2 Volts **06** = -6.0 Volts H = Horizontal MountS = Surface MountV = Vertical Mount

12 = -12.0 Volts **15** = -15.0 Volts

Package Styles 600 and 610.)

(For dimensions and PC board layout, see

Specifications

Characteristics (T _a = 25°C unless noted)	Symbols	Conditions	PT78NR200 SERIES			
			Min	Тур	Max	Units
Output Current	I_{o}	Over V_{in} range V_{o} = -5.2V V_{o} = -12.0V	0.1* 0.1*		2.0 1.0	A A
Short Circuit Current	I_{sc}	V_{in} =10 V	_	4×I _{max}	_	Apk
Inrush Current	$I_{ m ir}$ $t_{ m ir}$	V _{in} =10V On start-up	_	4 0.5	_	A mSec
Input Voltage Range	V_{in}	$0.1 \le I_o \le I_{max}$	9	_	15	V
Output Voltage Tolerance	ΔV_{o}	Over V_{in} range T_a = 0°C to +70°C	_	±1.0	±3.0	$%V_{o}$
Line Regulation	Regline	Over V _{in} range	_	±0.5	±1.0	$%V_{o}$
Load Regulation	Reg _{load}	$0.3 \le I_o \le I_{max}$	_	±0.5	±1.0	$% V_{o}$
Vo Ripple/Noise	V_n	$V_{in}=10V, I_o=I_{max}$	_	±2	_	$%V_{o}$
Transient Response (with 100µF output cap)	t _{tr}	50% load change V _o over/undershoot	_	100 5.0	250 —	μSec %V _o
Efficiency	η	$V_{in}=9V, I_{o}=0.5 \times I_{max}, V_{o}=-12V$	_	78	_	%
Switching Frequency	f_{o}	Over V _{in} and I _o ranges	600	650	700	kHz
Absolute Maximum Operating Temperaturte Range	T_a	100 LFM airflow Over V _{in} and I _o Ranges	0	_	+85	°C
Recommended Operating Temperature Range	T_a	100 LFM airflow Over V _{in} and I _o Ranges	0	_	+60**	°C
Thermal Resistance	θ_{ja}	100 LFM airflow	_	35	_	°C/W
Storage Temperature	T_{s}	_	-40	_	+125	°C
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3		500		G's
Mechanical Vibration	_	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board		10		G's
Weight	_	_	_	11		Grams

^{*}ISR will operate down to no load with reduced specifications.

 $\textbf{Note:}\ The\ PT78NR200\ series\ requires\ a\ 100\mu F\ electrolytic\ or\ tantalum\ output\ capacitor\ for\ proper\ operation\ in\ all\ applications.$

^{**}See Thermal Derating chart.

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