1 Amp Plus to Minus Voltage **Integrated Switching Regulator**

SLTS058B

(Revised 8/31/2000)

V = Vertical Mount

S = Surface Mount

H = Horizontal

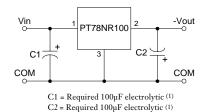
Mount



- Negative output from positive input
- Wide Input Range
- Self-Contained Inductor
- Short Circuit Protection
- Over-Temperature Protection
- Fast Transient Response

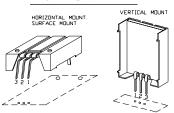
The PT78NR100 Series creates a negative output voltage from a positive input voltage greater than 7V. These easy-to-use, 3-terminal, Integrated Switching Regulators (ISRs) have maximum output power of 5 watts and a negative output voltage that is laser trimmed. They also have excellent line and load regulation.

Standard Application



Pin-Out Information

Pin	Function
1	+ $V_{ m in}$
2	$-V_{ m out}$
3	GND



SUGGESTED BOARD LAYOUT Pkg Style 500

Ordering Information

oraering intorm	ation		
PT78NR1	XX	Υ	
Output Voltage		Packa	age Suffix

03 = -3.0 Volts**05** = -5.0 Volts 52 = -5.2 Volts

06 = -6.0 Volts

07 = -7.0 Volts 08 = -8.0 Volts09 = -9.0 Volts

10 = -10.0 Volts **12** = -12.0 Volts

14 = -13.9 Volts **15** = -15.0 Volts

Specifications

Characteristics			P1			
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	I _o	$\begin{array}{ccc} \text{Over V}_{\text{in}} \text{ range} & V_{\text{o}}\text{=-}5V \\ & V_{\text{o}}\text{=-}6V \\ & V_{\text{o}}\text{=-}7, -8, -9V \\ & V_{\text{o}}\text{=-}10V \\ & V_{\text{o}}\text{=-}12V \\ & V_{\text{o}}\text{=-}13.9, -15V \\ \end{array}$	0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2) 0.05 (2)		1.00 0.8 0.55 0.5 0.40 0.30	A
Short Circuit Current	I_{sc}	$V_{in}=10V$	_	$4\times I_{max}$	_	Apk
Inrush Current	$\begin{array}{c} I_{ir} \\ t_{ir} \end{array}$	V _{in} =10V On start-up	_	4 0.5	_	A mSec
Input Voltage Range	$ m V_{in}$	$\begin{array}{ll} 0.1 \leq I_{o} \leq I_{max} & V_{o}\text{=-}5V \\ V_{o}\text{=-}6, -7, -8, -9V \\ V_{o}\text{=-}10, -12V \\ V_{o}\text{=-}13.9, -15V \end{array}$	7 7 7 7	=	25 21 18 15	V V V
Output Voltage Tolerance	$\Delta { m V_o}$	Over V _{in} range T _a =-20°C to +70°C	_	±1.0	±3.0	%Vo
Line Regulation	Reg _{line}	Over V _{in} range	_	±0.5	±1.0	$%V_{o}$
Load Regulation	Regload	$0.1 \leq I_o \leq I_{max}$	_	±0.5	±1.0	$%V_{o}$
V _o Ripple/Noise	V_n	V_{in} =10V, I_o = I_{max}	_	±2	_	$%V_{o}$
Transient Response (with 100µF output cap)	t _{tr}	50% load change $V_{\rm o}$ over/undershoot	_	100 5.0	250 —	μSec %V _o
Efficiency	η	$V_{in}=10V, I_{o}=0.5 \times I_{max}, V_{o}=-5V$	_	75	_	%
Switching Frequency	f_{0}	Over Vin and Io ranges	600	650	700	kHz
Absolute Maximum Operating Temperaturte Range	T_a	Free Air Convection, (40-60LFM) Over $V_{\rm in}$ Range	-40	_	+85 (3)	°C
Thermal Resistance	θ_{ja}	Free Air Convection, (40-60LFM)	_	45	_	°C/W
Storage Temperature	T_s	_	-40	_	+125	°C
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3	_	500	_	G's
Mechanical Vibration	_	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board	_	5	-	G's
Weight	_	_	_	6.5		Grams

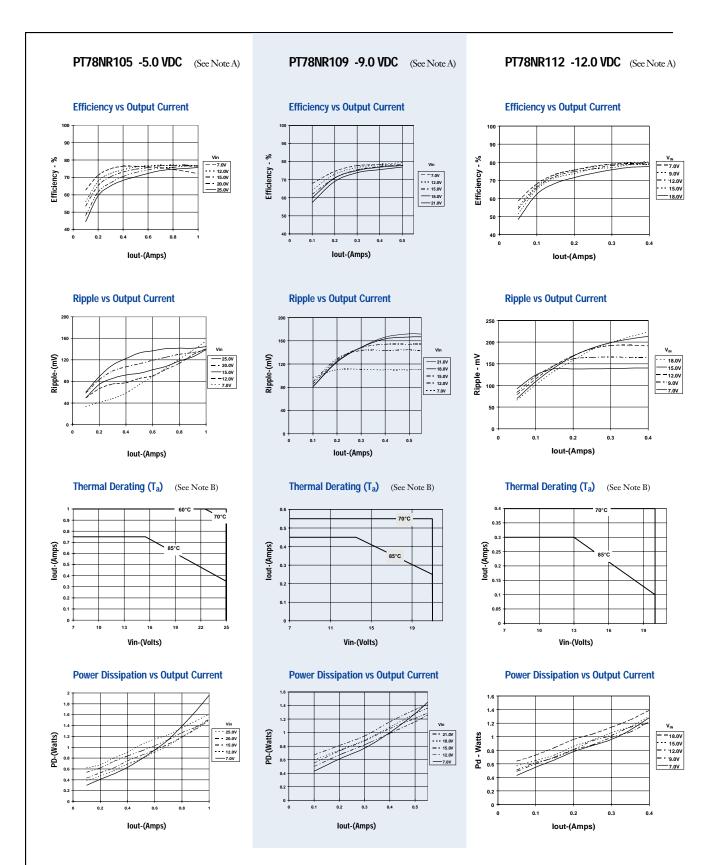
Notes: (1) The PT78NR100 Series requires a 100µF electrolytic or tantalum capacitor at both the input and output for proper operation in all applications. The input capacitor, C_1 must have a ripple current rating ≥ 600 mArms, and an ESR $\leq 0.2\Omega$.

(2) The ISR will operate down to no load with reduced specifications.

(3) See Thermal Derating chart.



1 Amp Plus to Minus Voltage Integrated Switching Regulator



Note A: All data listed in the above graphs, except for derating data, bas been developed from actual products tested at 25°C. This data is considered typical data for the ISR. Note B: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)







8-Feb-2013

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
PT78NR103H		SIP MODULE	EFA	3		TBD	Call TI	Call TI	-40 to 85	()	
PT78NR103S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM	-40 to 85		
PT78NR103V	OBSOLETE	SIP MODULE	EFD	3		TBD	Call TI	Call TI	-40 to 85		
PT78NR105H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR105S	ACTIVE	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			Samples
PT78NR105V	LIFEBUY	SIP MODULE	EFD	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR106H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR106S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR106V	OBSOLETE	SIP MODULE	EFD	3		TBD	Call TI	Call TI			
PT78NR107H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR107S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR107V	LIFEBUY	SIP MODULE	EFD	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR108S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR108ST	LIFEBUY	SIP MODULE	EFC	3		TBD	Call TI	Call TI			
PT78NR108V	LIFEBUY	SIP MODULE	EFD	3		TBD	Call TI	Call TI			
PT78NR109H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR109S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR109V	LIFEBUY	SIP MODULE	EFD	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR112H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			





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Orderable Device	Status	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Top-Side Markings	Samples
PT78NR112S		SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM		(+)	
PT78NR112ST	LIFEBUY	SIP MODULE	EFC	3	200	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR112V	LIFEBUY	SIP MODULE	EFD	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR115H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR115S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR115V	LIFEBUY	SIP MODULE	EFD	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR152H	LIFEBUY	SIP MODULE	EFA	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			
PT78NR152S	LIFEBUY	SIP MODULE	EFC	3	25	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM			
PT78NR152V	LIFEBUY	SIP MODULE	EFD	3	25	Pb-Free (RoHS)	Call TI	N / A for Pkg Type			

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes. **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.



PACKAGE OPTION ADDENDUM

8-Feb-2013

(4) Only one of markings shown within the brackets will appear on the physical device.

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