Signetics

Linear Products

NE5533/5533A NE/SA/SE5534/5534A

Dual and Single Low Noise Op Amp

Product Specification

DESCRIPTION

The 5533/5534 are dual and single high-performance low noise operational amplifiers. Compared to other operational amplifiers, such as TL083, they show better noise performance, improved output drive capability and considerably higher small-signal and power bandwidths.

This makes the devices especially suitable for application in high quality and professional audio equipment, in instrumentation and control circuits and telephone channel amplifiers. The op amps are internally compensated for gain equal to, or higher than, three. The frequency response can be optimized with an external compensation capacitor for various applications (unity gain amplifier, capacitive load, slew rate, low overshoot, etc.) If very low noise is of prime importance, it is recommended that the 5533A/5534A version be used which has guaranteed noise specifications.

FEATURES

- **Small-signal bandwidth: 10MHz**
- **Output drive capability: 600Ω, $10V_{RMS}$ at $V_S = \pm 18V$**
- **Input noise voltage: $4nV/\sqrt{Hz}$**
- **DC voltage gain: 100000**
- **AC voltage gain: 6000 at 10kHz**
- **Power bandwith: 200kHz**
- **Slew rate: 13V/μs**
- **Large supply voltage range: ±3 to ±20V**
- **5534 MIL-STD processing available**

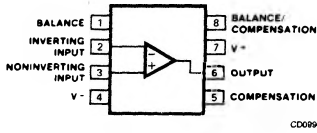
APPLICATIONS

- **Audio equipment**
- **Instrumentation and control circuits**
- **Telephone channel amplifiers**
- **Medical equipment**

PIN CONFIGURATIONS

NE/SA/SE5534/5534A

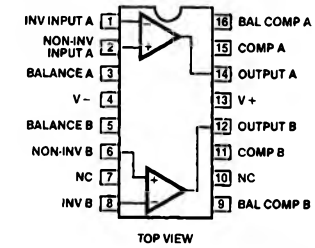
D, FE, N Packages



CD08900S

NE5533/5533A

N Package

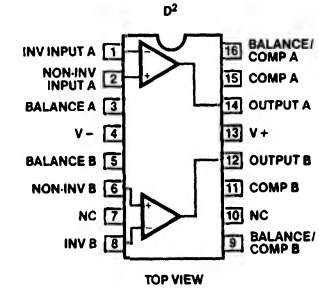


TOP VIEW

CD13740S

NE5533/5533A

D Package



TOP VIEW

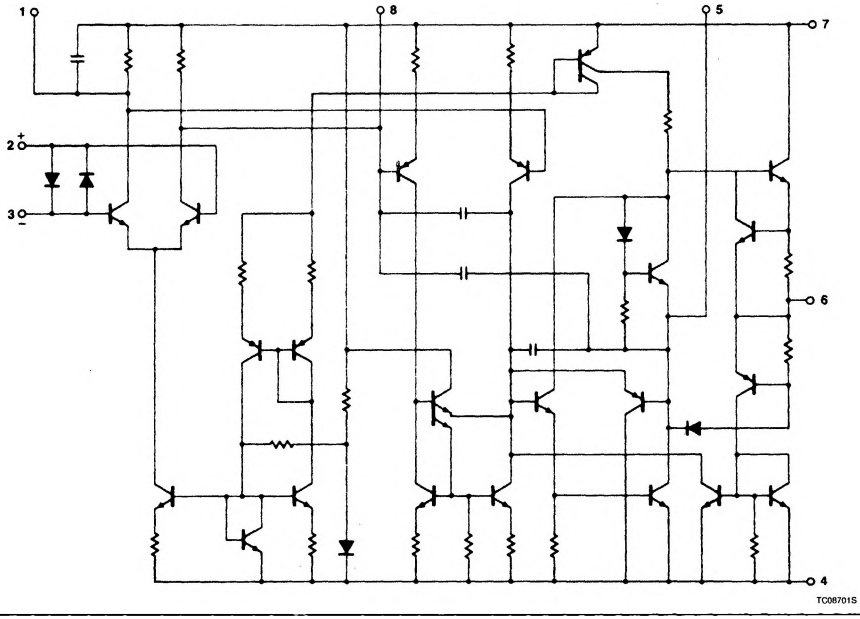
CD11390S

NOTE:
This device may not be symboled in standard format.

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE
14-Pin Plastic DIP	0 to +70°C	NE5533N
16-Pin Plastic SO package	0 to +70°C	NE5533AD
14-Pin Plastic DIP	0 to +70°C	NE5533AN
16-Pin Plastic SO package	0 to +70°C	NE5533D
8-Pin Plastic SO package	0 to +70°C	NE5534D
8-Pin Hermetic Cerdip	0 to +70°C	NE5534FE
8-Pin Plastic DIP	0 to +70°C	NE5534N
8-Pin Plastic SO package	0 to +70°C	NE5534AD
8-Pin Hermetic Cerdip	0 to +70°C	NE5534AFE
8-Pin Plastic DIP	0 to +70°C	NE5534AN
8-Pin Plastic DIP	−40°C to +85°C	SA5534N
8-Pin Plastic SO package	−40°C to +85°C	SA5534AD
8-Pin Plastic DIP	−40°C to +85°C	SA5534AN
8-Pin Hermetic Cerdip	−55°C to +125°C	SE5534AFE
8-Pin Plastic DIP	−55°C to +125°C	SE5534N
8-Pin Hermetic Cerdip	−55°C to +125°C	SE5534AFE
8-Pin Plastic DIP	−55°C to +125°C	SE5534AN

EQUIVALENT SCHEMATIC



ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V_S	Supply voltage	± 22	V
V_{IN}	Input voltage	± V supply	V
V_{DIFF}	Differential input voltage[1]	± 0.5	V
T_A	Operating temperature range		
	SE	−55 to +125	°C
	SA	−40 to +85	°C
	NE	0 to +70	°C
T_{STG}	Storage temperature range	−65 to +150	°C
T_J	Junction temperature	150	°C
P_D	Power dissipation at 25°C[2]		
	5533D	1350	mW
	5533N	1500	mW
	5534D	750	mW
	5534FE	800	mW
	5534N	1150	mW
	Output short-circuit duration[3]	Indefinite	
T_{SOLD}	Lead soldering temperature (10sec max)	300	°C

NOTES:

1. Diodes protect the inputs against over voltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6V. Maximum current should be limited to ± 10mA.
2. For operation at elevated temperature, derate packages based on the following junction-to-ambient thermal resistance:
   - 8-pin ceramic DIP 150°C/W
   - 8-pin plastic DIP 105°C/W
   - 8-pin plastic SO 160°C/W
   - 14-pin plastic DIP 80°C/W
   - 16-pin plastic SO 90°C/W
3. Output may be shorted to ground at V_S = ± 15V, T_A = 25°C. Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

Dual and Single Low Noise Op Amp

NE5533/5533A NE/SA/SE5534/5534A

DC ELECTRICAL CHARACTERISTICS $T_A = 25°C$, $V_S = \pm 15V$, unless otherwise specified.[1, 2, 3]

SYMBOL	PARAMETER	TEST CONDITIONS	SE5534/5534A			NE5533/5533A NE/SA5534/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
V_{OS}	Offset voltage			0.5	2		0.5	4	mV
		Over temperature			3			5	mV
$\Delta V_{OS}/\Delta T$				5			5		µV/°C
I_{OS}	Offset current			10	200		20	300	nA
		Over temperature			500			400	nA
$\Delta I_{OS}/\Delta T$				200			200		pA/°C
I_B	Input current			400	800		500	1500	nA
		Over temperature			1500			2000	nA
$\Delta I_B/\Delta T$				5			5		nA/°C
I_{CC}	Supply current per op amp			4	6.5		4	8	mA
		Over temperature			9			10	mA
V_{CM}	Common mode input range		± 12	± 13		± 12	± 13		V
CMRR	Common mode rejection ratio		80	100		70	100		dB
PSRR	Power supply rejection ratio			10	50		10	100	µV/V
A_{VOL}	Large-signal voltage gain	$R_L \geq 600\Omega$, $V_O = \pm 10V$	50	100		25	100		V/mV
		Over temperature	25			15			V/mV
V_{OUT}	Output swing	$R_L \geq 600\Omega$	± 12	± 13		± 12	± 13		V
		Over temperature	± 10	± 12		± 10	± 12		V
		$R_L \geq 600\Omega$, $V_S = \pm 18V$	± 15	± 16		± 15	± 16		V
		$R_L \geq 2k\Omega$	± 13	± 13.5		± 13	± 13.5		V
		Over temperature	± 12	± 12.5		± 12	± 12.5		V
R_{IN}	Input resistance		50	100		30	100		kΩ
I_{SC}	Output short circuit current			38			38		mA

NOTES:
1. For NE5533/5533A/5534/5534A, $T_{MIN} = 0°C$, $T_{MAX} = 70°C$.
2. For SE5534/5534A, $T_{MIN} = -55°C$, $T_{MAX} = +125°C$.
3. For SA5534/5534A, $T_{MIN} = -40°C$, $T_{MAX} = +125°C$.

AC ELECTRICAL CHARACTERISTICS $T_A = 25°C$, $V_S = \pm 15V$, unless otherwise specified.

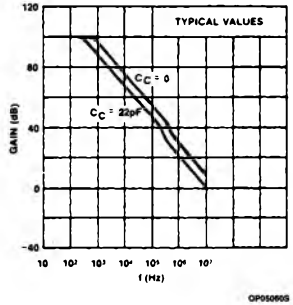
SYMBOL	PARAMETER	TEST CONDITIONS	SE5534/5534A			NE5533/5533A NESA5534/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
R_{OUT}	Output resistance	A_V = 30dB closed-loop f = 10kHz, R_L = 600Ω, C_C = 22pF		0.3			0.3		Ω
	Transient response	Voltage-follower, V_{IN} = 50mV R_L = 600Ω, C_C = 22pF, C_L = 100pF							
t_R	Rise time			20			20		ns
	Overshoot			20			20		%
	Transient response	V_{IN} = 50mV, R_L = 600Ω C_C = 47pF, C_L = 500pF							
t_R	Rise time			50			50		ns
	Overshoot			35			35		%
A_V	Gain	f = 10kHz, C_C = 0 f = 10kHz, C_C = 22pF		6 2.2			6 2.2		V/mV V/mV
GBW	Gain bandwidth product	C_C = 22pF, C_L = 100pF		10			10		MHz
SR	Slew rate	C_C = 0 C_C = 22pF		13 6			13 6		V/μs V/μs
	Power bandwidth	V_{OUT} = ± 10V, C_C = 0 V_{OUT} = ± 10V, C_C = 22pF V_{OUT} = ± 14V, R_L = 600Ω C_C = 22pF, V_{CC} = ± 18V		200 95 70			200 95 70		kHz kHz kHz

ELECTRICAL CHARACTERISTICS $T_A = 25°C$, $V_S = 15V$, unless otherwise specified.

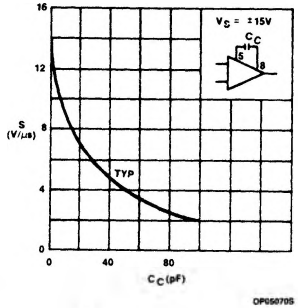
SYMBOL	PARAMETER	TEST CONDITIONS	5533/5534			5533A/5534A			UNIT
			Min	Typ	Max	Min	Typ	Max	
V_{NOISE}	Input noise voltage	f_O = 30Hz f_O = 1kHz		7 4			5.5 3.5	7 4.5	nV/√Hz nV/√Hz
I_{NOISE}	Input noise current	f_O = 30Hz f_O = 1kHz		2.5 0.6			1.5 0.4		pA/√Hz pA/√Hz
	Broadband noise figure	f = 10Hz – 20kHz, R_S = 5kΩ					0.9		dB
	Channel separation	f = 1kHz, R_S = 5kΩ		110			110		dB

TYPICAL PERFORMANCE CHARACTERISTICS

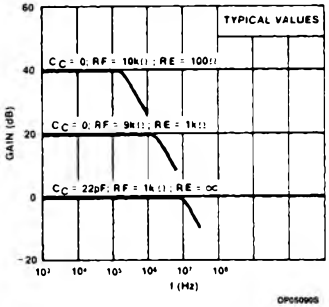
Open-Loop Frequency Response



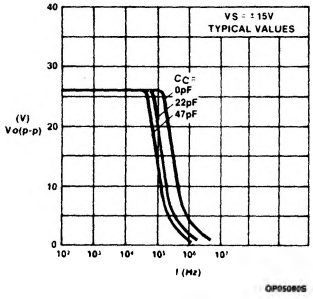
Slew Rate as a Function of Compensation Capacitance



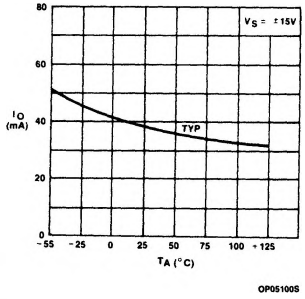
Closed-Loop Frequency Response



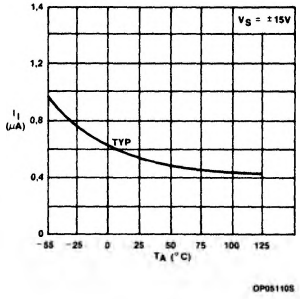
Large-Signal Frequency Response



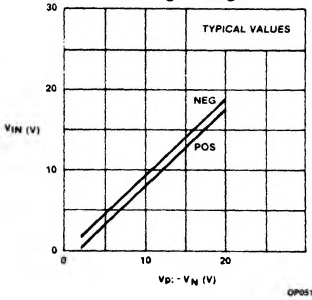
Output Short-Circuit Current



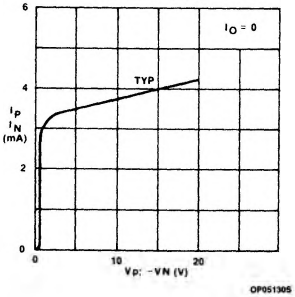
Input Bias Current



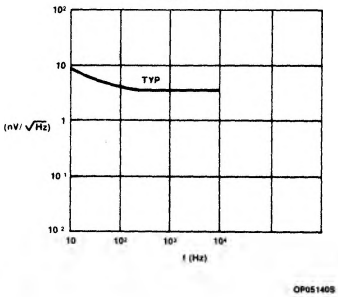
Input Common-Mode Voltage Range



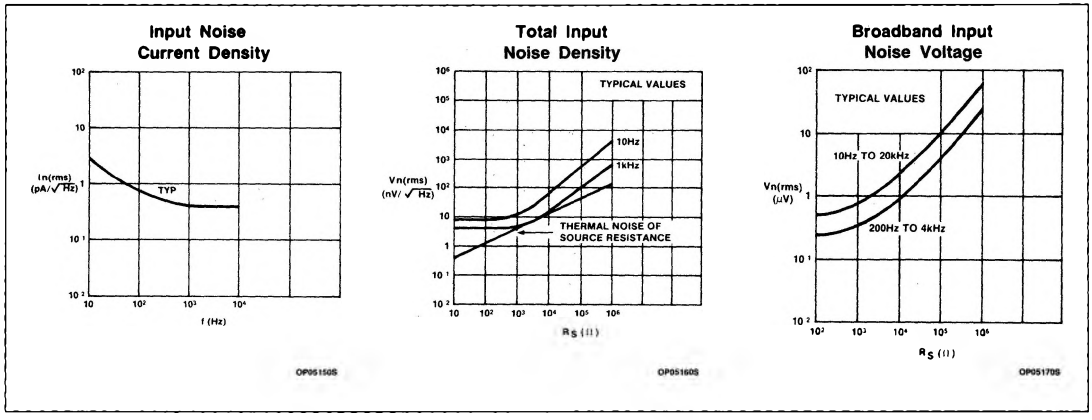
Supply Current per Op Amp



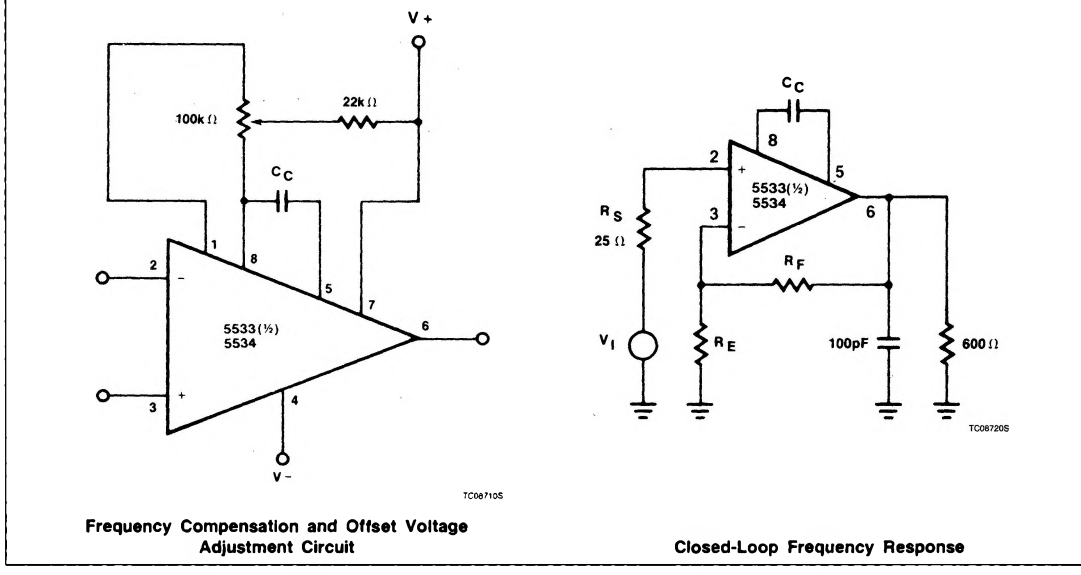
Input Noise Voltage Density



TYPICAL PERFORMANCE CHARACTERISTICS (Continued)



TEST LOAD CIRCUITS



Frequency Compensation and Offset Voltage Adjustment Circuit

Closed-Loop Frequency Response

NOISE TEST BLOCK DIAGRAM