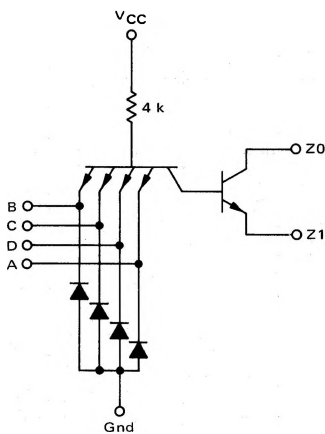


MC5400/7400 series

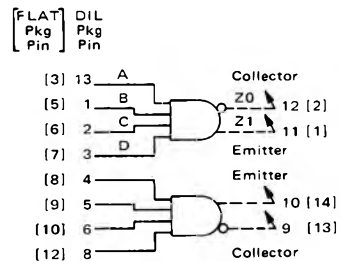
MC5460 • MC7460

Add Suffix F for TO-86 ceramic package (Case 607).
 Suffix L for TO-116 ceramic package (Case 632).
 Suffix P for TO-116 plastic package (Case 605) MC7460 only.



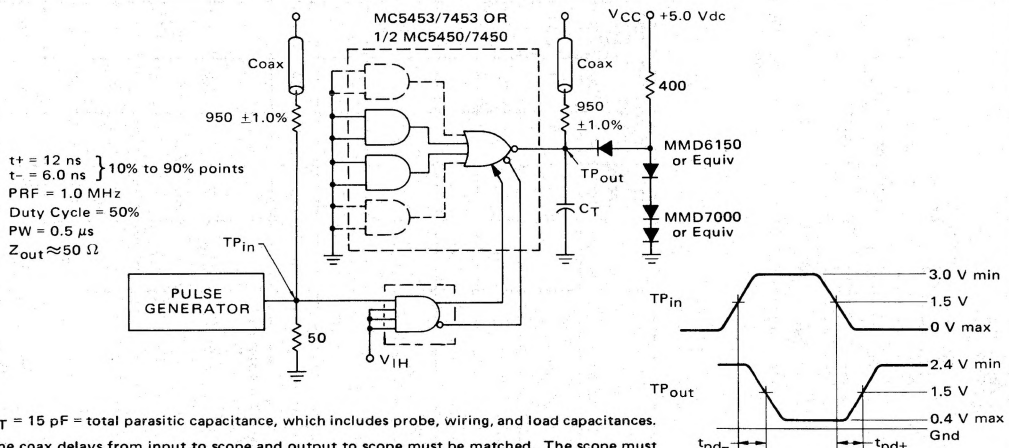
V_{CC} = Pin 14 [4]
Gnd = Pin 7 [11]

This device consists of two 4-input OR expanders for use with the AND-OR-INVERT gates. A maximum of four expander gates can be added to the MC5450/7450 or MC5453/7453 expandable gates without seriously affecting their operation.



Input Loading Factor = 1
Full output loading factor of the expandable gate is maintained.
Total Power Dissipation = 8.0 mW typ/pkg
Propagation Delay Time = 5.0 ns typ

SWITCHING TIME TEST CIRCUIT AND WAVEFORMS

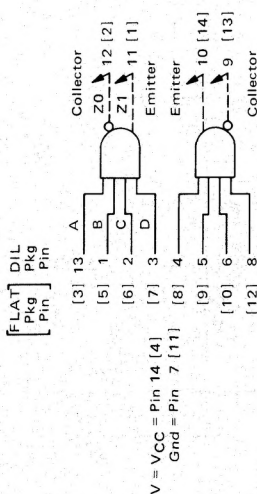


CT = 15 pF = total parasitic capacitance, which includes probe, wiring, and load capacitances. The coax delays from input to scope and output to scope must be matched. The scope must be terminated in 50-ohm impedance. The 950-ohm resistor and the scope termination impedance constitute a 20:1 attenuator probe. Coax shall be CT-070-50 or equivalent.

MC5460, MC7460 (continued)

ELECTRICAL CHARACTERISTICS

Test procedures are shown for only one expander. The other expander is tested in the same manner. Further, test procedures are shown for only one input of the expander being tested. To complete testing, sequence through remaining inputs.

[illegible]

* Resistor to ground.

[†]Resistor to VCCL.

See test circuit.