

DUAL DIFFERENTIAL COMPARATOR

SENSE AMPLIFIERS

MCI711C

... designed for use in level detection, low level sensing, and memory applications.

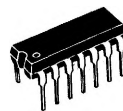
Lead 5 connected
to case



CASE 71A
"G" SUFFIX



CASE 72
(TO-91)
"F" SUFFIX



CASE 93
(TO-116)
"P" SUFFIX

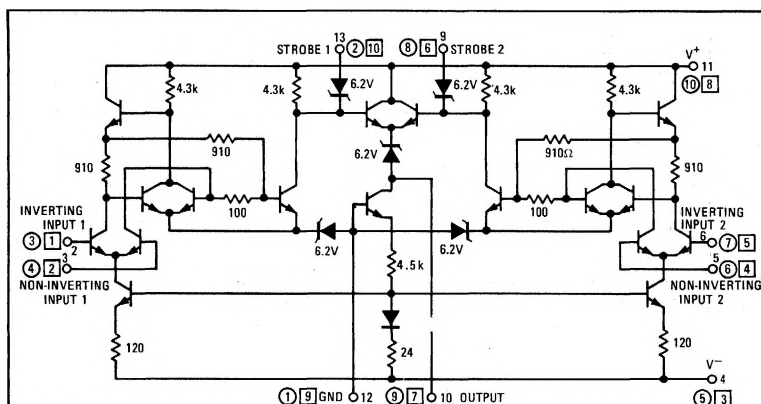
Typical Amplifier Features:

- Differential Input
Input Offset Voltage = 1.0 mV
Offset Voltage Drift = 5.0 $\mu\text{V}/^\circ\text{C}$
- Fast Response Time – 40 ns
- Output Compatible with All Saturating Logic Forms
 $V_{\text{out}} = +4.5 \text{ V}$ to -0.5 V typical
- Low Output Impedance – 200 ohms

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

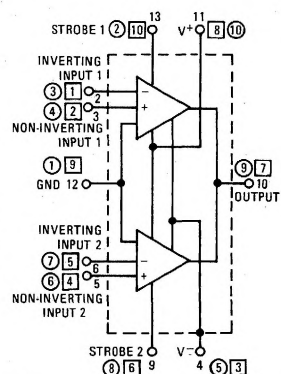
Rating	Symbol	Value	Unit
Power Supply Voltage	V^+ V^-	+14 -7.0	Vdc
Differential Input Signal	V_{in}	± 5.0	Volts
Common Mode Input Swing	CMV_{in}	± 7.0	Volts
Peak Load Current	I_L	50	mA
Power Dissipation (package limitation)	P_D		
Metal Can Derate above $T_A = 25^\circ\text{C}$		680 4.6	mW mW/ $^\circ\text{C}$
Flat Package Derate above $T_A = 25^\circ\text{C}$		500 3.3	mW mW/ $^\circ\text{C}$
Plastic Package Derate above $T_A = 25^\circ\text{C}$		400 3.3	mW mW/ $^\circ\text{C}$
Operating Temperature Range	T_A	0 to +75	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +150 -65 to +125	$^\circ\text{C}$

CIRCUIT SCHEMATIC



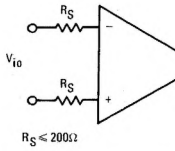
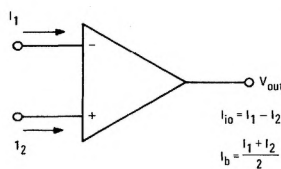
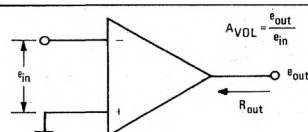
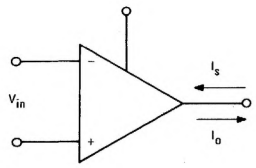
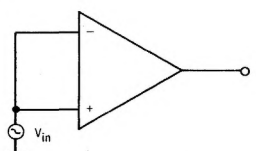
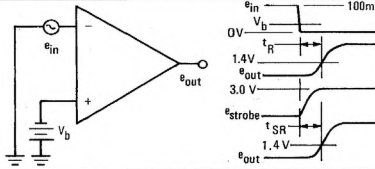
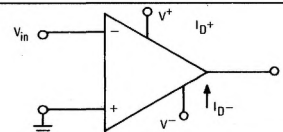
Number at end of terminal is pin number for plastic package. □ contains pin number for flat package. ○ contains pin number for metal can package.

EQUIVALENT CIRCUIT



MC1711C (continued)

ELECTRICAL CHARACTERISTICS (each comparator) $V^+ = +12 \text{ Vdc}$, $V^- = -6.0 \text{ Vdc}$, $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic Definitions	Characteristic	Symbol	Min	Typ	Max	Unit
 <p>V_{io}</p> <p>$R_S \leq 200\Omega$</p> <p>$V_{out} = 1.4 \text{ Vdc} @ 25^\circ\text{C}$ $V_{out} = 1.5 \text{ Vdc} @ 0^\circ\text{C}$ $V_{out} = 1.2 \text{ Vdc} @ +70^\circ\text{C}$</p>	Input Offset Voltage $CMV_{in} = 0 \text{ Vdc}$, $T_A = +25^\circ\text{C}$ $CMV_{in} \neq 0 \text{ Vdc}$, $T_A = +25^\circ\text{C}$ $CMV_{in} = 0 \text{ Vdc}$, $T_A = 0 \text{ to } +70^\circ\text{C}$ $CMV_{in} \neq 0 \text{ Vdc}$, $T_A = 0 \text{ to } +70^\circ\text{C}$	V_{io}	-	1.0	5.0	mVdc
	Temperature Coefficient of Input Offset Voltage	TC_{Vio}	-	5.0	-	$\mu\text{V}/^\circ\text{C}$
 <p>$I_{io} = I_1 - I_2$ $I_b = \frac{I_1 + I_2}{2}$</p>	Input Offset Current $V_{out} = 1.4 \text{ Vdc}$, $T_A = +25^\circ\text{C}$ $V_{out} = 1.5 \text{ Vdc}$, $T_A = 0^\circ\text{C}$ $V_{out} = 1.2 \text{ Vdc}$, $T_A = +70^\circ\text{C}$	I_{io}	-	0.5	15	μA dc
	Input Bias Current $V_{out} = 1.4 \text{ Vdc}$, $T_A = +25^\circ\text{C}$ $V_{out} = 1.5 \text{ Vdc}$, $T_A = 0^\circ\text{C}$ $V_{out} = 1.2 \text{ Vdc}$, $T_A = +70^\circ\text{C}$	I_b	-	25	100	μA dc
 <p>$A_{VOL} = \frac{e_{out}}{e_{in}}$</p>	Voltage Gain $T_A = +25^\circ\text{C}$ $T_A = -55 \text{ to } +125^\circ\text{C}$	A_{VOL}	700 500	1500 -	-	V/V
	Output Resistance	R_{out}	-	200	-	ohms
 <p>V_{in}</p> <p>I_s I_o</p>	Differential Voltage Range	V_{in}	± 5.0	-	-	Vdc
	Positive Output Voltage $V_{in} \geq 10 \text{ mVdc}$, $0 \leq I_o \leq 0.5 \text{ mA}$	V_{OH}	2.5	3.2	5.0	Vdc
	Negative Output Voltage $V_{in} \leq -10 \text{ mVdc}$	V_{OL}	-1.0	-0.5	0	Vdc
	Strobed Output Level $V_{strobe} \leq 0.3 \text{ Vdc}$	$V_{OL(st)}$	-1.0	-	0	Vdc
	Output Sink Current $V_{in} \leq -10 \text{ mV}$, $V_{out} \geq 0$	I_S	0.5	0.8	-	mA dc
 <p>V_{in}</p>	Strobe Current $V_{strobe} = 100 \text{ mVdc}$	I_{st}	-	1.2	2.5	mA dc
	Input Common Mode Range $V^- = -7.0 \text{ Vdc}$	CM_{Vin}	± 5.0	-	-	Volts
 <p>e_{in}, V_b, e_{out}, t_R, t_{SR}</p>	Response Time $V_b = 5.0 \text{ mV} + V_{io}$	t_R	-	40	-	ns
	Strobe Release Time	t_{SR}	-	12	-	ns
 <p>V_{in}, V^+, V^-, I_{D+}, I_{D-}</p>	Power Supply Current $V_{out} \leq 0 \text{ Vdc}$	I_{D+} I_{D-}	-	8.6 3.9	-	mA dc
	Power Consumption		-	130	200	mW

TYPICAL CHARACTERISTICS

FIGURE 1 – VOLTAGE TRANSFER CHARACTERISTICS

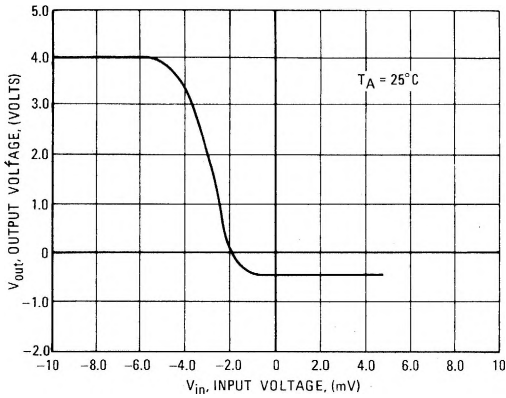


FIGURE 2 – INPUT BIAS CURRENT versus TEMPERATURE

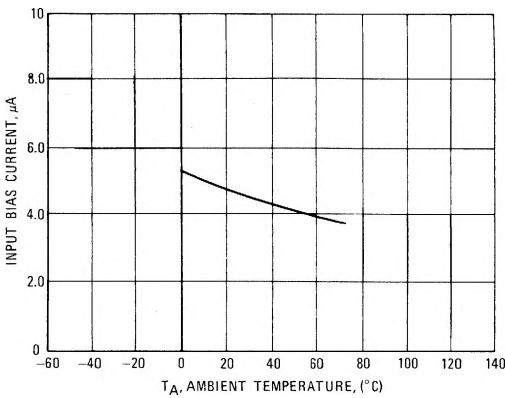


FIGURE 3 – VOLTAGE GAIN versus TEMPERATURE

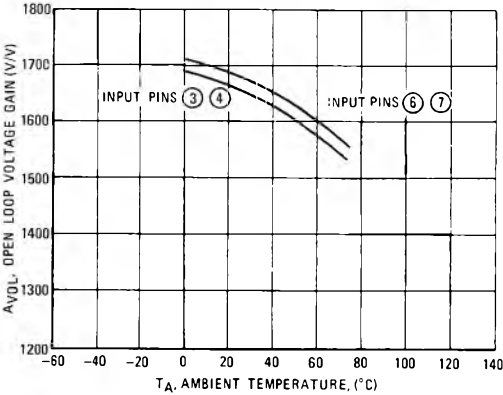


FIGURE 4 – RESPONSE TIME FOR VARIOUS INPUT OVERDRIVES

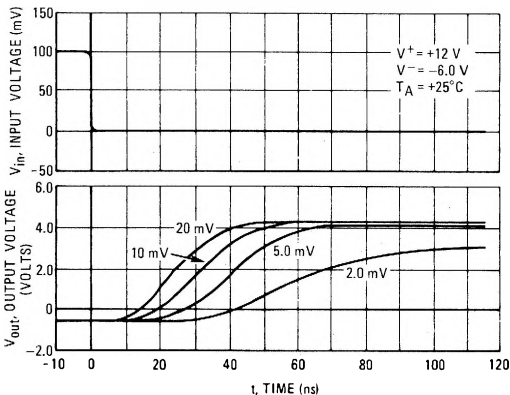


FIGURE 5 – VOLTAGE GAIN VARIATION WITH POWER SUPPLY VOLTAGE

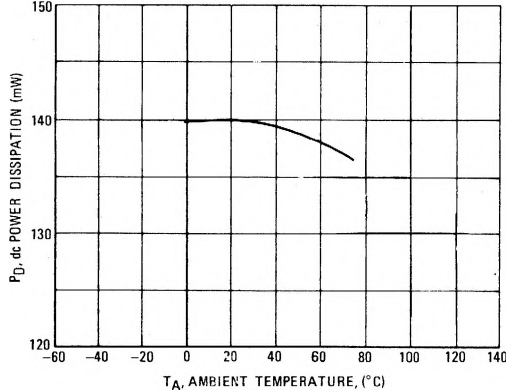
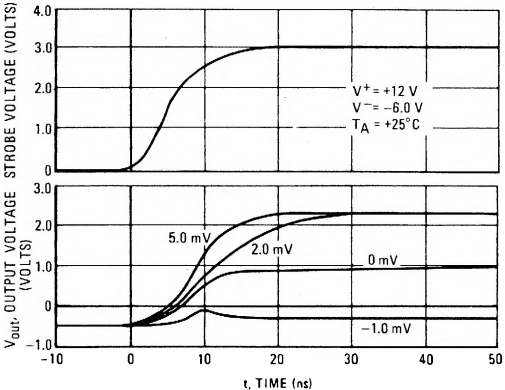


FIGURE 6 – STROBE RELEASE TIME FOR VARIOUS INPUT OVERDRIVES



MC1711C (continued)

FIGURE 7 – COMMON MODE PULSE RESPONSE

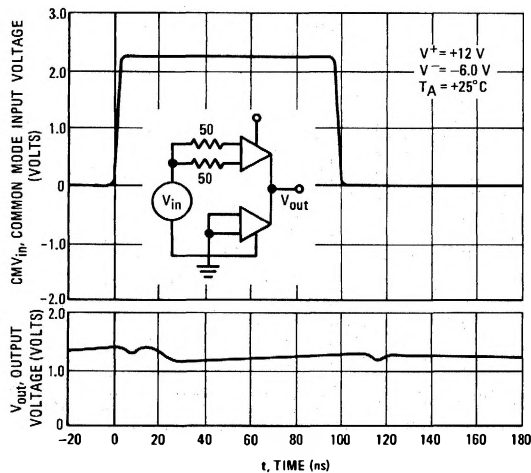


FIGURE 8 – OUTPUT PULSE STRETCHING WITH CAPACITIVE LOADING

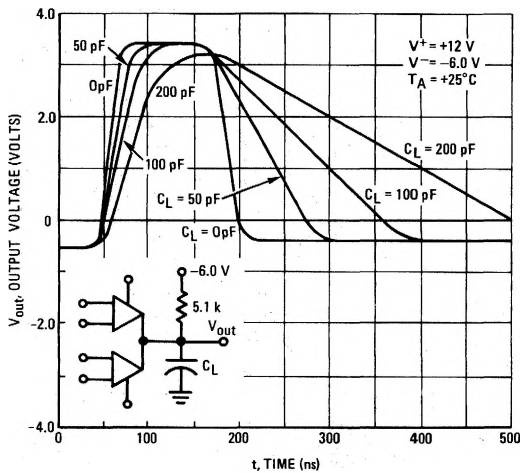


FIGURE 9 – SERIES RESISTANCE versus MRTL FAN-OUTS

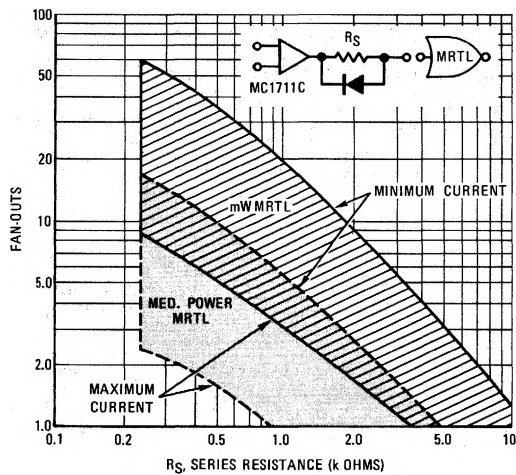


FIGURE 10 – FAN-OUT CAPABILITY WITH MDTL OR MTTL OUTPUT SWING

