

MC1413, MC1413B, NCV1413B

High Voltage, High Current Darlington Transistor Arrays

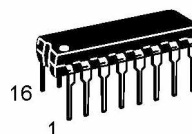
The seven NPN Darlington connected transistors in these arrays are well suited for driving lamps, relays, or printer hammers in a variety of industrial and consumer applications. Their high breakdown voltage and internal suppression diodes insure freedom from problems associated with inductive loads. Peak inrush currents to 500 mA permit them to drive incandescent lamps.

The MC1413, B with a 2.7 k Ω series input resistor is well suited for systems utilizing a 5.0 V TTL or CMOS Logic.

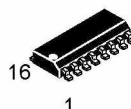


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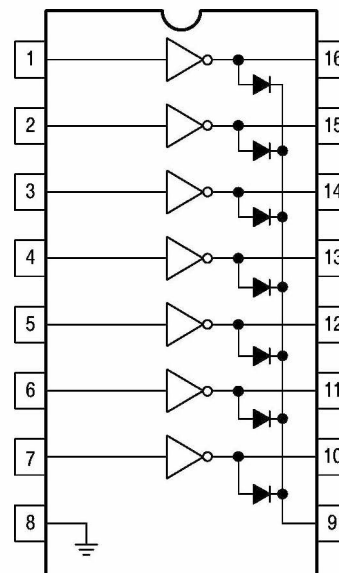


PDIP-16
P SUFFIX
CASE 648



SO-16
D SUFFIX
CASE 751B

PIN CONNECTIONS



(Top View)

ORDERING INFORMATION

Device	Package	Shipping
MC1413D	SO-16	48 Units/Rail
MC1413DR2	SO-16	2500 Tape & Reel
MC1413P	PDIP-16	25 Units/Rail
MC1413BD	SO-16	48 Units/Rail
MC1413BDR2	SO-16	2500 Tape & Reel
MC1413BP	PDIP-16	25 Units/Rail
NCV1413BDR2	SO-16	2500 Tape & Reel

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 2839 of this data sheet.

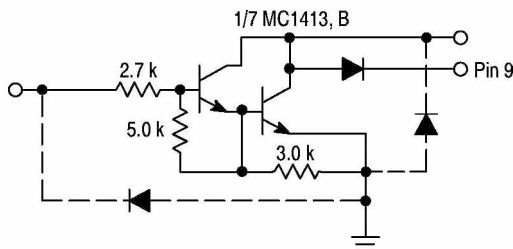


Figure 1. Representative Schematic Diagram

MC1413, MC1413B, NCV1413B

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, and rating apply to any one device in the package, unless otherwise noted.)

Rating	Symbol	Value	Unit
Output Voltage	V_O	50	V
Input Voltage	V_I	30	V
Collector Current – Continuous	I_C	500	mA
Base Current – Continuous	I_B	25	mA
Operating Ambient Temperature Range MC1413 MC1413B NCV1413B	T_A	–20 to +85 –40 to +85 –40 to +125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	–55 to +150	$^\circ\text{C}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Thermal Resistance, Junction–to–Ambient Case 648, P Suffix Case 751B, D Suffix	$R\theta_{JA}$	67 100	$^\circ\text{C/W}$
Thermal Resistance, Junction–to–Case Case 648, P Suffix Case 751B, D Suffix	$R\theta_{JC}$	22 20	$^\circ\text{C/W}$

NOTE: ESD data available upon request.

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Output Leakage Current ($V_O = 50\text{ V}$, $T_A = +85^\circ\text{C}$) ($V_O = 50\text{ V}$, $T_A = +25^\circ\text{C}$)	I_{CEX}	– –	– –	100 50	μA
Collector–Emitter Saturation Voltage ($I_C = 350\text{ mA}$, $I_B = 500\text{ }\mu\text{A}$) ($I_C = 200\text{ mA}$, $I_B = 350\text{ }\mu\text{A}$) ($I_C = 100\text{ mA}$, $I_B = 250\text{ }\mu\text{A}$)	$V_{\text{CE(sat)}}$	– – –	1.1 0.95 0.85	1.6 1.3 1.1	V
Input Current – On Condition ($V_I = 3.85\text{ V}$)	$I_{\text{I(on)}}$	–	0.93	1.35	mA
Input Voltage – On Condition ($V_{\text{CE}} = 2.0\text{ V}$, $I_C = 200\text{ mA}$) ($V_{\text{CE}} = 2.0\text{ V}$, $I_C = 250\text{ mA}$) ($V_{\text{CE}} = 2.0\text{ V}$, $I_C = 300\text{ mA}$)	$V_{\text{I(on)}}$	– – –	– – –	2.4 2.7 3.0	V
Input Current – Off Condition ($I_C = 500\text{ }\mu\text{A}$, $T_A = 85^\circ\text{C}$)	$I_{\text{I(off)}}$	50	100	–	μA
DC Current Gain ($V_{\text{CE}} = 2.0\text{ V}$, $I_C = 350\text{ mA}$)	h_{FE}	1000	–	–	–
Input Capacitance	C_i	–	15	30	pF
Turn–On Delay Time (50% E_i to 50% E_o)	t_{on}	–	0.25	1.0	μs
Turn–Off Delay Time (50% E_i to 50% E_o)	t_{off}	–	0.25	1.0	μs
Clamp Diode Leakage Current ($V_R = 50\text{ V}$)	I_R	– –	– –	50 100	μA
Clamp Diode Forward Voltage ($I_F = 350\text{ mA}$)	V_F	–	1.5	2.0	V

NOTE: NCV1413B $T_{\text{low}} = -40^\circ\text{C}$, $T_{\text{high}} = +125^\circ\text{C}$. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control.

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Typical Performance Curves – $T_A = 25^\circ\text{C}$

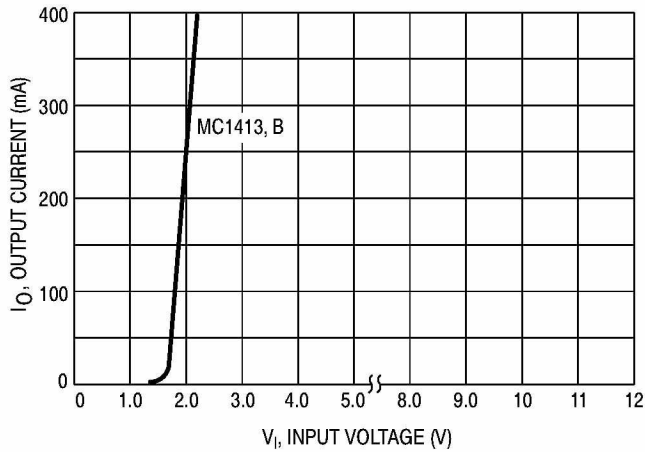


Figure 2. Output Current versus Input Voltage

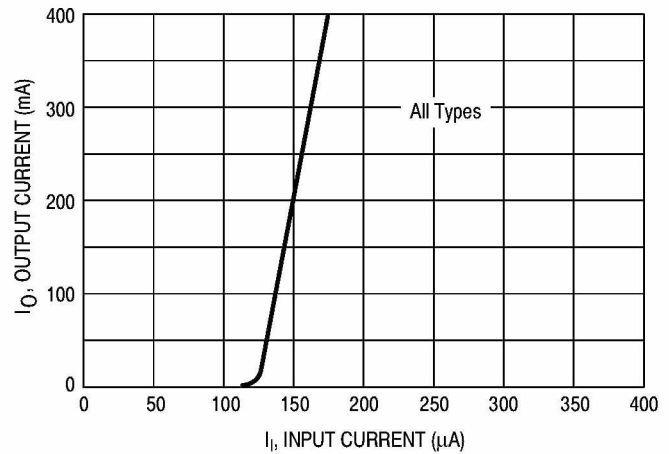


Figure 3. Output Current versus Input Current

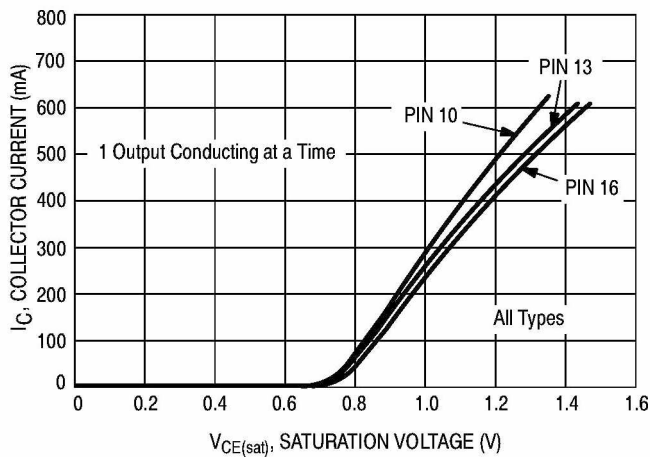


Figure 4. Typical Output Characteristics

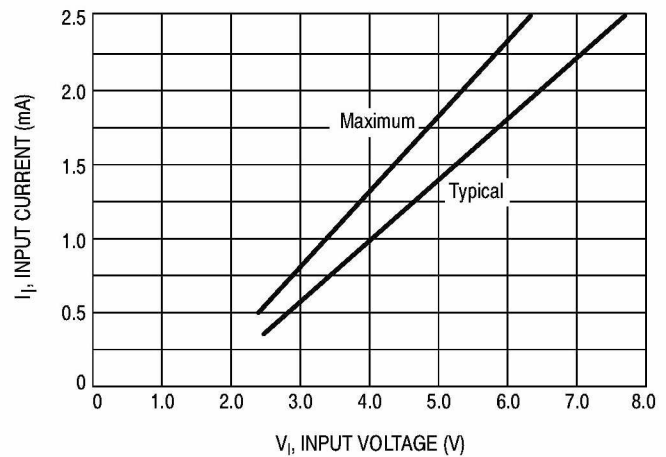


Figure 5. Input Characteristics – MC1413, B

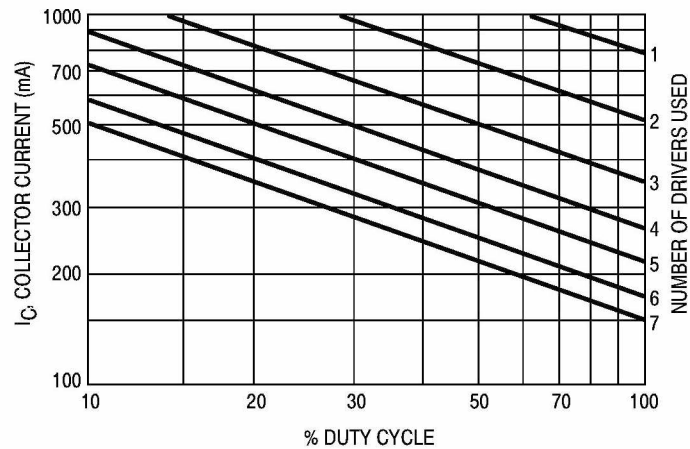
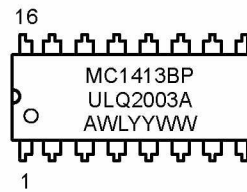
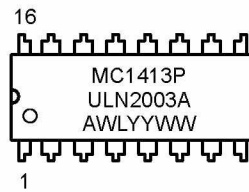


Figure 6. Maximum Collector Current
versus Duty Cycle
(and Number of Drivers in Use)

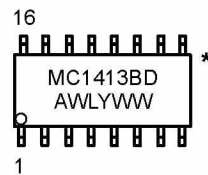
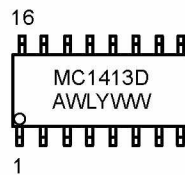
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MARKING DIAGRAMS

PDIP-16 P SUFFIX CASE 648



SO-16 D SUFFIX CASE 751B



A = Assembly Location
WL = Wafer Lot
YY, Y = Year
WW = Work Week

*This marking diagram also applies to NCV1413B.