

LMC555 CMOS Timer

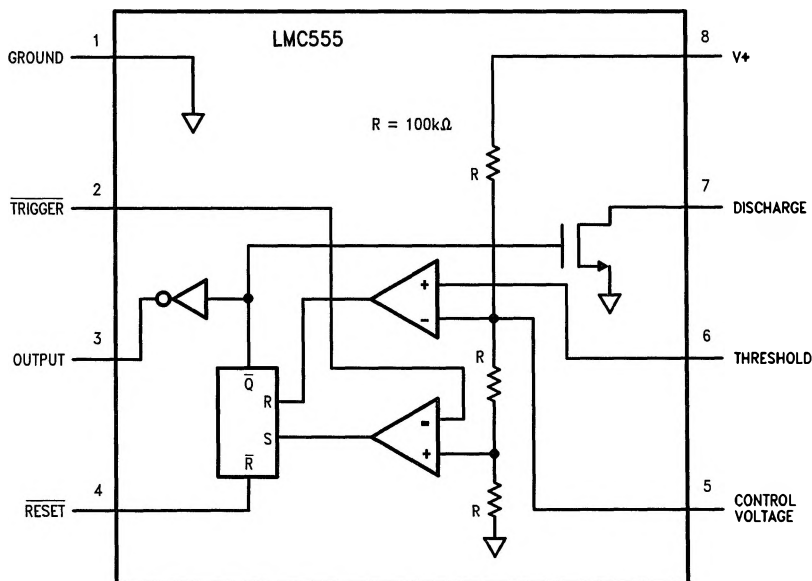
General Description

The LMC555 is a CMOS version of the industry standard 555 series general purpose timers. It offers the same capability of generating accurate time delays and frequencies but with much lower power dissipation and supply current spikes. When operated as a one-shot, the time delay is precisely controlled by a single external resistor and capacitor. In the astable mode the oscillation frequency and duty cycle are accurately set by two external resistors and one capacitor. The use of National Semiconductor's LCMOST™ process extends both the frequency range and low supply capability.

Features

- Less than 1 mW typical power dissipation at 5V supply
- 3 MHz astable frequency capability
- 1.5V supply operating voltage guaranteed
- Output fully compatible with TTL and CMOS logic at 5V supply
- Tested to -10 mA, +50 mA output current levels
- Reduced supply current spikes during output transitions
- Extremely low reset, trigger, and threshold currents
- Excellent temperature stability
- Pin-for-pin compatible with 555 series of timers

Block and Connection Diagrams



TL/H/8669-1

(Pinouts for Molded and Metal Can Packages are identical)

Order Number LMC555CH, LMC555CM or LMC555CN
See NS Package Number H08C, M08A or N08E

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage, V_S	15V
Input Voltages, V_2, V_4, V_5, V_6	-0.3V to $V_S + 0.3V$
Output Voltages, V_3, V_7	15V
Output Current I_3, I_7	100 mA
Operating Temperature Range	-40°C to +85°C*
Storage Temperature Range	-65°C to +150°C

Soldering Information

Dual-In-Line Package	
Soldering (10 seconds)	260°C
Small Outline Package	
Vapor Phase (60 seconds)	215°C
Infrared (15 seconds)	220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

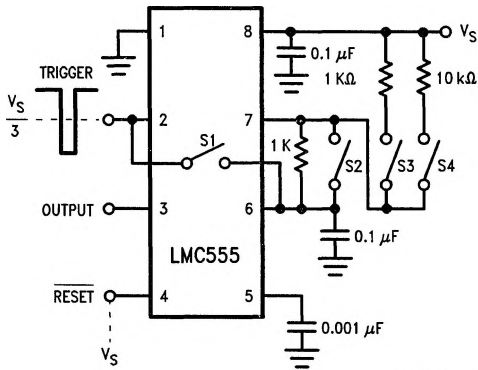
Electrical Characteristics

Test Circuit, $T = 25^\circ\text{C}$, all switches open, $\overline{\text{RESET}}$ to V_S unless otherwise noted

Symbol	Parameter	Conditions	Min	Typ	Max	Units (Limits)
I ₈	Supply Current	$V_S = 1.5V$ $V_S = 5V$ $V_S = 12V$		50 100 150	150 250 400	μA
V ₅	Control Voltage	$V_S = 1.5V$ $V_S = 5V$ $V_S = 12V$	0.8 2.9 7.4	1.0 3.3 8.0	1.2 3.8 8.6	V
V ₇	Discharge Saturation Voltage	$V_S = 1.5V, I_7 = 1\text{ mA}$ $V_S = 5V, I_7 = 10\text{ mA}$		75 150	150 300	mV
V _{3L}	Output Voltage (Low)	$V_S = 1.5V, I_3 = 1\text{ mA}$ $V_S = 5V, I_3 = 8\text{ mA}$ $V_S = 12V, I_3 = 50\text{ mA}$		0.2 0.3 1.0	0.4 0.6 2.0	V
V _{3H}	Output Voltage (High)	$V_S = 1.5V, I_3 = -0.25\text{ mA}$ $V_S = 5V, I_3 = -2\text{ mA}$ $V_S = 12V, I_3 = -10\text{ mA}$	1.0 4.4 10.5	1.25 4.7 11.3		V
V ₂	Trigger Voltage	$V_S = 1.5V$ $V_S = 12V$	0.4 3.7	0.5 4.0	0.6 4.3	V
I ₂	Trigger Current	$V_S = 5V$		10		pA
V ₄	Reset Voltage	$V_S = 1.5V$ $V_S = 12V$	0.4 0.4	0.7 0.75	1.0 1.1	V
I ₄	Reset Current	$V_S = 5V$		10		pA
I ₆	Threshold Current	$V_S = 5V$		10		pA
I ₇	Discharge Leakage	$V_S = 12V$		1.0	100	nA
t	Timing Accuracy	SW 2, 4 Closed $V_S = 1.5V$ $V_S = 5V$ $V_S = 12V$	0.9 1.0 1.0	1.1 1.1 1.1	1.25 1.20 1.25	ms
$\Delta t/\Delta V_S$	Timing Shift with Supply	$V_S = 5V \pm 1V$		0.3		%/V
$\Delta t/\Delta T$	Timing Shift with Temperature	$V_S = 5V$ $-40^\circ\text{C} \leq T \leq +85^\circ\text{C}$		75		ppm/°C
f _A	Astable Frequency	SW 1, 3 Closed $V_S = 12V$	4.0	4.8	5.6	kHz
f _{MAX}	Maximum Frequency	Max. Freq. Test Circuit, $V_S = 5V$		3.0		MHz
t _R , t _F	Output Rise and Fall Times	Max. Freq. Test Circuit $V_S = 5V, C_L = 10\text{ pF}$		15		ns
t _{PD}	Trigger Propagation Delay	$V_S = 5V$, Measure Delay from Trigger to Output		100		ns

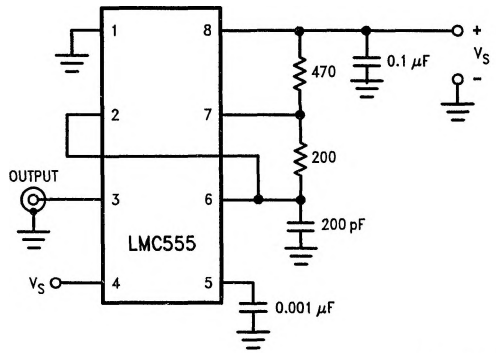
* Refer to RETSC555X drawing for specifications of military LMC555H version.

Test Circuit



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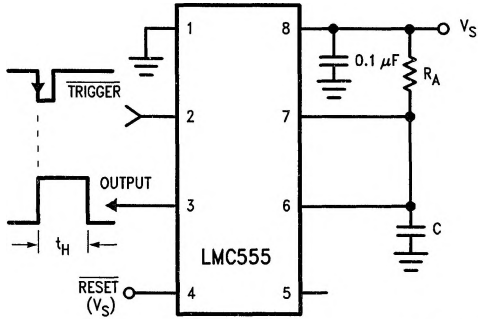
Maximum Frequency Test Circuit



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Typical Applications

Monostable (One-Shot)



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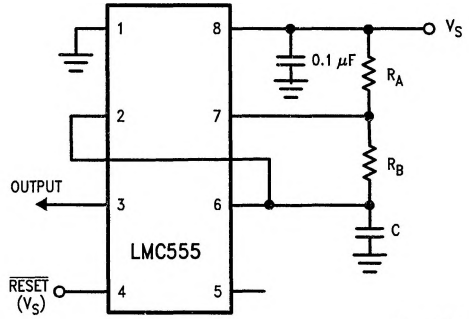
$t_H = 1.1 R_A C$ (Gives time that output is high following trigger)

RESET overrides TRIGGER, which can override THRESHOLD. Therefore, the trigger pulse must be shorter than the desired t_H .

The minimum trigger pulse width is 20 ns.

The minimum reset pulse width is 400 ns.

Variable Duty Cycle Oscillator

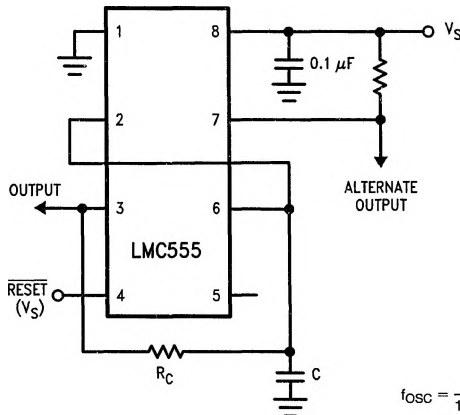


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$$f_{osc} = \frac{1.44}{(R_A + 2R_B)C}$$

$$\text{Duty Cycle} = \frac{R_B}{R_A + 2R_B} \quad (\text{Gives fraction of total period that output is low})$$

50% Duty Cycle Oscillator



$$f_{osc} = \frac{1}{1.4R_C C}$$

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