



# Operational Amplifiers

## LM709C operational amplifier

### general description

The LM709C is a monolithic operational amplifier intended for general-purpose applications. Operation is completely specified over the range of voltages commonly used for these devices. The design, in addition to providing high gain, minimizes both offset voltage and bias currents. Further, the class-B output stage gives a large output capability with minimum power drain.

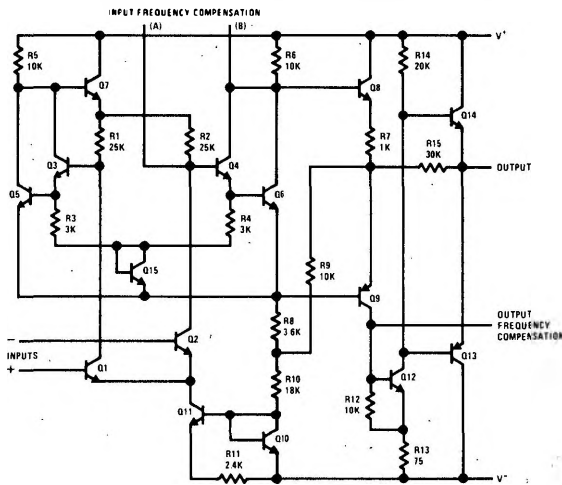
External components are used to frequency compensate the amplifier. Although the unity-gain compensation network specified will make the amplifier unconditionally stable in all feedback

configurations, compensation can be tailored to optimize high-frequency performance for any gain setting.

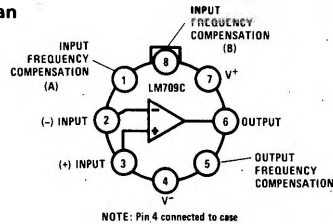
The fact that the amplifier is built on a single silicon chip provides low offset and temperature drift at minimum cost. It also ensures negligible drift due to temperature gradients in the vicinity of the amplifier.

The LM709C is commercial-industrial version of the LM709. It is identical to the LM709 except that it is specified for operation from 0°C to 70°C.

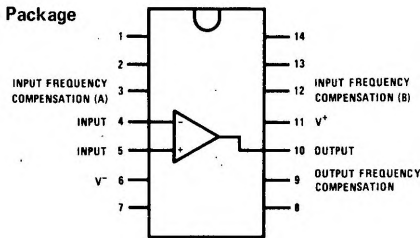
### schematic and connection diagrams



#### Metal Can

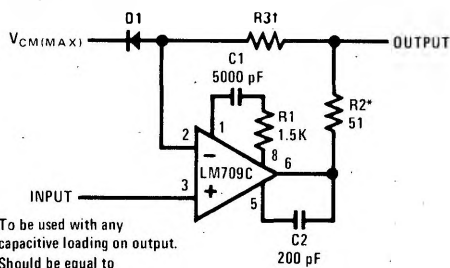


#### Dip Package



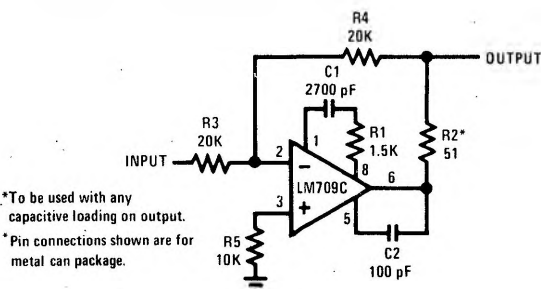
### typical applications\*\*

#### Voltage Follower



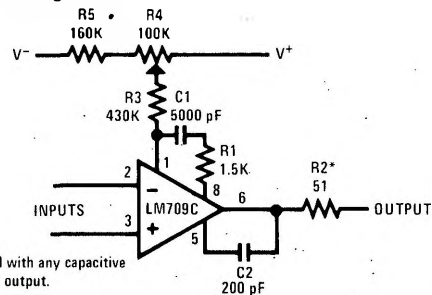
\*To be used with any capacitive loading on output.  
†Should be equal to dc source resistance on input.

#### Unity Gain Inverting Amplifier



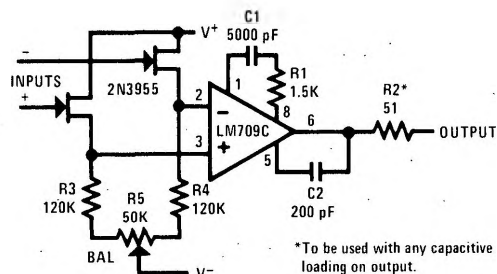
\*To be used with any capacitive loading on output.  
\* Pin connections shown are for metal can package.

#### Offset Balancing Circuit



\*To be used with any capacitive loading on output.

#### FET Operational Amplifier



\*To be used with any capacitive loading on output.

**absolute maximum ratings**

Supply Voltage	±18V
Power Dissipation (Note 1)	250 mW
Differential Input Voltage	±5V
Input Voltage	±10V
Output Short-Circuit Duration ( $T_A = 25^\circ\text{C}$ )	5 sec
Storage Temperature Range	-65°C to +150°C
Operating Temperature Range	0°C to +70°C
Lead Temperature (soldering, 60 sec)	300°C

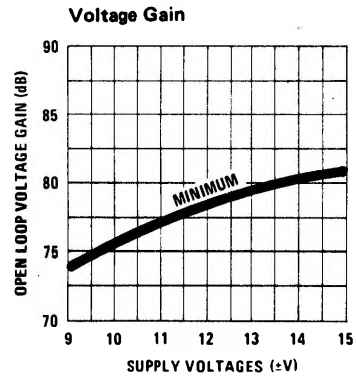
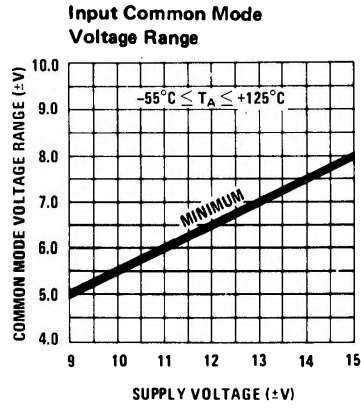
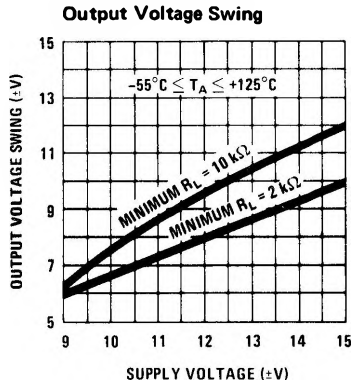
**electrical characteristics** (Note 2)

PARAMETER	CONDITION	MIN	TYP	MAX	UNITS
Input Offset Voltage	$T_A = 25^\circ\text{C}$ , $R_S \leq 10\text{ k}\Omega$		2.0	7.5	mV
Input Offset Current	$T_A = 25^\circ\text{C}$		100	500	nA
Input Bias Current	$T_A = 25^\circ\text{C}$		0.3	1.5	$\mu\text{A}$
Input Resistance	$T_A = 25^\circ\text{C}$	50	250		$\text{k}\Omega$
Output Resistance	$T_A = 25^\circ\text{C}$		150		$\Omega$
Supply Current	$T_A = 25^\circ\text{C}$ , $V_S = \pm 15\text{V}$		2.6	6.6	mA
Transient Response Risetime	$V_{IN} = 20\text{ mV}$ , $C_L \leq 100\text{ pF}$ $T_A = 25^\circ\text{C}$		0.3	1.0	$\mu\text{s}$
Overshoot			10	30	%
Slewing Rate	$T_A = 25^\circ\text{C}$		0.25		V/ $\mu\text{s}$
Input Offset Voltage	$R_S \leq 10\text{ k}\Omega$			10	mV
Average Temperature Coefficient of Input Offset Voltage	$R_S = 50\Omega$ $R_S = 10\text{ k}\Omega$		6.0 12		$\mu\text{V}/^\circ\text{C}$ $\mu\text{V}/^\circ\text{C}$
Large-Signal Voltage Gain	$V_S = \pm 15\text{V}$ , $R_L \geq 2\text{ k}\Omega$ $V_{OUT} = \pm 10\text{V}$	15,000	45,000		
Output Voltage Swing	$V_S = \pm 15\text{V}$ , $R_L = 10\text{ k}\Omega$ $V_S = \pm 15\text{V}$ , $R_L = 2\text{ k}\Omega$	±12 ±10	±14 ±13		V V
Input Voltage Range	$V_S = \pm 15\text{V}$	±8.0	±10		V
Common Mode Rejection Ratio	$R_S \leq 10\text{ k}\Omega$	65	90		dB
Supply Voltage Rejection Ratio	$R_S \leq 10\text{ k}\Omega$		25	200	$\mu\text{V}/\text{V}$
Input Offset Current	$T_A = +70^\circ\text{C}$ $T_A = 0^\circ\text{C}$		75 125	400 750	nA nA
Input Bias Current	$T_A = 0^\circ\text{C}$		0.36	2.0	$\mu\text{A}$

**Note 1:** For operating at elevated temperatures, the device must be derated based on a 100°C maximum junction temperature and a thermal resistance of 45°C/W junction to case or 150°C/W junction to ambient for the metal can package. For the flat package, the derating is based on a thermal resistance of 185°C/W when mounted on a 1/16-inch-thick, epoxy-glass board with ten, 0.03-inch-wide, 2-ounce copper conductors.

**Note 2:** These specifications apply for  $0^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$ ,  $\pm 9\text{V} \leq V_S \leq \pm 15\text{V}$ ,  $C_1 = 5000\text{ pF}$ ,  $R_1 = 1.5\text{K}$ ,  $C_2 = 200\text{ pF}$  and  $R_2 = 51\Omega$  unless otherwise specified.

guaranteed performance characteristics



typical performance characteristics

