LM384 5W Audio Power Amplifier

Check for Samples: LM384

FEATURES

- **Wide supply voltage range: 12V to 26V**
- **Low quiescent power drain**
- **Voltage gain fixed at 50**
- **High peak current capability: 1.3A**
- **Input referenced to GND**
- **High input impedance: 150kΩ**
- **Low distortion: 0.25% (P_O=4W, R_L=8Ω)**
- **Quiescent output voltage is at one half of the supply voltage**
- **Standard dual-in-line package**

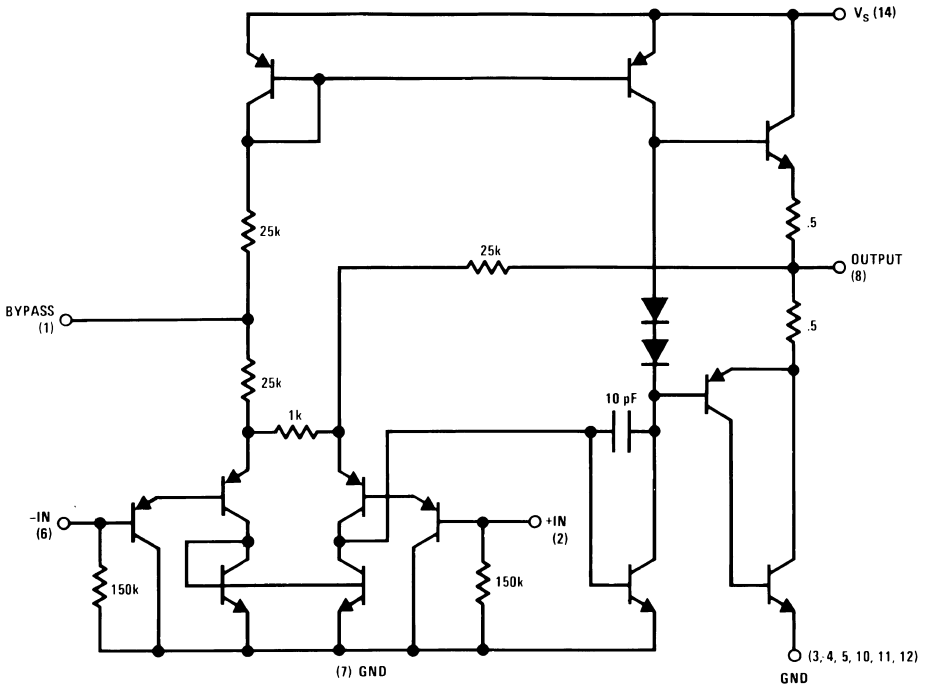
DESCRIPTION

The LM384 is a power audio amplifier for consumer applications. In order to hold system cost to a minimum, gain is internally fixed at 34 dB. A unique input stage allows ground referenced input signals. The output automatically self-centers to one-half the supply voltage.

The output is short-circuit proof with internal thermal limiting. The package outline is standard dual-in-line. A copper lead frame is used with the center three pins on either side comprising a heat sink. This makes the device easy to use in standard p-c layout.

Uses include simple phonograph amplifiers, intercoms, line drivers, teaching machine outputs, alarms, ultrasonic drivers, TV sound systems, AM-FM radio, sound projector systems, etc. See AN-69 for circuit details.

Schematic Diagram



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

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Absolute Maximum Ratings (1)

Supply Voltage	28V
Peak Current	1.3A
Power Dissipation (See (2) (3))	1.67W
Input Voltage	±0.5V
Storage Temperature	−65°C to +150°C
Operating Temperature	0°C to +70°C
Lead Temperature (Soldering, 10 sec.)	260°C
Thermal Resistance	
θ_{JC}	30°C/W
θ_{JA}	79°C/W

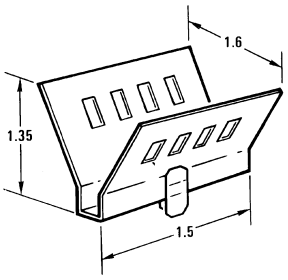
(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits.
(2) The maximum junction temperature of the LM384 is 150°C.
(3) The package is to be derated at 15°C/W junction to heat sink pins.

Electrical Characteristics (1)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Z_{IN}	Input Resistance			150		kΩ
I_{BIAS}	Bias Current	Inputs Floating		100		nA
A_V	Gain		40	50	60	V/V
P_{OUT}	Output Power	THD = 10%, R_L = 8Ω	5	5.5		W
I_Q	Quiescent Supply Current			8.5	25	mA
$V_{OUT\ Q}$	Quiescent Output Voltage			11		V
BW	Bandwidth	P_{OUT} = 2W, R_L = 8Ω		450		kHz
V^+	Supply Voltage		12		26	V
I_{SC}	Short Circuit Current (2)			1.3		A
$PSRR_{RTO}$	Power Supply Rejection Ratio			31		dB
	(3))					
THD	Total Harmonic Distortion	P_{OUT} = 4W, R_L = 8Ω		0.25	1.0	%

(1) V^+ = 22V and T_A = 25°C operating with a Staver V7 heat sink for 30 seconds.
(2) Output is fully protected against a shorted speaker condition at all voltages up to 22V.
(3) Rejection ratio referred to the output with C_{BYPASS} = 5 µF, freq = 120 Hz.

Heat Sink Dimensions

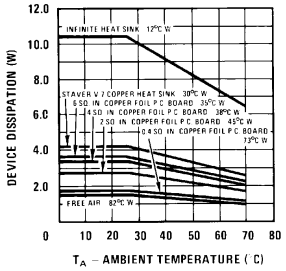


Staver Company
41 Saxon Ave.
P.O. Drawer H
Bay Shore, N.Y.
Tel: (516) 666-8000

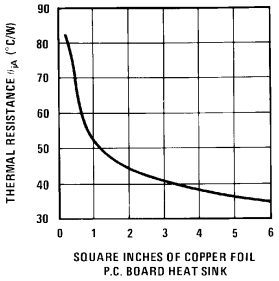
Figure 1. Staver “V7” Heat Sink

Typical Performance Characteristics

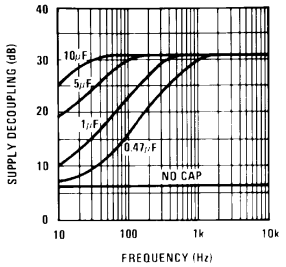
Device Dissipation vs Ambient Temperature



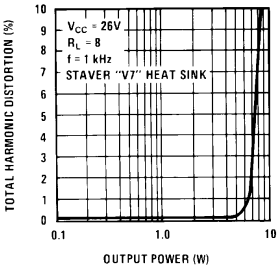
Thermal Resistance vs Square Inches



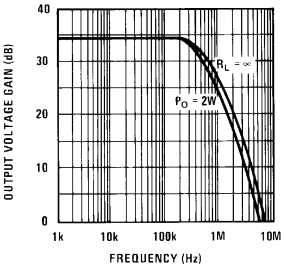
Supply Decoupling vs Frequency



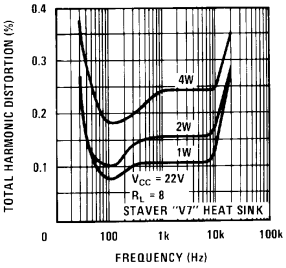
Total Harmonic Distortion vs Output Power



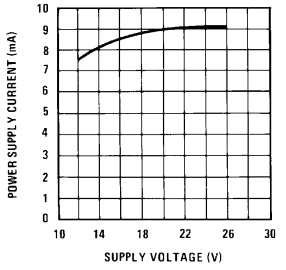
Output Voltage Gain vs Frequency



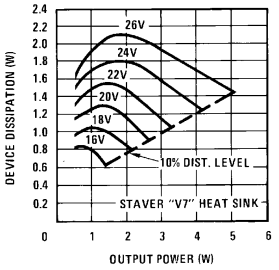
Total Harmonic Distortion vs Frequency



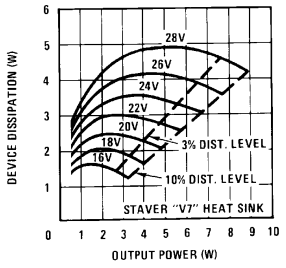
Power Supply Current vs Supply Voltage



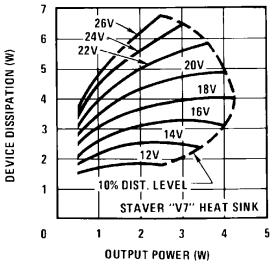
Device Dissipation vs Output Power—16Ω Load



Device Dissipation vs Output Power—8Ω Load



Device Dissipation vs Output Power—4Ω Load



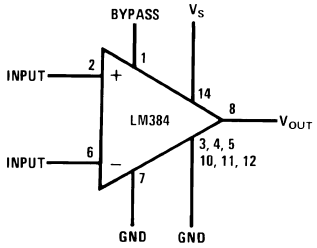


Figure 2. Block Diagram

Heatsink Pins

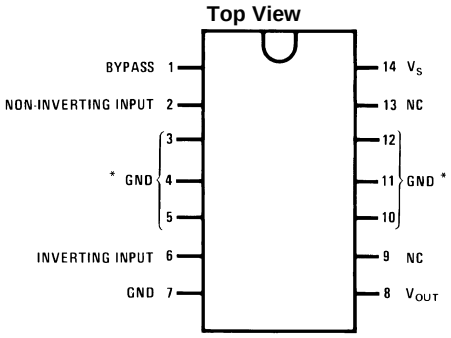


Figure 3. Dual-In-Line Package

Typical Applications

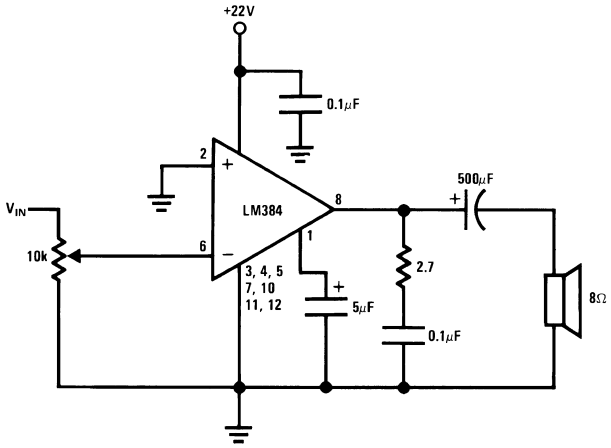


Figure 4. Typical 5W Amplifier

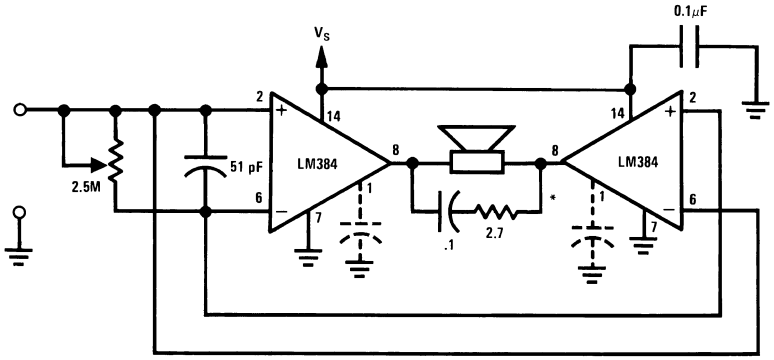
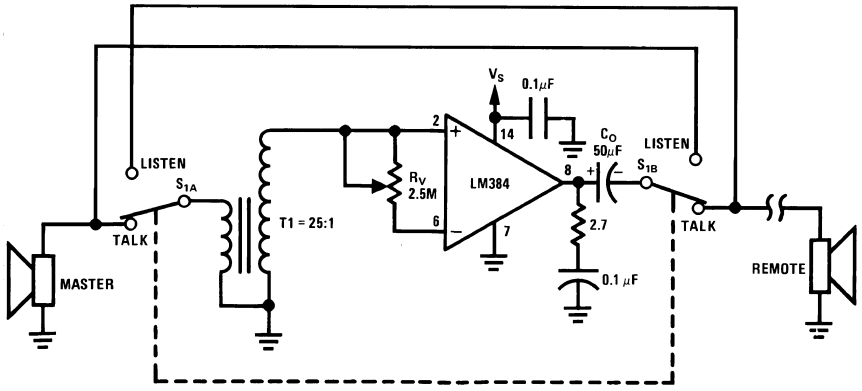


Figure 5. Bridge Amplifier



*For stability with high current loads

Figure 6. Intercom

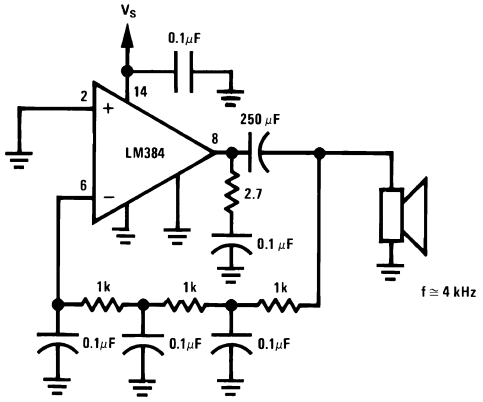


Figure 7. Phase Shift Oscillator

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
LM384N	ACTIVE	PDIP	NFF	14	25	TBD	SNPB	Level-1-NA-UNLIM	0 to 70	LM384N	Samples
LM384N/NOPB	ACTIVE	PDIP	NFF	14	25	Pb-Free (RoHS Exempt)	SN	Level-1-NA-UNLIM	0 to 70	LM384N	Samples

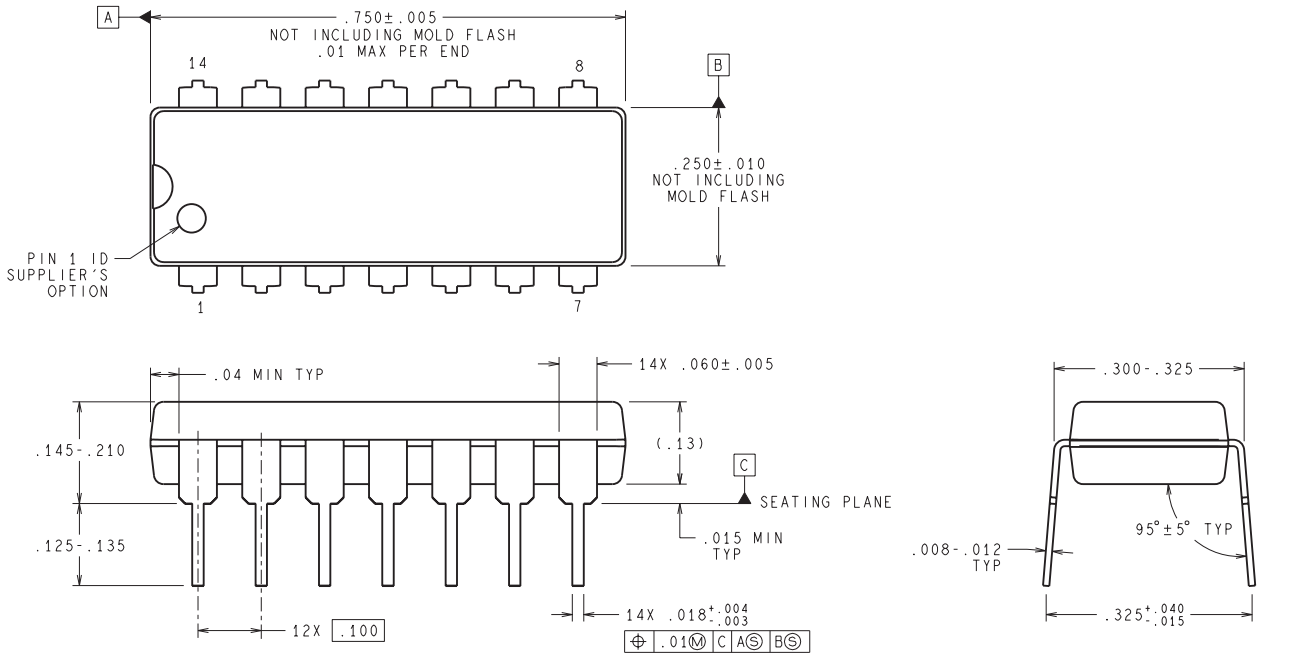
(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.
Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) Only one of markings shown within the brackets will appear on the physical device.

NFF0014A



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