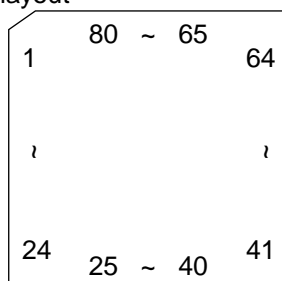


■ LC72366-9989 (IC801) : Main micon

1. Pin layout



2. Pin function

Pin No.	Symbol	Function	Pin No.	Symbol	Function
1	XIN	4.5MHz crystal oscillation	41	CD ON	CD 8V supply on
2	TEST2	Connect to ground	42	RELAY	5V power control
3	JBUS SI	J-BUS signal data input (to 74HC126)	43	BBE CTL	No use
4	JBUS SO	J-BUS signal data output (to 74HC126)	44	BEEP	No use
5	JBUS SCK	J-BUS output clock signal (to 74HC126)	45	SW4	
6	JBUS I/O CONT	J-BUS I/O control	46	SW1	CD mechanism switch 1 for disc in, 8cm disc
7	SUBQ	CD LSI subcode data input	47	SW3	CD mecha. switch 3 for disc present, loading end
8		No use	48	REST	CD pickup rest position
9	SQCK	CD LSI subcode clock	49	JOG0	Rotary encoder input 0
10	RESET	Micon reset pin	50	JOG1	Rotary encoder input 1
11	LCD SI	No use	51	CD SENSE	CD LSI sense signal
12	LCD SO	LCD driver data output	52	STATUS	CD LSI status signal
13	LCDSCK	LCD driver clock signal	53	PSAVE2	Power save 2 : +B detect
14	LCD CE	LCD driver chip enable port	54	SD/ST	Station detection, Stereo indication
15	FM ILLUMI	No use	55	REMOCON	Remocon input
16	AM ILLUMI	No use	56	JBUS INT	J-BUS interrupt
17	CD ILLUMI	No use	57	BAND	FM/AM band selection
18	DIMMER OUT	No use	58	MONO	FM mono selection
19	LM0	CD loading motor output	59	IFRQ/AGC	During FM auto search, IF request output H after SD detected. During AM auto search, AGC output.
20	LM1	CD loading motor output			
21		No use	60	MUTE	Muting switch
22		No use	61	LEVEL	Level meter input
23		No use	62	S METER	S meter input
24	KS2	No use	63	KEY CHANGE	Connect to ground
25	KS1	No use	64	KEY2	Key 2 data input (AD)
26	KS0	Output for initial setting diode matrix	65	KEY1	Key 1 data input (AD)
27	DETACH	Front panel detect	66	KEY0	Key 0 data input (AD)
28	K2	No use	67	PSAVE1	Power save 1, ACC power detect
29	K1	Input for initial setting diode matrix	68	SENSE	Voltage sense
30	K0	Input for initial setting diode matrix	69		No use
31	Vdd	5V power supply	70	FM IF COUNT	FM IF counter data input
32	SW2	CD mechanism switch 2 for 12cm disc	71		No use
33	CD LSI RESET	CD LSI reset	72		No use
34	MCLK	CD LSI command clock signal	73	Vdd	5V power supply
35	MDATA	CD LSI command data output	76	AM OSC	No use
36	MLD	CD LSI command load signal	75	FM OSC	FM input frequency
37		No use	76	Vss	Ground
38		No use	77		No use
39	SCL	E.volume clock output	78	EO	PLL error output signal
40	SDA	E.volume data output	79	TEST1	Connect to ground
			80	XOUT	4.5MHz crystal oscillation