Ordering number : ENA2014

SANYO Semiconductors
DATA SHEET

An ON Semiconductor Company

Monolithic Digital IC

LB1973JA — Two-channel H-Bridge Driver

Overview

The LB1973JA is a two-channel H-bridge driver that supports for low saturation draive operation. It is optimal for H-bridge drive of stepping motors (AF and zoom) in portable equipment such as camera cell phones.

Features

- Two-channel H-bridge driver
- 2ch simultaneous connection is possible
- Parallel input interface
- 2 phase excitation, 1-2 phase excitation drive are possible
- The range of the operation voltage is wide.(1.8V to 7.5V)
- Built-in thermal protection

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V_{CC} max		-0.3 to +8.0	V
Output voltage	V_{OUT} max		- V_{SF} to V_{CC}+V_{SF}	V
Input voltage	V_{IN} max		-0.3 to +8.0	V
Spark killer Di order direction electric	I_{SF} max		1000	mA
Ground pin source current	I_{GND}	Per channel	1000	mA
Allowable power dissipation	Pd max	*Mounted on a bord	800	mW
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

* Mounted on a Specified board : 114.3mm×76.1mm×1.6mm, glass epoxy

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

Allowable Operating Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V_{CC}		1.8 to 7.5	V
High-level input voltage	V_{IH}	R_{IN} = 1kΩ	1.3 to 7.5	V
Low-level input voltage	V_{IL}	R_{IN} = 1kΩ	-0.3 to +0.5	V

Electrical Characteristics at Ta = 25°C, V_{CC} = 1.9V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Source current	$I_{CCO}1$	V_{CC} = 1.9V,IN1 to IN4 = Low level		0.01	1	μA
	$I_{CCO}2$	V_{CC} = 3V,IN1 to IN4 = Low level		0.01	1	μA
	$I_{CC}1$	IN1 = High level,IN2 to IN4 = Low level		18	25	mA
	$I_{CC}2$	IN1 = High level,IN2 to IN4 = Low level, V_{CC} = 3V		19	27.5	mA
Output saturation voltage1 (single connection)	$V_{OUT}11$	I_{OUT} = 270mA,V_{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V_{OUT} = Upper Tr and Under Tr IN1 = High level, IN2 to IN4 = Low level Supplementation: Standard similar as for IN2 to IN4 = High level		0.2	0.3	V
	$V_{OUT}12$	I_{OUT} = 350mA,V_{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V_{OUT} = Upper Tr and Under Tr IN1 = High level, IN2 to IN4 = Low level Supplementation: Standard similar as for IN2 to IN4 = High level		0.25	0.4	V
Output saturation voltage2 (parallel connection)	$V_{OUT}21$	I_{OUT} = 270mA,V_{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V_{OUT} = Upper Tr and Under Tr OUT1-3,OUT2-4 short. IN1 and IN3 = High level, IN2 and IN4 = Low level Supplementation: Standard similar as for IN2 and IN4 = High level		0.12	0.2	V
	$V_{OUT}22$	I_{OUT} = 500mA,V_{CC} = 1.9V to 3.6V, Ta = -20 to 85°C V_{OUT} = Upper Tr and Under Tr OUT1-3,OUT2-4 short. IN1 and IN3 = High level,IN2 and IN4 = Low level Supplementation: Standard similar as for IN2 and IN4 = High level		0.2	0.35	V
Output electric current with the parasitic element	I_{PA}	V_{IN} = 1.9 to 3.6V, Ta = -20 to 85°C *1			9	mA
Input current	I_{IN}	V_{IN} = 1.9V		32	70	μA
Themal shutdown operation temperature	Ttsd	*2: Design guarantee		140		°C
Temperature hysteresis width	ΔT	*2: Design guarantee		20		°C
Spark killer Diode						
Reverse current	I_S(leak)	V_{CC}-OUT = 8V, V_{IN} = Low level			10	μA
Forword voltage	V_{SF}	I_{SF} = 400mA, V_{IN} = Low level			1.7	V

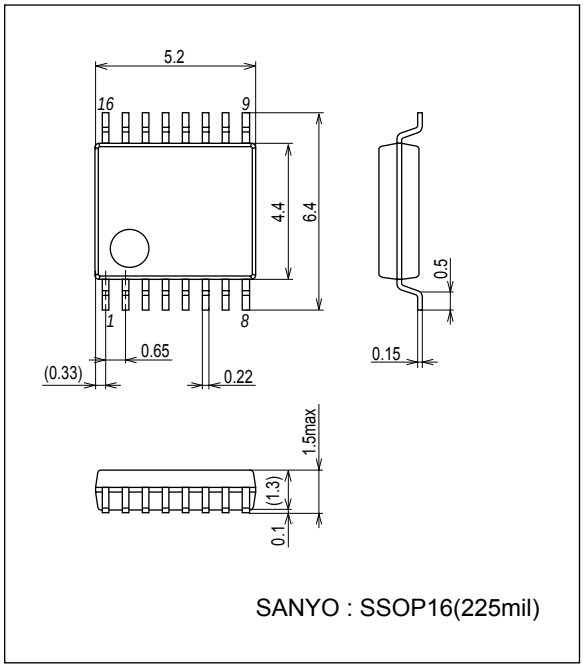
*1: Output electric current with the parasitic element_I_{PA}: The current value that the off ch(-free) output is pulled at the time of one side ch drive by a parasitic element

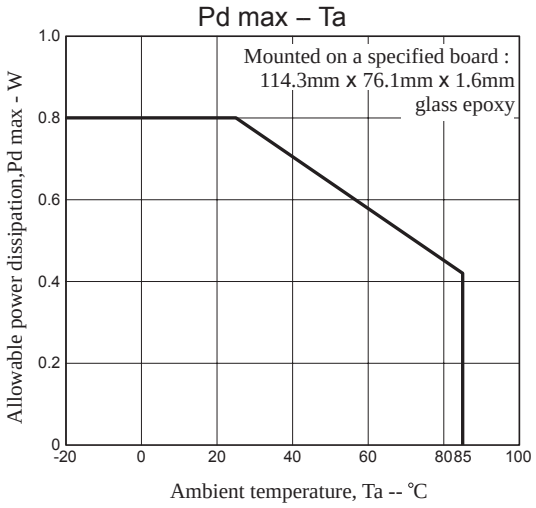
*2: Design guarantee value and does not measure

* VSF: The current order direction voltage true in a time

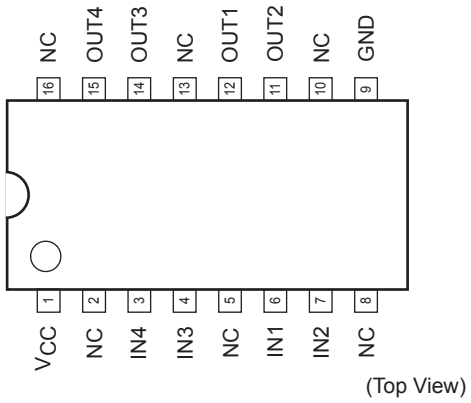
Package Dimensions

unit : mm (typ)
3178B





Pin Assignment

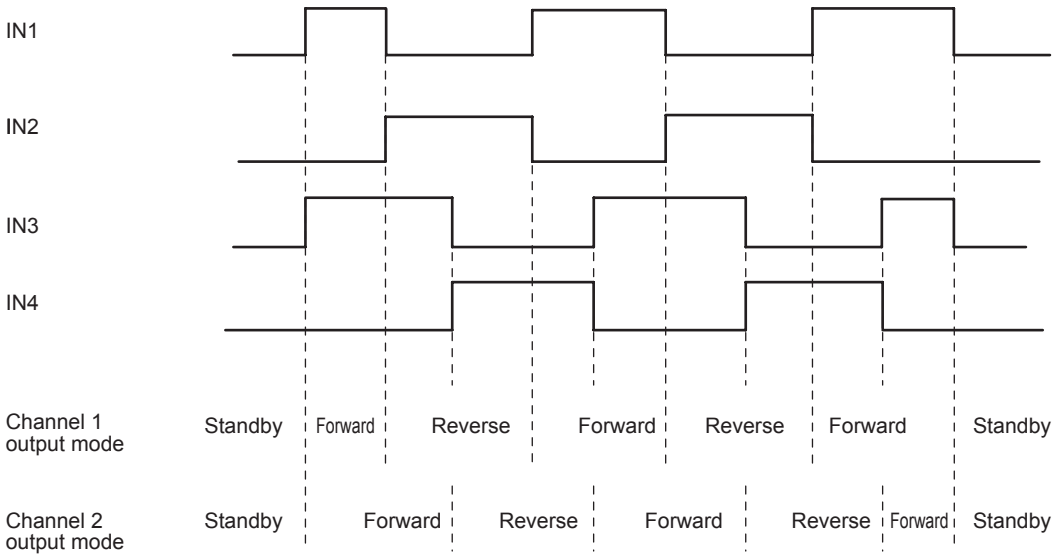


Truth Table

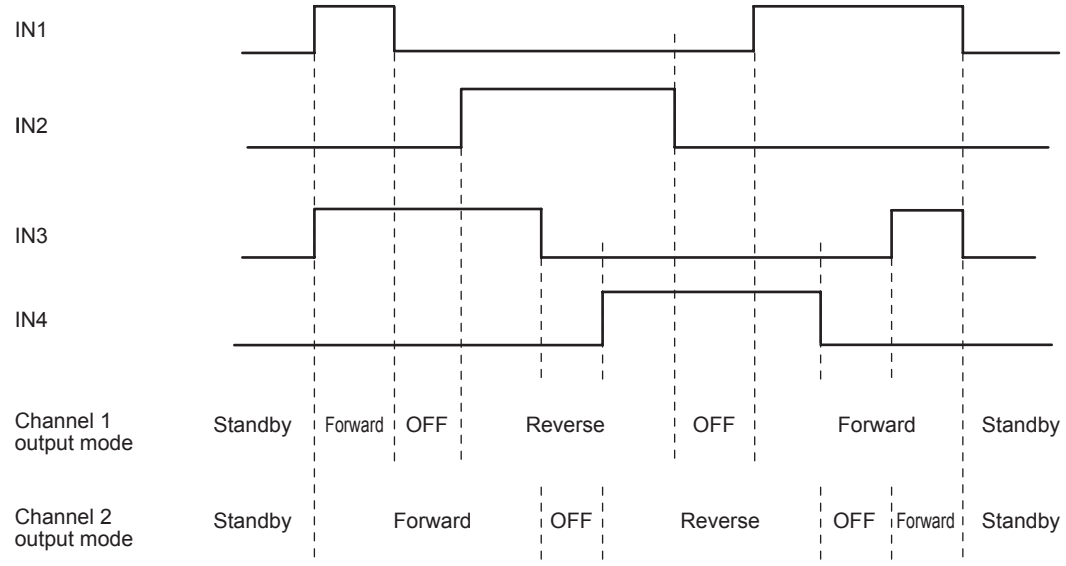
Input				Output				Mode
IN1	IN2	IN3	IN4	OUT1	OUT2	OUT3	OUT4	
Low	Low	Low	Low	Off	Off	Off	Off	Standby mode
High	Low	-	-	High	Low	-	-	Channel 1, forward
Low	High			Low	High			Channel 1, reverse
-	-	High	Low	-	-	High	Low	Channel 2, forward
		Low	High			Low	High	Channel 2, reverse
High	High	-	-	The logic output for the first high-level input is produced.				
-	-	High	High					

Stepping motor control example

(1) Timing chart for 2-phase drive



(2) Timing chart for 1-2 phase drive



Block Diagram