National Semiconductor

DS26LV31
3V Enhanced CMOS Quad Differential Line Driver

General Description

The DS26LV31 is a high-speed quad differential CMOS driver that is compatible with both TIA/EIA-422-B and CCITT V.11. The CMOS DS26LV31 features low I_{CC} of XX mA MAX which makes it ideal for battery powered and power conscious applications.
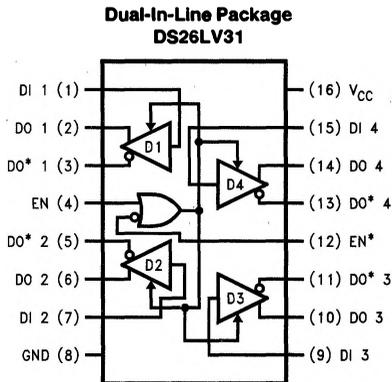
The TRI-STATE® enables, EN and $\overline{EN}$, allow the device to be active High or active Low. The enables are common to all four drivers. Protection diodes protect all the driver inputs against electrostatic discharge. The driver inputs (DI) are compatible with TTL and CMOS levels.

Features

- Low power design
  - $I_{CC} \leq 500\ \mu A$ max
- Compatible with TIA/EIA-422-B (RS-422) and CCITT V.11 recommendation
- Guaranteed AC parameter:
  - Maximum driver skew   TBD
  - Transition time   TBD
- Pin compatible with DS26C31
- Available in SOIC packaging

Connection Diagram

Dual-In-Line Package
DS26LV31

TL/F/12642–1

Order Number DS26LV31M or DS26LV31N
See NS Package Number M16A or N16A

Truth Table

Enables		Input	Outputs	
EN	$\overline{EN}$	DI	DO	$\overline{DO}$
L	H	X	Z	Z
H	L	H	H	L
H	H			
L	L	L	L	H

L = Low logic state   X = Irrelevant
H = High logic state   Z = TRI-STATE (high impedance)