BIPOLAR RAM (32x8) 8X353

FEATURES

- **32 bytes of storage**
- **Dedicated port address**
- **Fast select feature for use with extended microcode**
- **On chip address decoding**
- **Separate address input pins**
- **Single 5 volt power supply**
- **0.3 inch, slim line package**

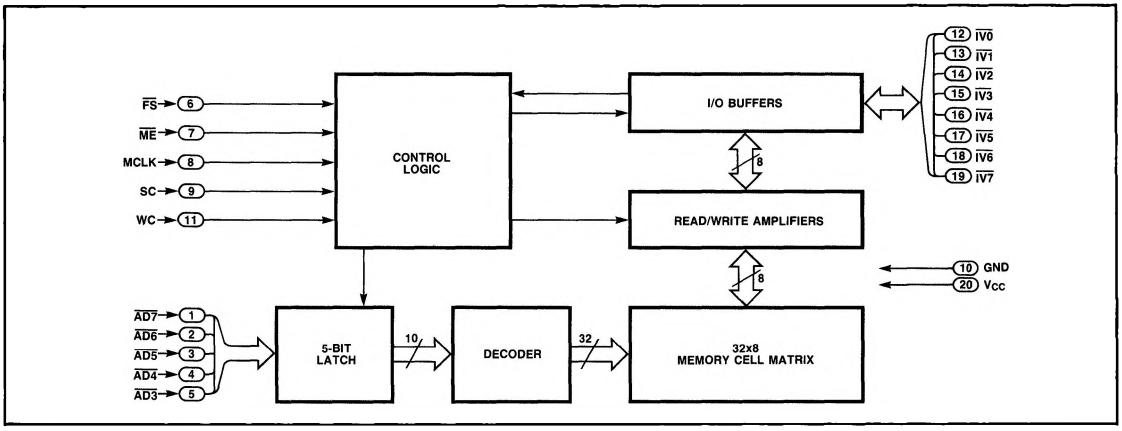
PRODUCT DESCRIPTION

The 8X353 is a 32-byte RAM designed principally as a working storage element in 8X305-based systems. The 8X353 is ideal for applications requiring a relatively small amount of data storage and maximum I/O flexibility. Since the 8X353 takes up 32 of 256 locations on a controller bank, this device allows single-cycle, bank to bank data transfer and other implementations in which the user does not wish to dedicate an entire I/O bank to data storage. Contributing to the versatility of the 8X353 is a fast select feature which allows the chip to be selected externally of the $\overline{IV}$ bus. A block diagram and summary of operation is shown below.

ORDERING INFORMATION

Contact Local Sales Representative

BLOCK DIAGRAM



SUMMARY OF OPERATION

INPUT						RESULTING OUTPUT				
$\overline{ME}$	SC	WC	MCLK	$\overline{FS}$	SELECT LATCH	$\overline{IV}$ BUS	ADDRESS BUS	DATA	ADDRESS LATCH	SELECT LATCH
H	X	X	X	H	X	Ignore	Ignore	Keep	Keep	Keep
H	X	X	H	L	X	Ignore	Input	Keep	Update	Keep
H	X	X	L	L	X	Ignore	Ignore	Keep	Keep	Keep
L	L	L	L	L	X	Output	Ignore	Keep	Keep	Keep
L	L	L	L	H	L	Ignore	Ignore	Keep	Keep	Keep
L	L	L	L	H	H	Output	Ignore	Keep	Keep	Keep
L	X	L	H	L	X	Ignore	Input	Keep	Update	Keep
L	L	H	L	X	X	Ignore	Ignore	Keep	Keep	Keep
L	L	H	H	L	X	Input	Ignore	Update	Keep	Keep
L	L	H	H	H	L	Ignore	Ignore	Keep	Keep	Keep
L	L	H	H	H	H	Input	Ignore	Update	Keep	Keep
L	H	L	L	X	X	Ignore	Ignore	Keep	Keep	Keep
L	H	L	H	X	X	Input (Address)	Input	Keep	Update	Update*
X	H	H	X	X	X	Not Defined				

Notes:
Depending on IV bus data X = don't care